



Cornell Laboratory for
Accelerator-based Sciences and
Education (CLASSE)



CVD Coated Copper Substrate SRF Cavity research at Cornell University

M. Ge, T. Gruber, J. J. Kaufman, M. Liepe, J. T. Maniscalco,
T. Oseroff, R. D. Porter, Z. Sun (Cornell University);
V. Arrieta, S. R. McNeal (Ultramet).

SRF2019 conference in Dresden, Germany
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Outline:

- Introduction of Chemical Vapor Deposition (CVD)
- Nb-Cu Cavities
 - Cavity preparation
 - Cavity test results and analysis
- Nb-Cu Sample study
- Nb₃Sn-Cu plates
- Conclusion



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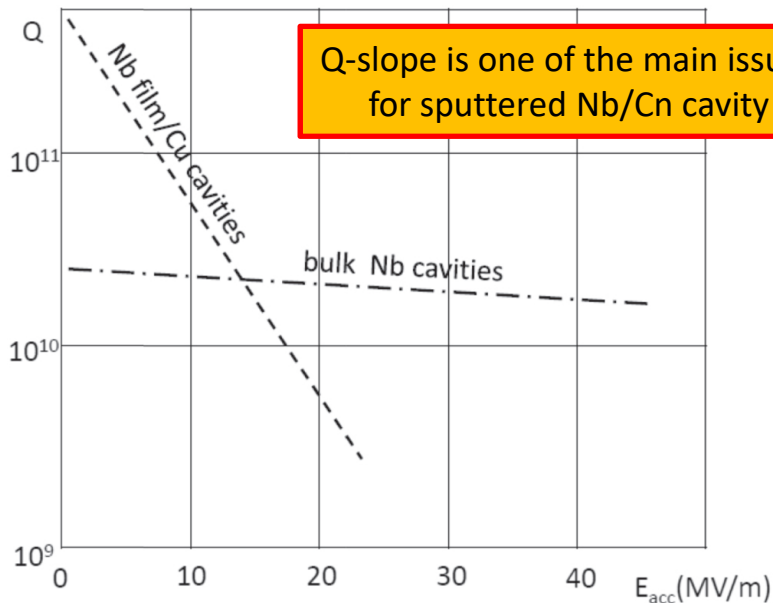


- **Lower cost**
 - High RRR Nb \approx 10x price of Cu
 - Can avoid expensive electron beam welding
- **Increased thermal stability**
 - Nb: 75 W/(m·K) \rightarrow Cu: 300-2000 W/(m·K)
- Can **avoid inclusions** caused by machining
- Can coat Nb-layer on **complex shaped** parts.

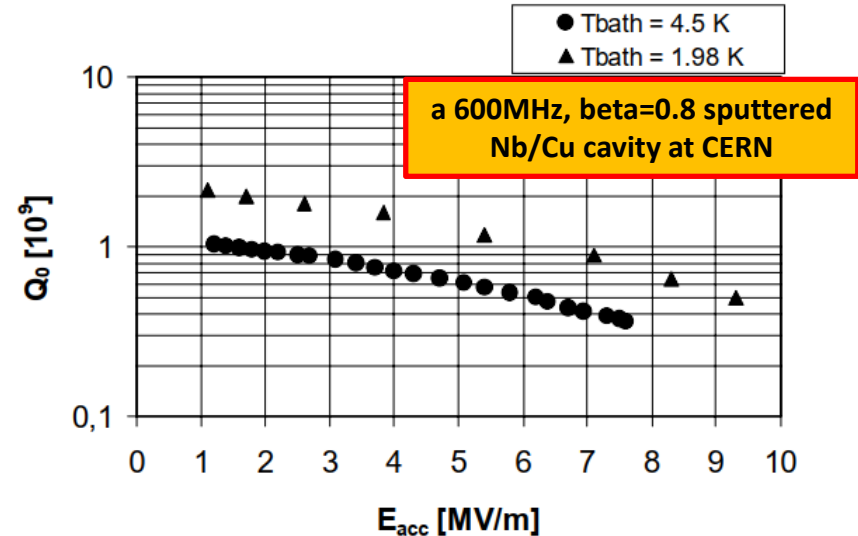
Why Do We Need CVD?

Advantages of CVD (as an alternative way of coating Nb-layer):

- Forms a metallurgical diffusion bond
 - Strong enough withstand High Pressure Rinses (HPR) and tumbling;
 - Creates good thermal contact;
- High deposition rate
 - 300 $\mu\text{m}/\text{hour}$;
 - Can make both thick and thin films of high-pure Nb;
 - Potentially CVD cavity is capable of doping/infusion.



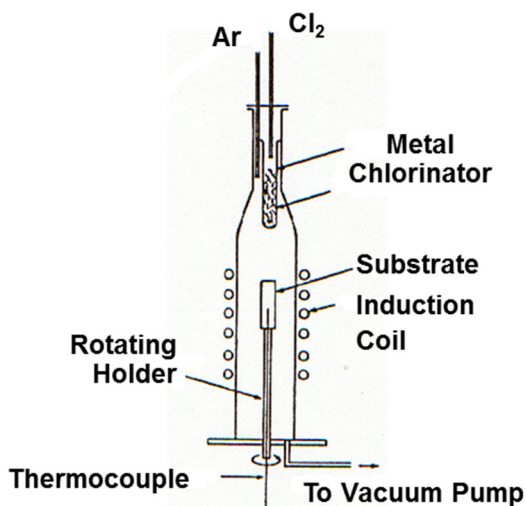
Q-slope is one of the main issues for sputtered Nb/Cu cavity



a 600MHz, beta=0.8 sputtered Nb/Cu cavity at CERN

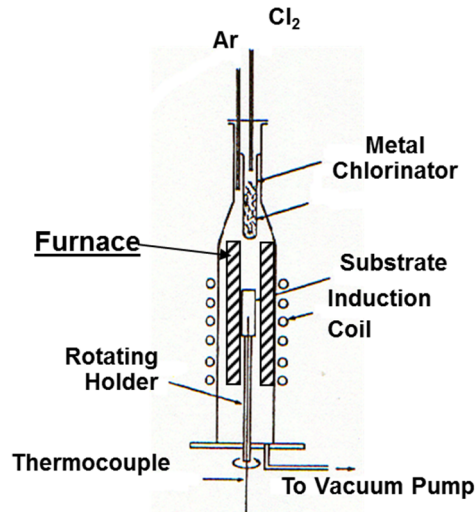
Introduction of CVD

- CVD is a vacuum deposition method;
- A substrate is heated up and exposed to precursors;
- Reaction/deposition takes place on the substrate surface and leaves thin-film coating on it.



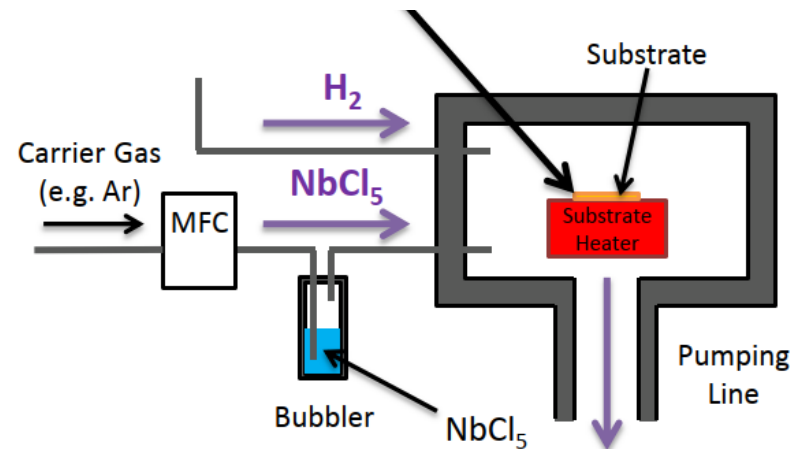
Cold-Wall Process

- **Cold-Wall Process:** Selective heating and preferential deposition;
- **Hot-Wall Process:** Isothermal heating and uniform temperature control.



Hot-Wall Process

- **Common method for coating Nb:**
 - 1) NbCl₅ is vaporized;
 - 2) Hydrogen is added;
 - 3) The substrate is heated up to a temperature (e.g. 700 °C);
 - 4) Deposition takes place on the substrate;
 - 5) Resultant gasses pumped away.



Reactor diagram showing use of NbCl₅ to produce CVD

niobium
P. Pizzol et al., "CVD Deposition of Nb Based Materials
for SRF Cavities," in Proc. of IPAC 2016



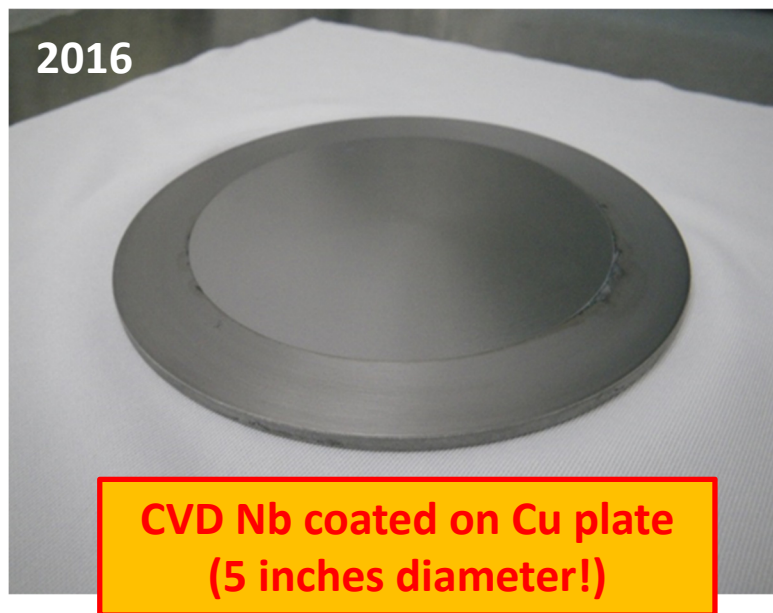
Technical Challenges of CVD for SRF

- 1) High Pure Nb;
- 2) Thick and strong Nb layer;
- 3) Coating Nb layer on large samples;
- 4) Coating Nb layer on a full-size cavity;
- 5) Low residual resistance (R_0);
- 6) High accelerating gradient (E_{acc}).
- 7) No severe Q-slope;

**Achievements in early CVD works
done with Ultramet:**



RRR measurement system at Cornell





2017 DOE SBIR Phase-II:

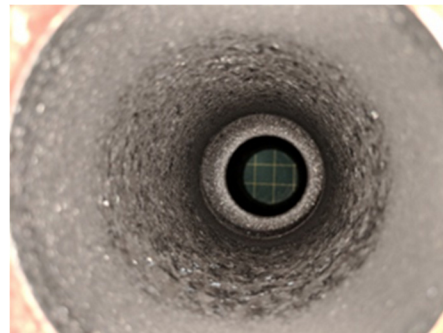
“Fabrication and Testing of Thick Film CVD Niobium-Lined Copper SRF Cavities for High Gradient Applications”

The CVD niobium is bonded at the nuclear level to the copper cavity substrate resulting in excellent adhesion characteristics:

CVD processing at
Ultramet



Half 1.3GHz single-cell cavity

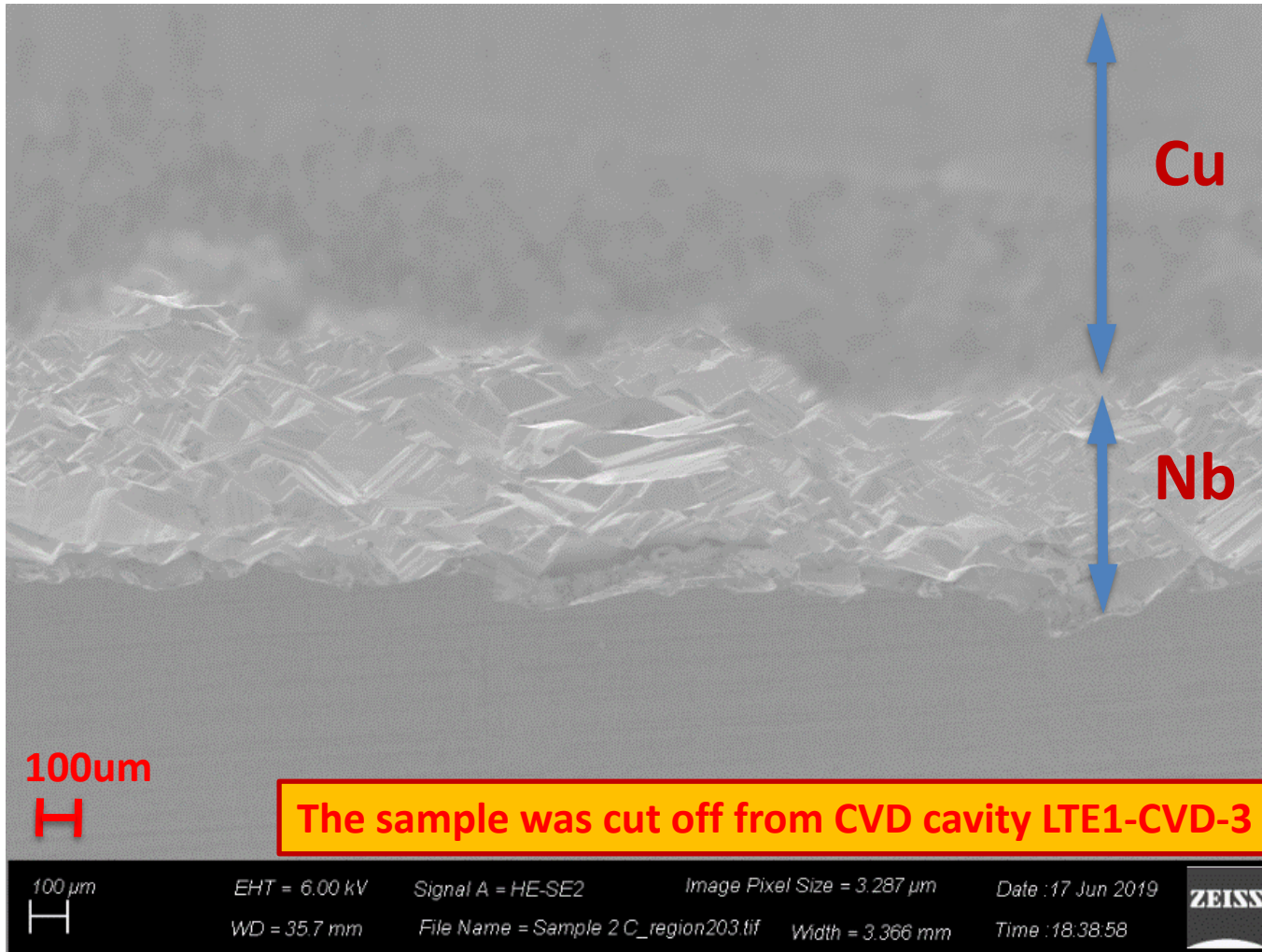


1.3GHz single-cell CVD
Nb-Cu cavity





Cross-section SEM of the CVD layer



- Average thickness of Nb layer > 200 um.
- Plenty room for surface treatment, e.g. EP, tumbling, etc.
- Capable of introducing impurity on surface, e.g. Doping/infusion.



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LTE1-CVD-2:

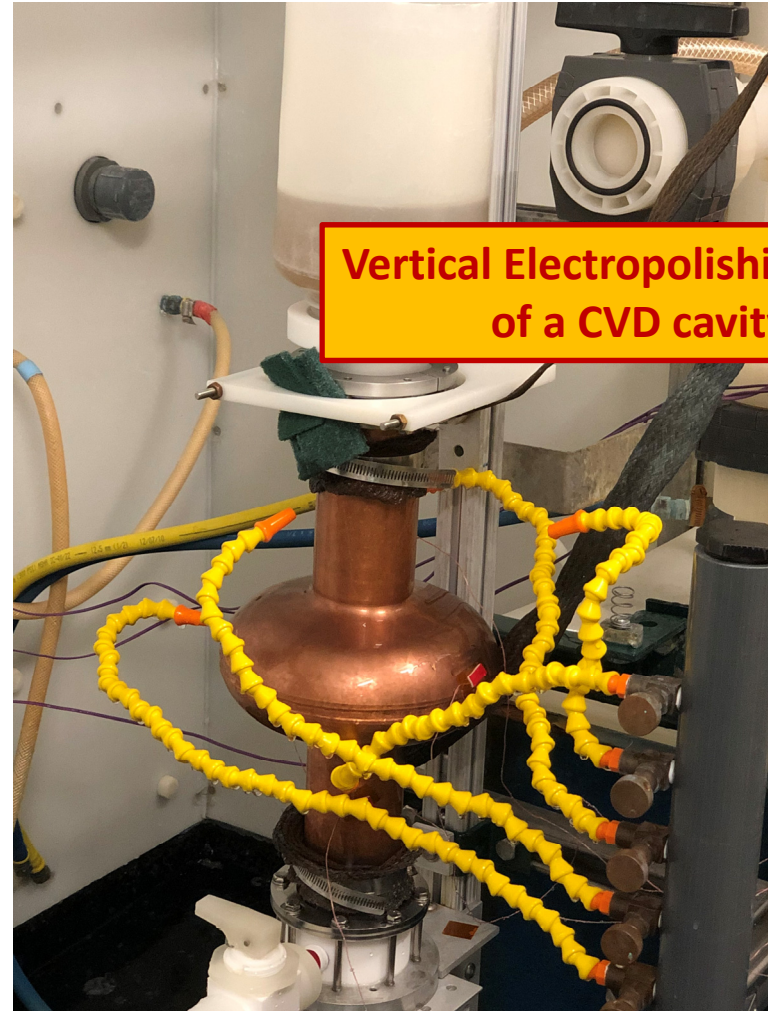
- **10 um VEP**
- VT: cooldown1 + cooldown2

LTE1-CVD-3:

- VT1: had cold leak;
- VT2: re-tighten flanges;
- VT3: **5 um VEP** ;
- VT4: re-HPR and re-test;
- VT5: dressed with T-map

LTE1-CVD-4:

- **Tumbling** and 10um EP



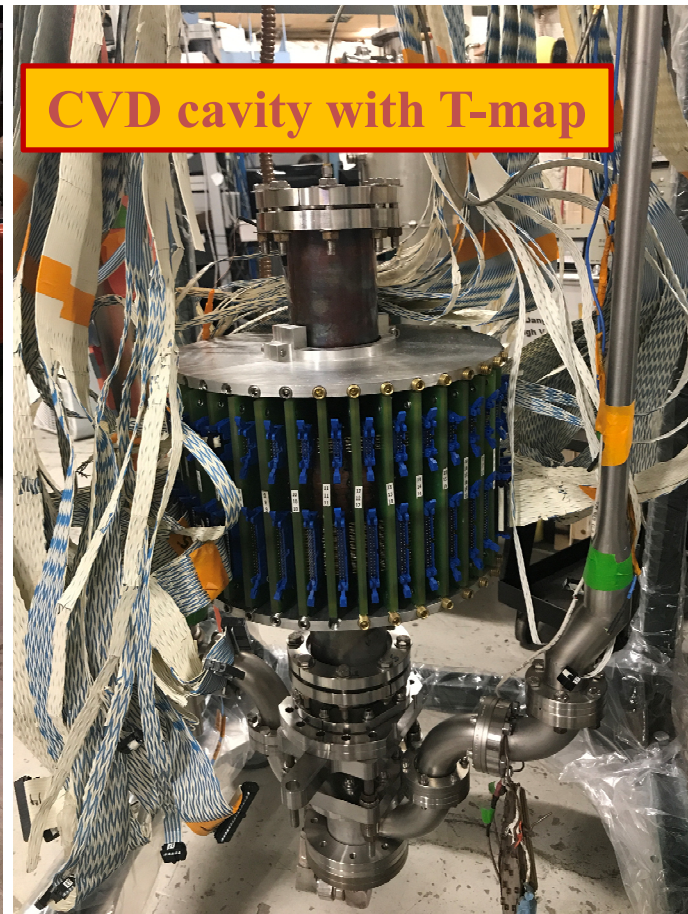
Vertical Electropolishing (VEP)
of a CVD cavity



Vertical Test Set-up



CVD cavity with T-map

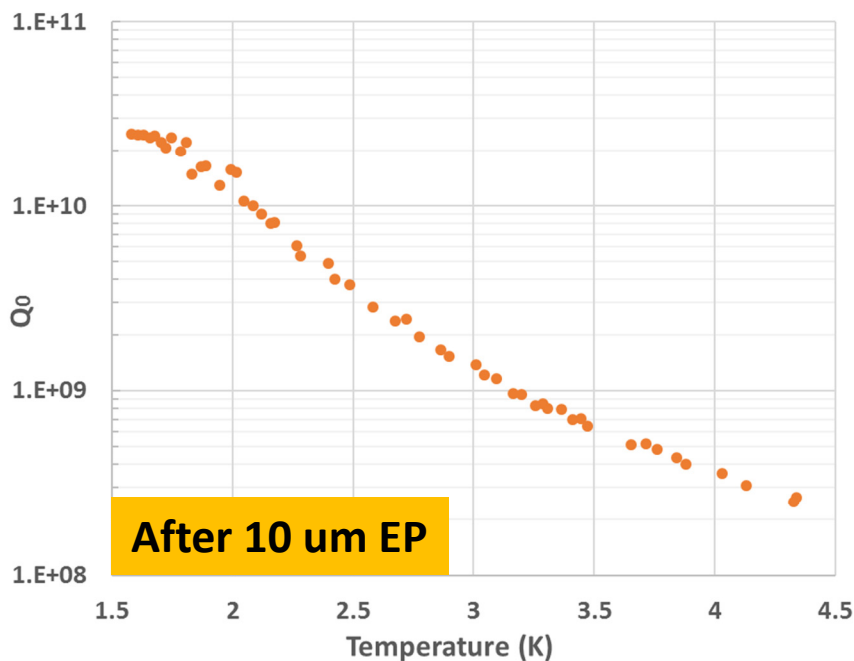


Instrumentation for Vertical Tests:

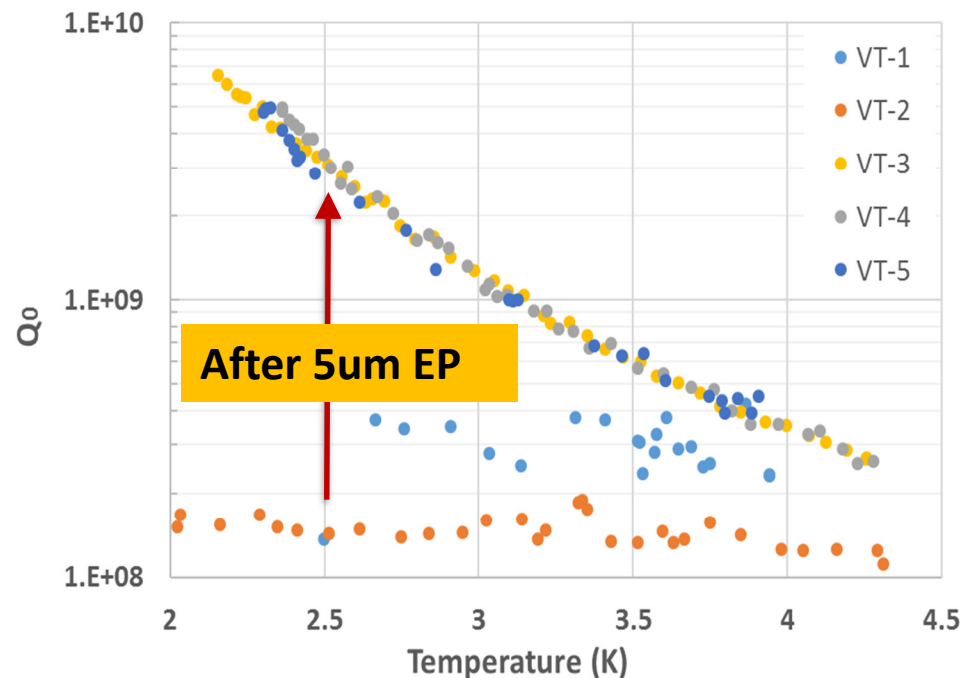
- Helmholtz coil,
- Fluxgate sensors,
- Slow-cool stinger,
- Variable input coupler.
- Cernox sensors,



Q₀ vs. Temperature



LTE1-CVD-2

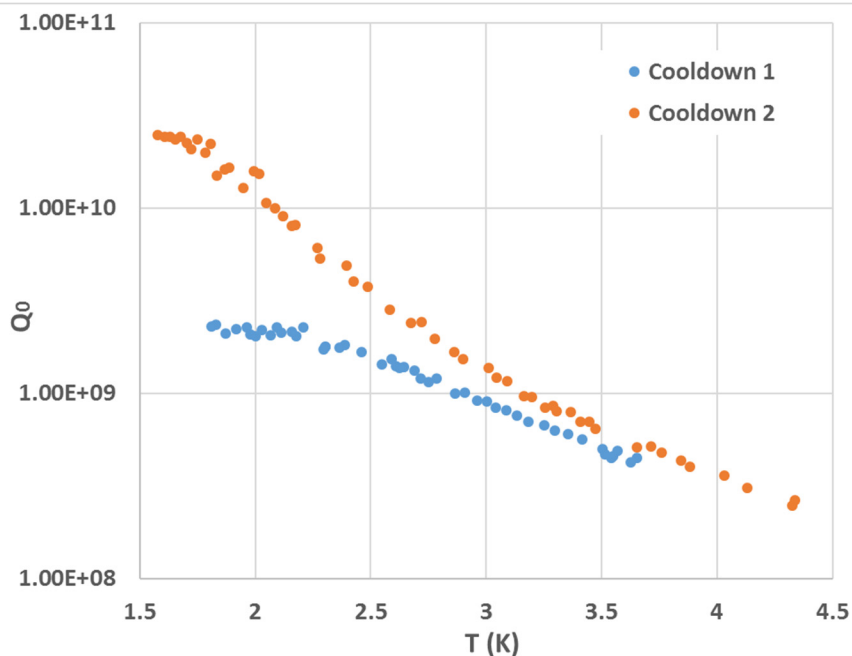


LTE1-CVD-3

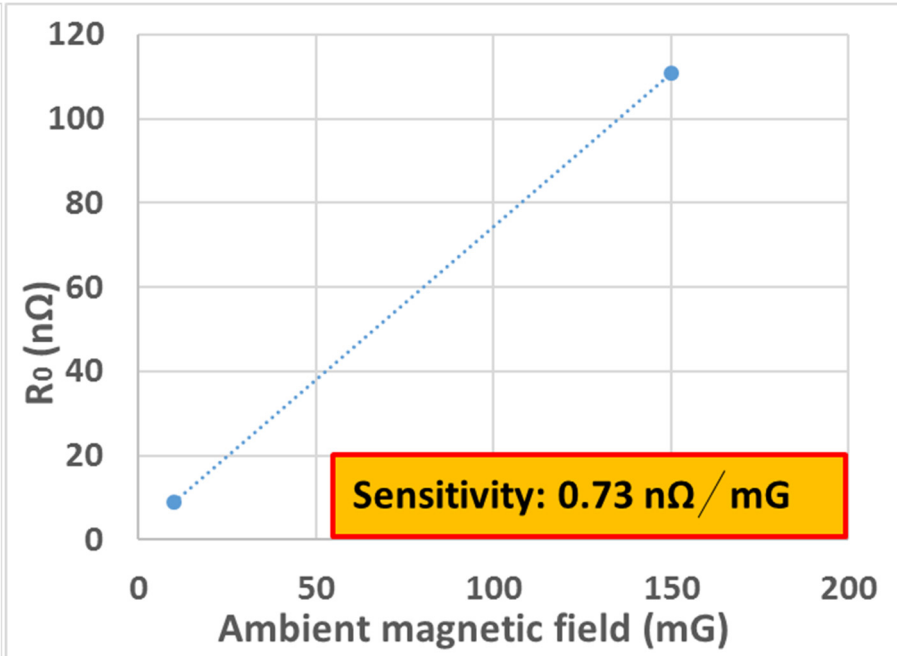
- Q₀ achieved **>1e10 at 2K** after light VEP, closed to a heavy EP'd 1.3GHz bulk-Nb SRF-cavity;
- **R₀ < 10 nΩ** for both cavity after light VEP;
- Before VEP, cavity Q₀ was dominated by R₀(~1000n Ω), but the cavity was still superconducting.



- Cavity preparation: CVD + 10 μm VEP

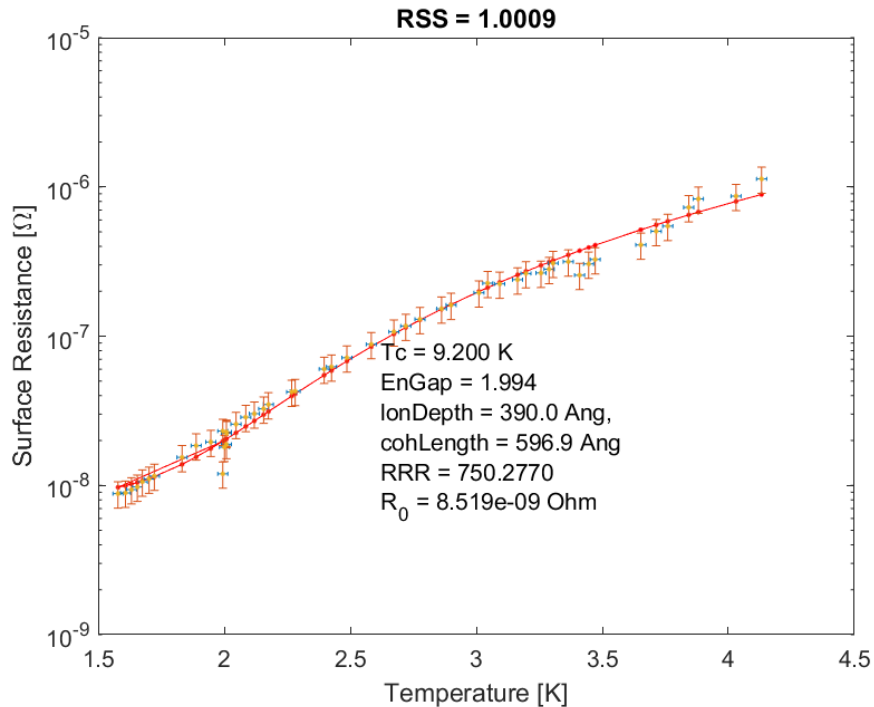


Q_0 vs temperature curve

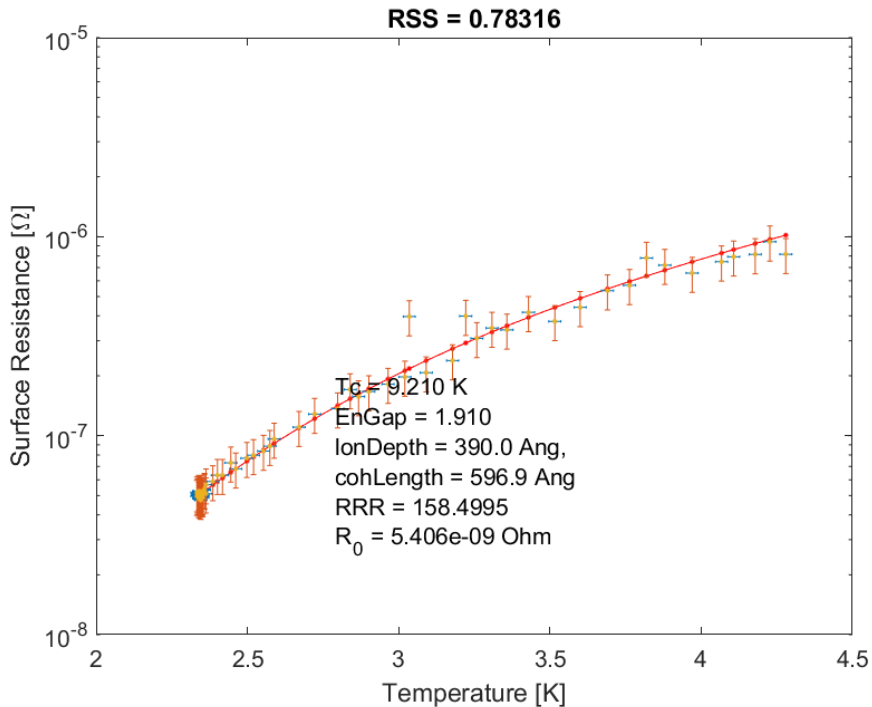


R_0 vs ambient magnetic field curve

- Cooldown 1: with 150 mGauss ambient magnetic field;
- Cooldown 2: with <10 mGauss ambient magnetic field;
- **The sensitivity is very closed to a EP'd bulk Nb cavity.**



LTE1-CVD-2 (VT2)



LTE1-CVD-3 (VT4)

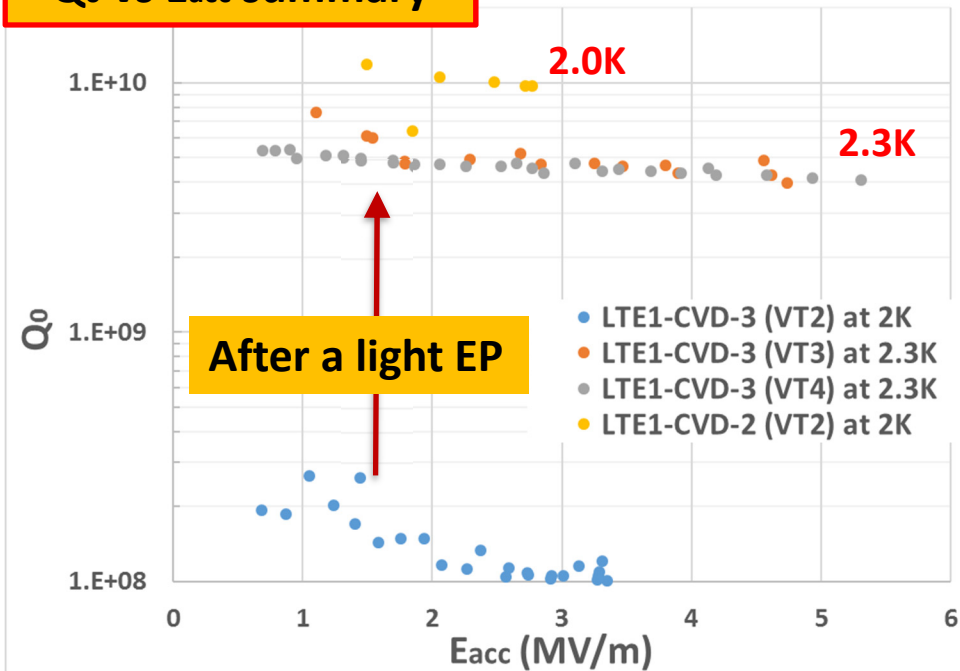
	Mean free path (nm)	Tc (K)	Energy Gap	R ₀ (nΩ)
LTE1-CVD-2 (VT2):	4500	9.20	1.994	8.5
LTE1-CVD-3 (VT4):	948	9.21	1.910	5.4

- **Larger MFP indicates clean and pure Nb.**

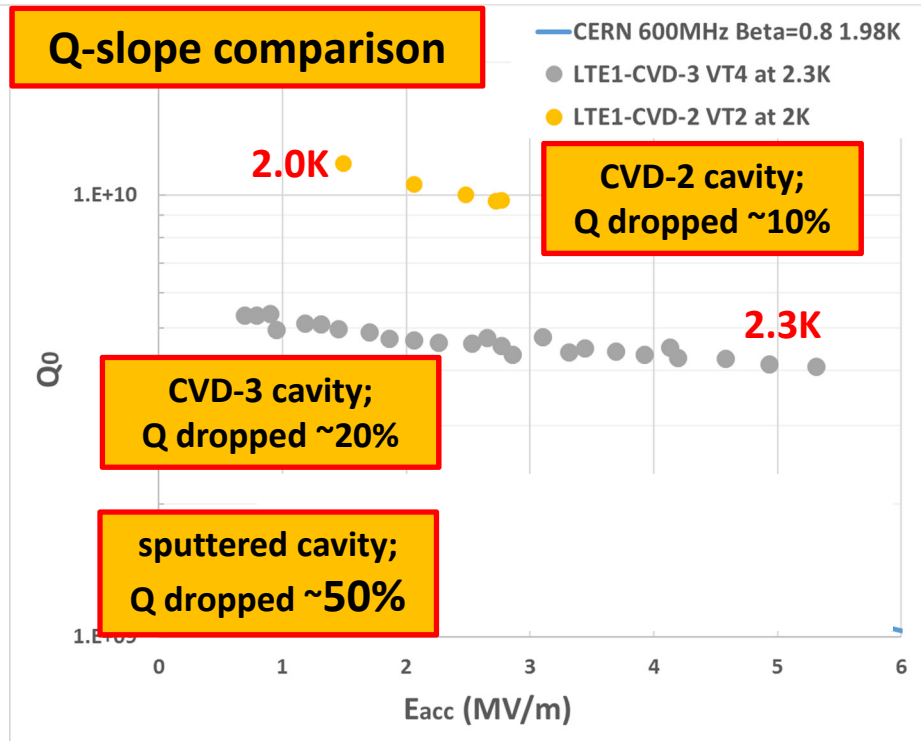


Q₀ vs. E_{acc} & Q-Slope Comparison

Q₀ vs E_{acc} summary



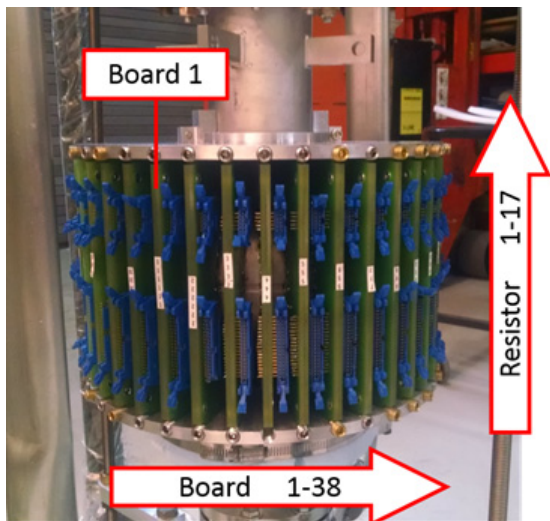
Q-slope comparison



- Q₀ vs. E_{acc} curve is flat, **no severe Q-slope observed**;
- Quench field is low (~5 MV/m);

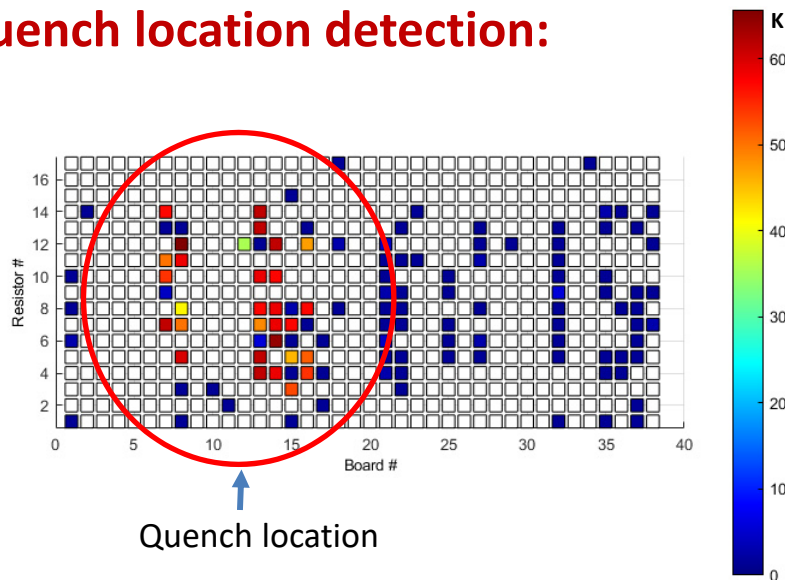


Temperature Mapping Results

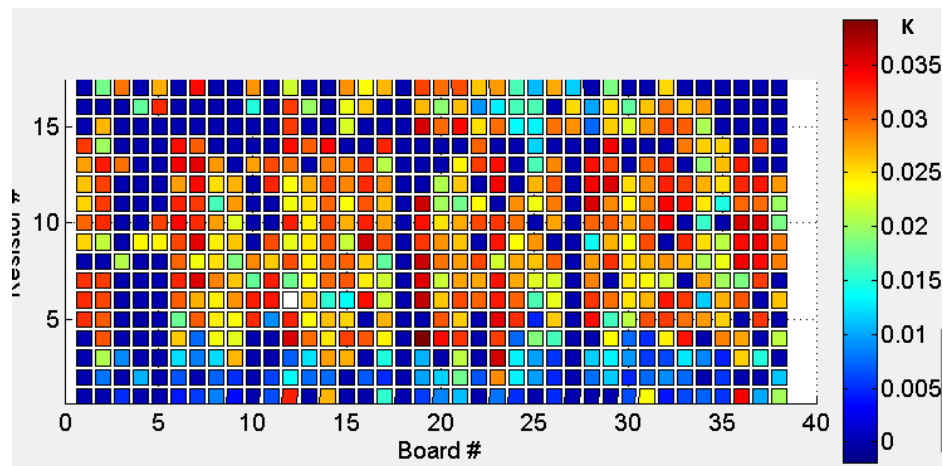


T-map set up for the test

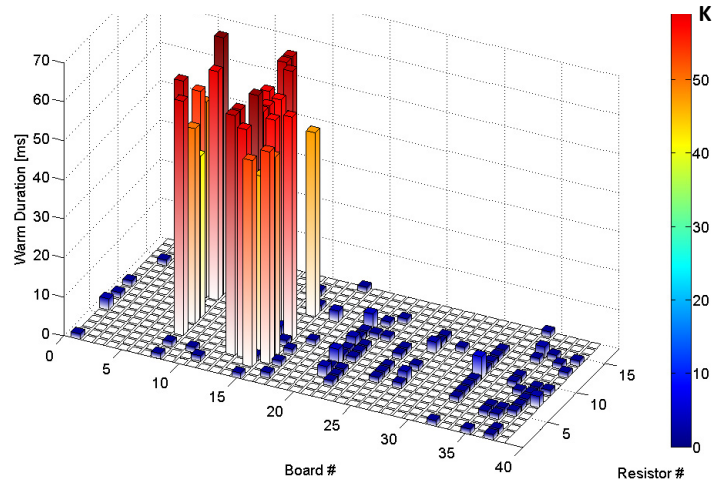
Quench location detection:



Quench location



LTE1-CVD-3 T-map results at $E_{acc} = 3.28\text{MV/m}$

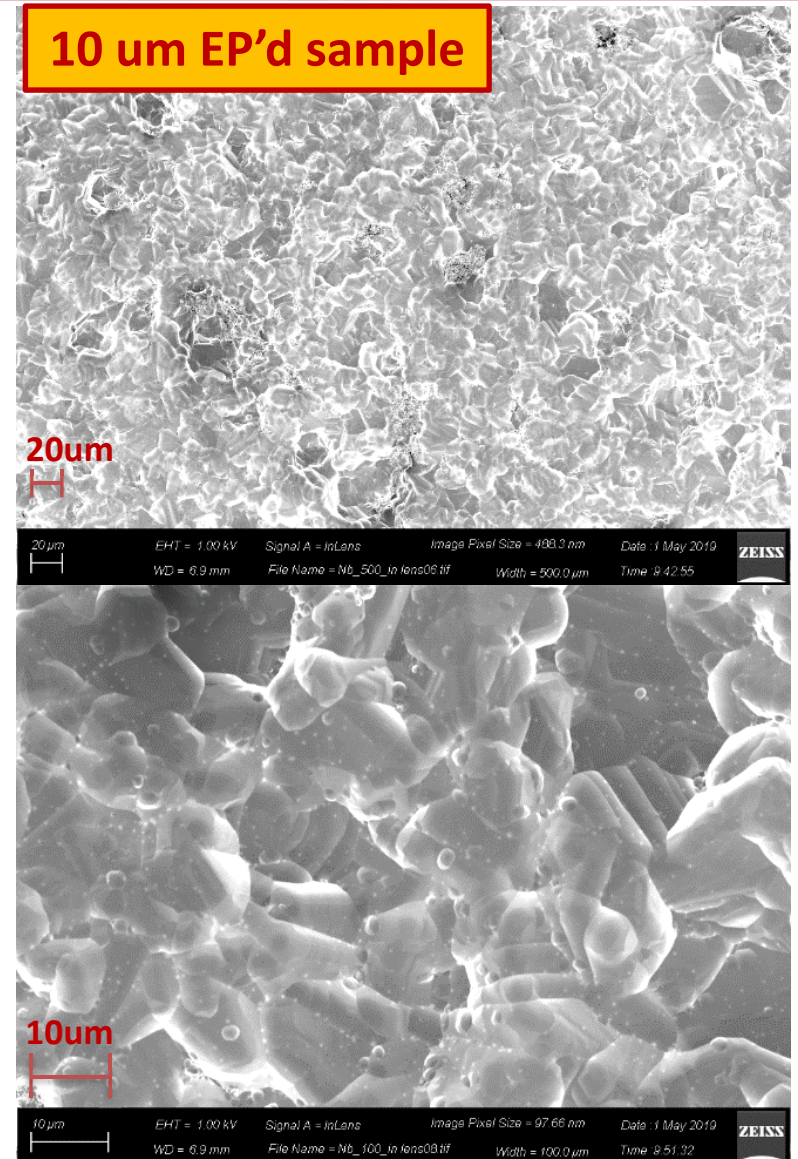
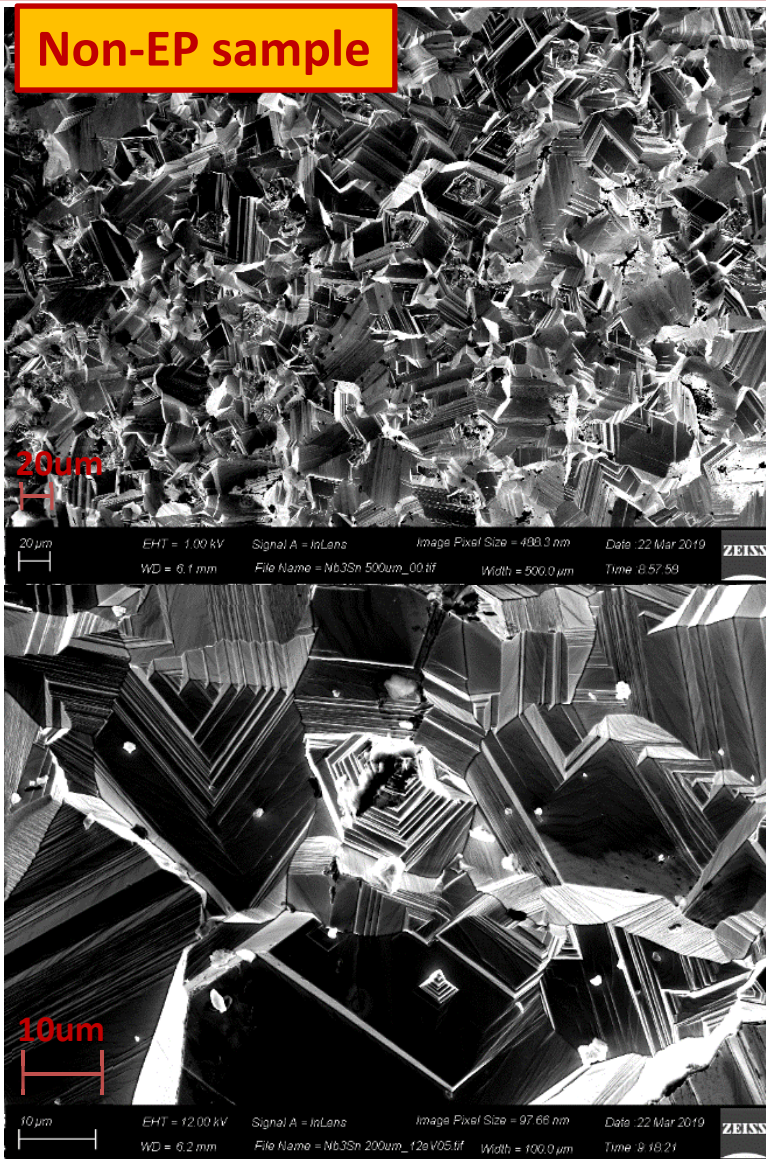




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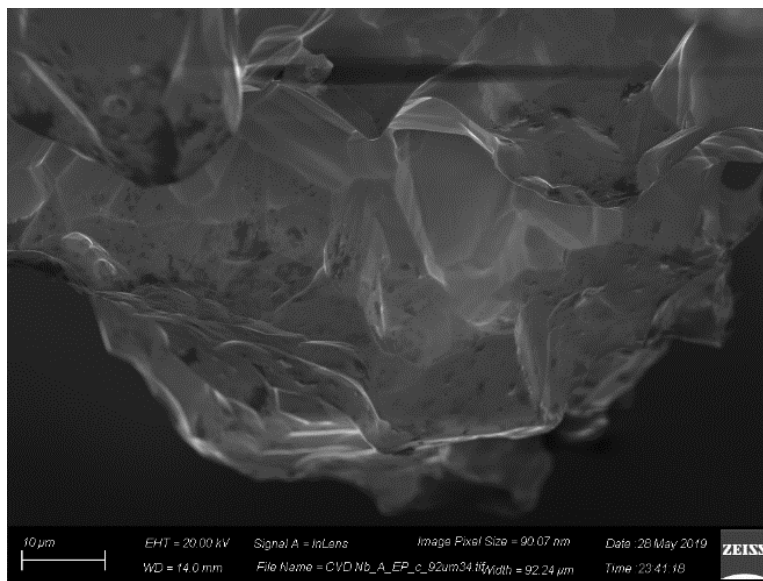
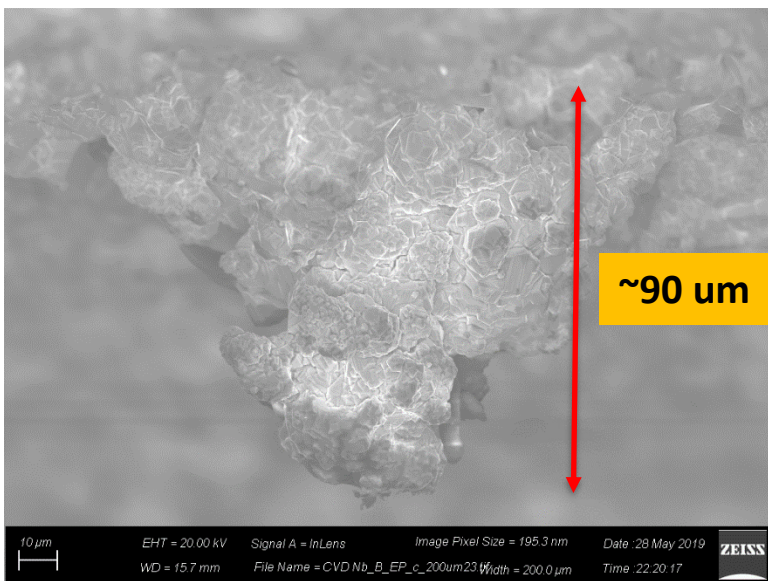
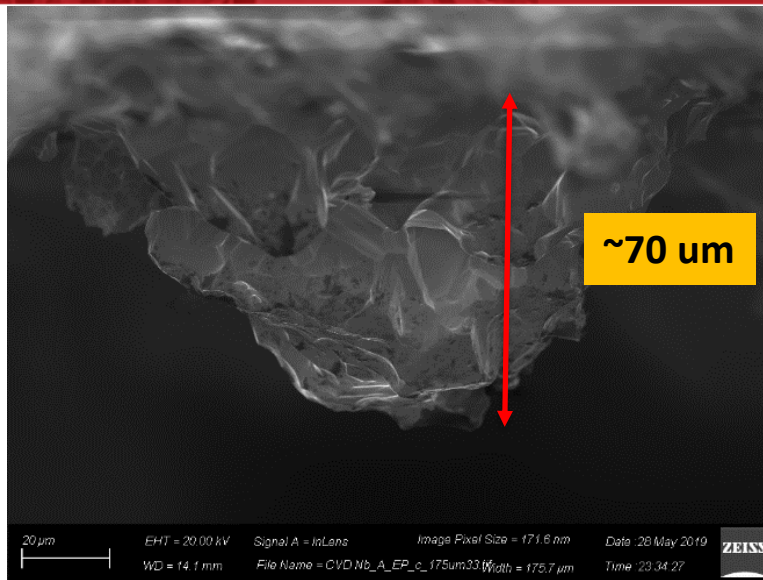
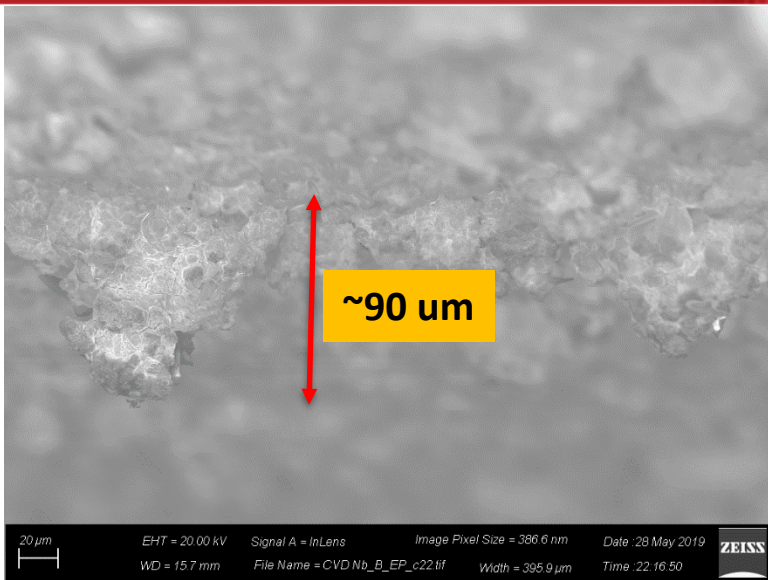
SEM Scan Comparison



- Round edge after EP.



Cross-Section SEM Comparison



Non-EP sample

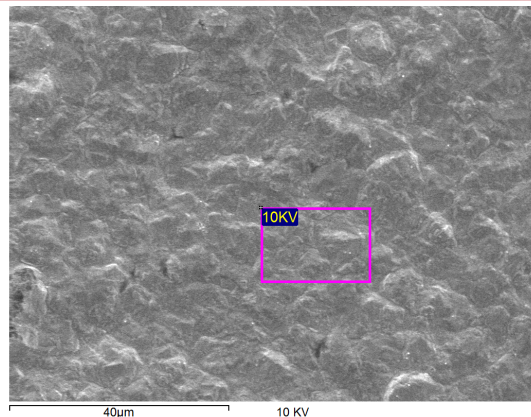
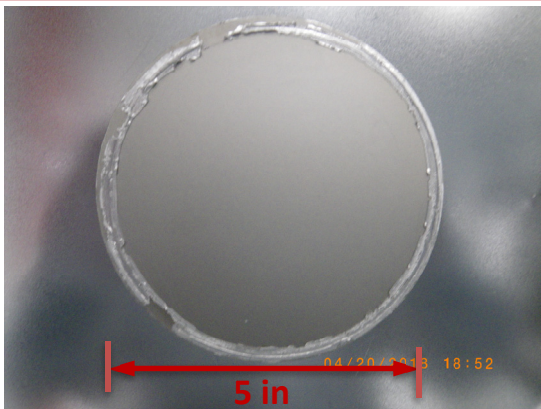
10 um EP'd sample



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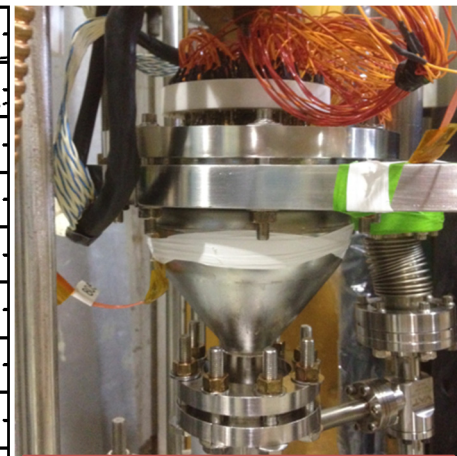
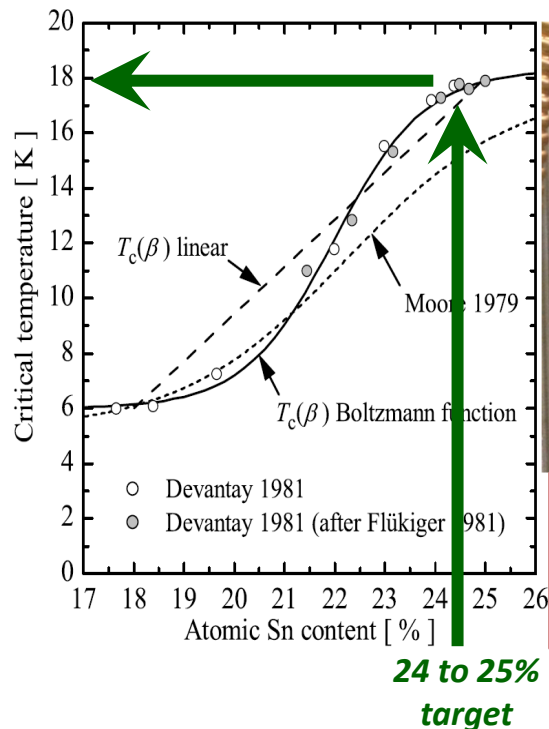
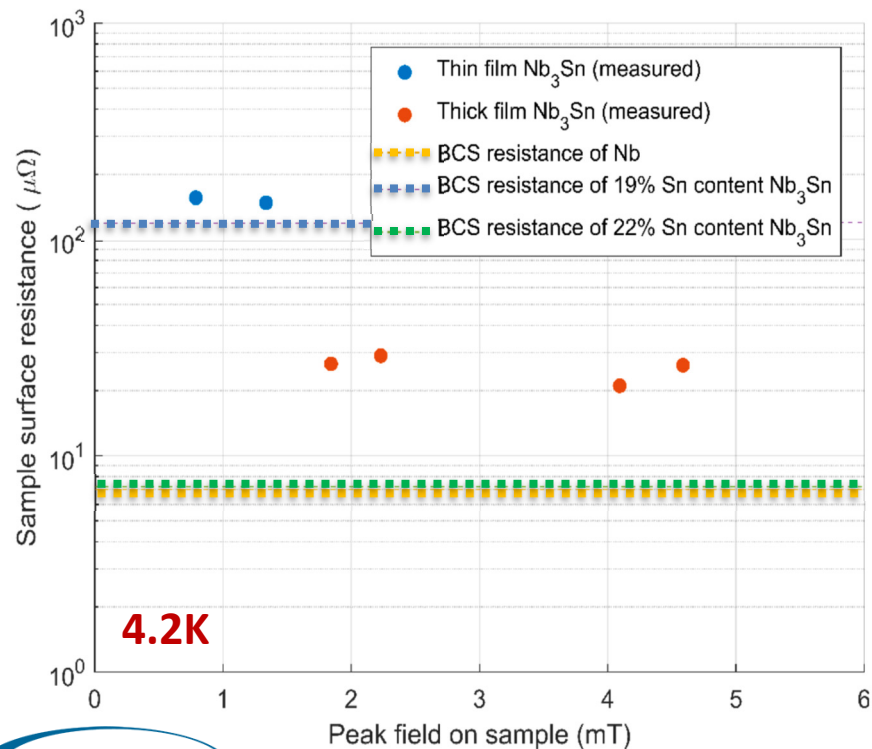
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CVD Nb₃Sn-Cu Plates



- **5 inches** large and uniform Nb₃Sn layer;
- Atomic Sn content: **19-22%**.


T_c vs. Tin Content



The plate tested on 4GHz Cornell sample host cavity



Conclusion

- ✓ 1) High Pure Nb (RRR > 250, large MFP);
- ✓ 2) Thick and strong Nb layer (>200um; HPR tumbling and EP);
- ✓ 3) Coating Nb layer on large samples;
- ✓ 4) Coating Nb layer on a full-scale cavity;
- ✓ 5) Low residual resistance ($R_0 < 10 \text{ n}\Omega$);
- ? ✓ 6) No severe Q-slope up to 5MV/m;
-  7) high accelerating gradient (E_{acc}).



Acknowledgement

The CVD works are strongly supported by DOE SBIR program (All the SBIR awards have been awarded to Ultramet):

- 2005 SBIR DOE Phase I: “Cost-Effective, High-Performance, High Purity Niobium Superconducting Radio Frequency Cavities”
- 2012 SBIR DOE Phase I: “Advanced Manufacturing and Testing of Seamless High Purity Niobium Superconducting Radio Frequency Cavities”
- 2013 SBIR DOE Phase I: “Barrier Coating Development for the Manufacture and Testing of Superconducting Radio Frequency Cavities”
- 2016 SBIR DOE Phase I: “Low Temperature CVD Process Development for Forming Niobium Layers on Copper”
- 2017 SBIR DOE Phase II: “Fabrication and Testing of Thick Film CVD Niobium-Lined Copper SRF Cavities for High Gradient Applications”
- 2018 SBIR DOE Phase II: “CVD Process Development of Thin Film Triniobium-Tin on Copper SRF Cavities”
- 2019 SBIR DOE Phase I: “Advanced Horizontal CVD Reactor Development for Increased Process Efficiency to Produce Seamless Thick-Film CVD Niobium-Lined Copper SRF Cavities”

Thank You!