

Nb₃Sn Cavities: Material Characterisation and Coating Process Optimisation

Daniel L. Hall Matthias Liepe



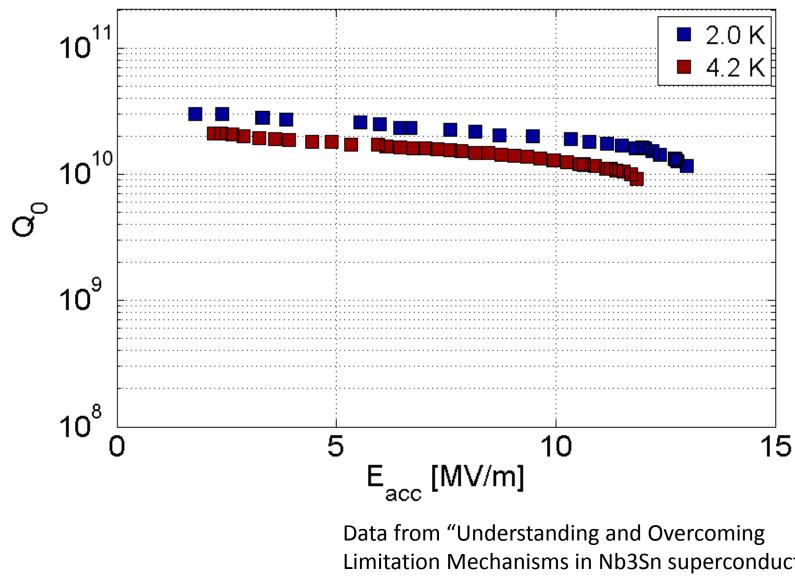


The promise of Nb₃Sn

- An intermetallic alloy of niobium and tin with a T_c of 18 K
- BCS resistance at 4.2 K is low enough to operate at cavity without pumping on the cryostat
- Superheating field of 400 mT corresponds to a peak theoretical field of 90 MV/m in ILC cavities



This time two years ago



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Limitation Mechanisms in Nb3Sn superconducting Cavities", Sam Posen, Cornell University, 2015



Talk outline

- Altering the coating procedure

 What happens to the RF performance?
- Characterising Cornell's Nb₃Sn
 - What kind of properties does it have?
- Practical use of Nb₃Sn
 - What performance can be expected for state-ofthe-art Nb₃Sn cavities in a cryomodule?

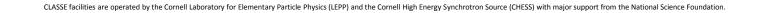
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Altering the coating process

What changes?

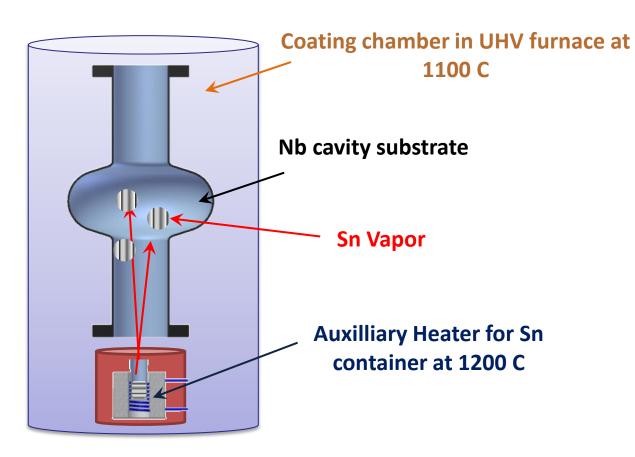




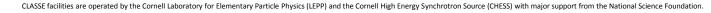
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Cornell's Nb₃Sn vapour diffusion





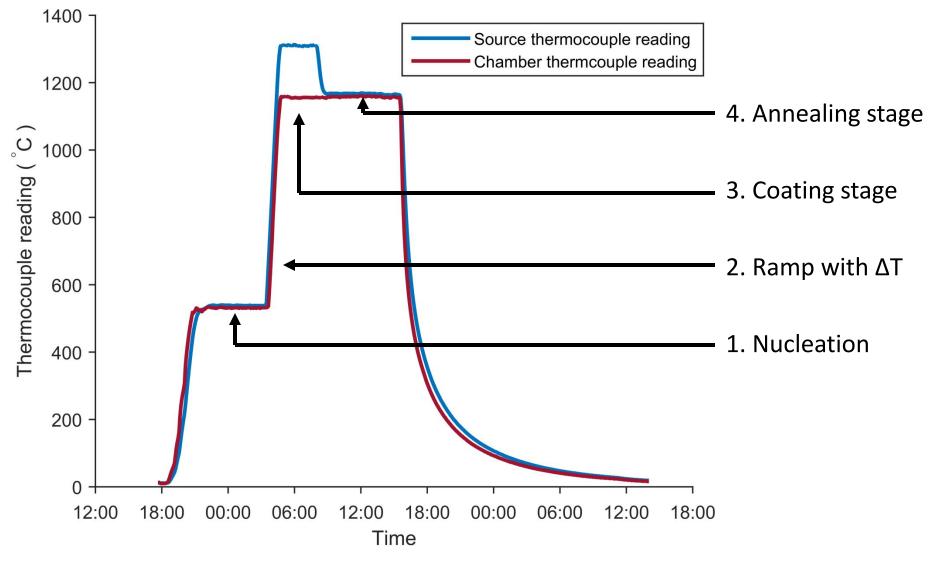






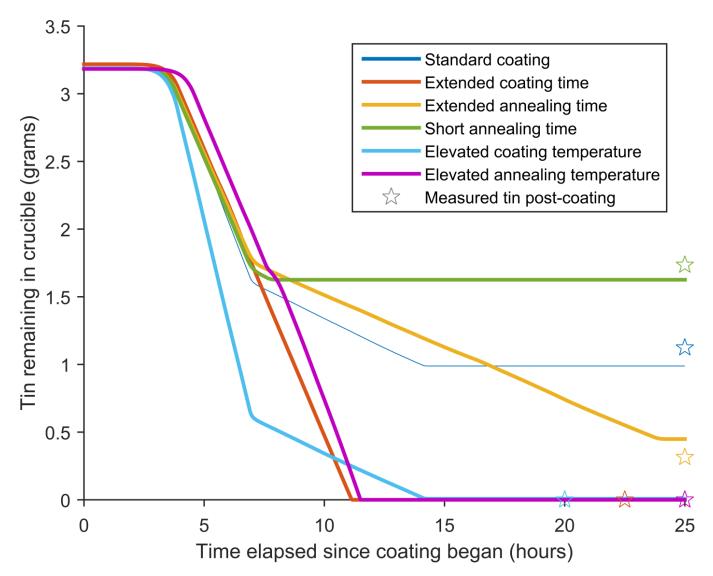
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The coating process





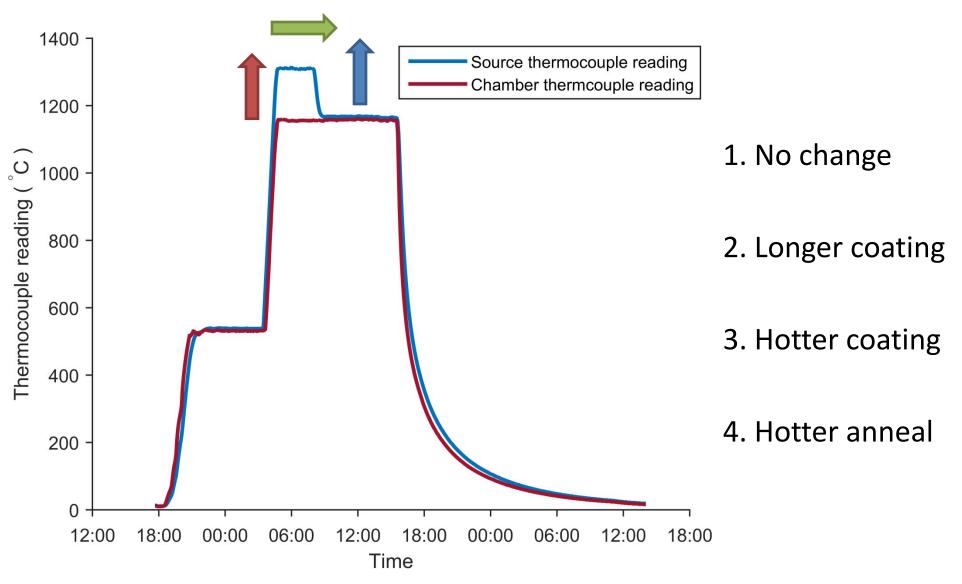
Simulating tin usage







What was changed?

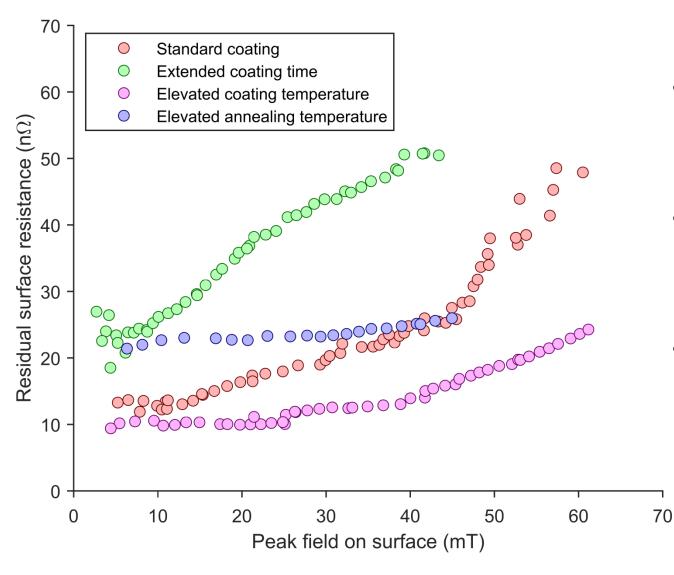


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Changes in residual resistance



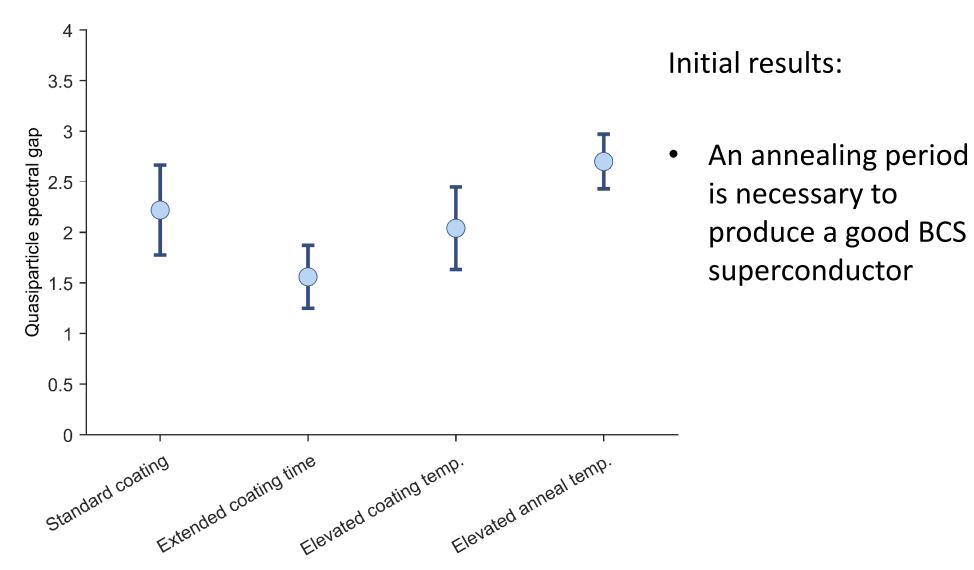
Initial results:

- Get tin to the surface faster
- Tin gas must have a short mean free path
- You can overdo the amount of tin you put on the surface



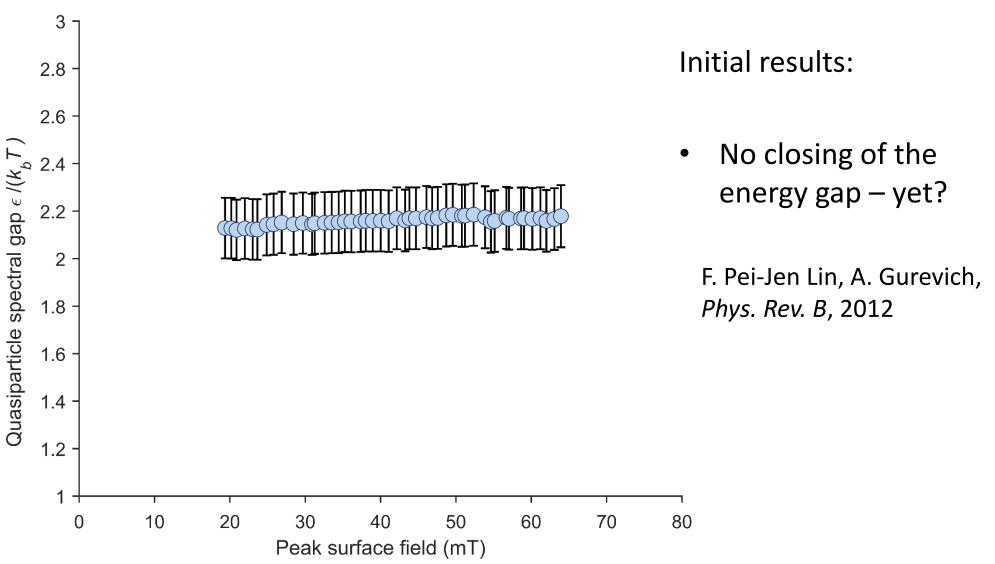


Changes in BCS performance





Spectral gap dependence on field





Where to next with the coating?

• We have begun exploring the coating parameter space

• The goal is to now lower the total surface resistance as much as possible





What have we made?

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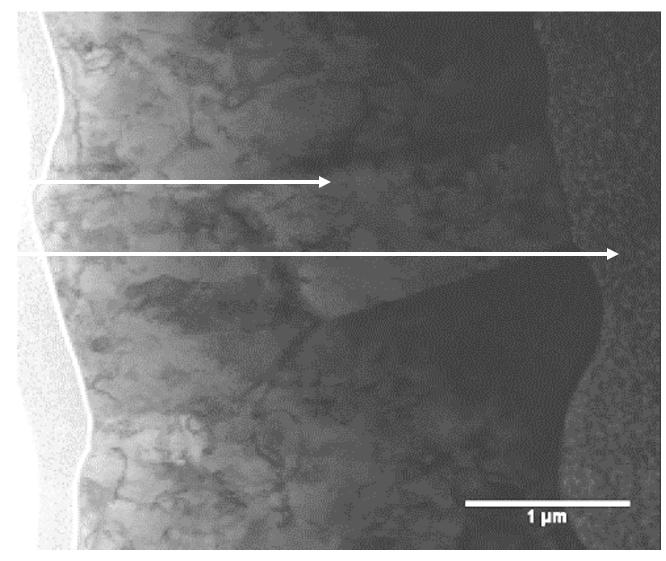
Material characterisation

 What material characteristics are responsible for the RF performance – both Q₀ and quench field?

• Surface analysis: TEM/EDX, SEM, AFM, XRD



Presence of tin depleted phases



Nb₃Sn layer

Nb substrate

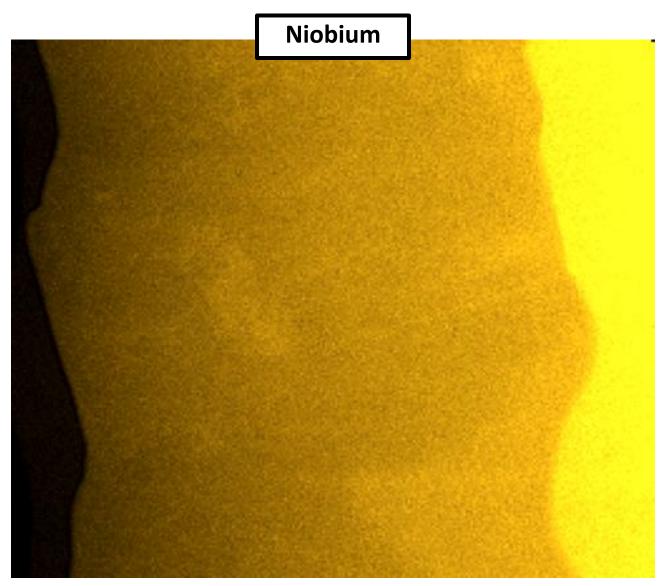
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Image courtesy of Thomas Proslier, ANL

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Presence of tin depleted phases



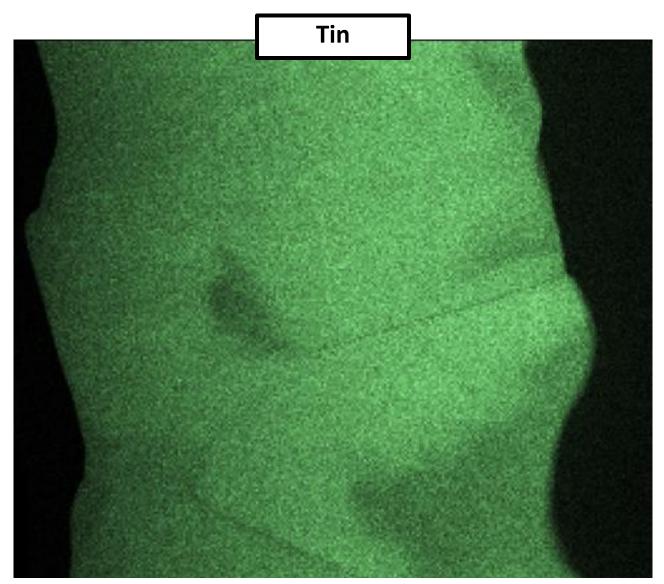
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Presence of tin depleted phases



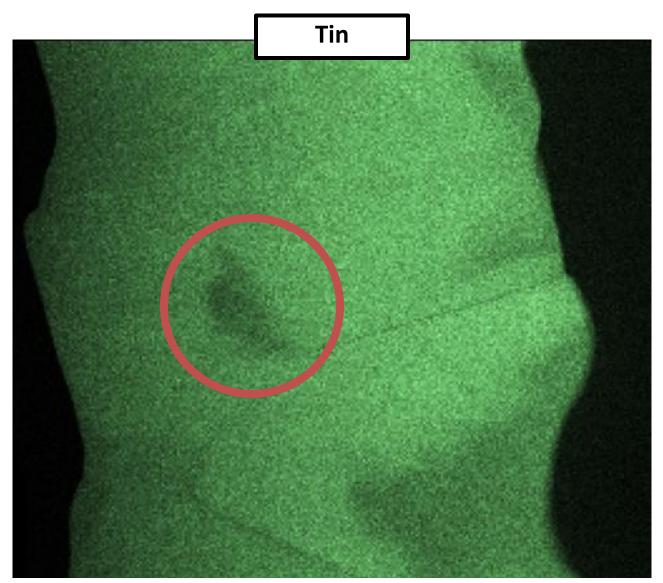
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Presence of tin depleted phases

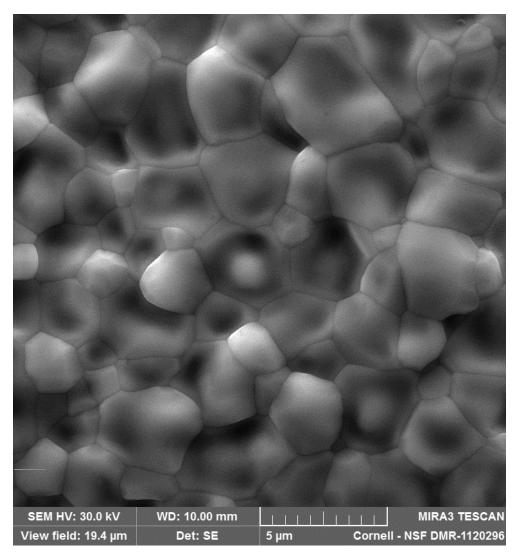


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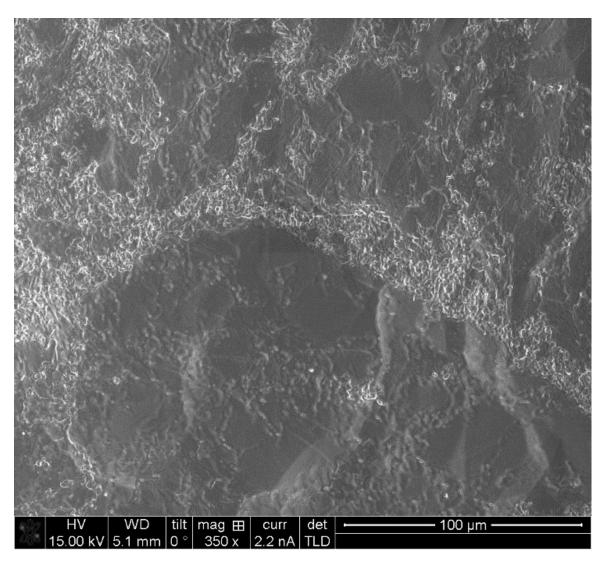


Surface uniformity



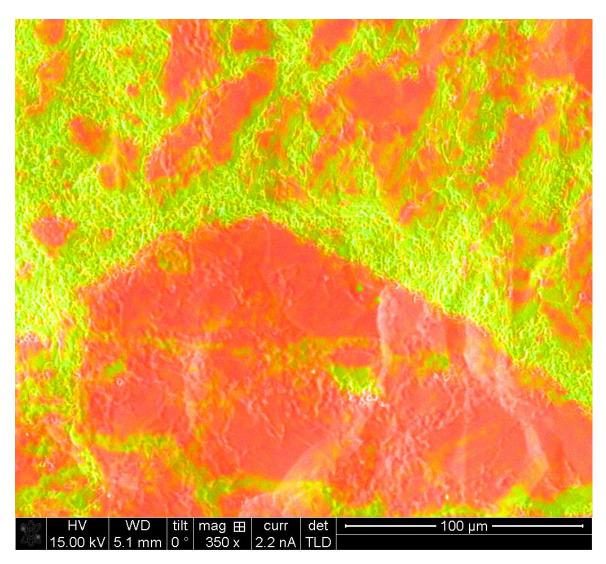
- Stoichiometric defects are on the order of 1 μm or less
- Have we seen any that are bigger than that?





- Yes!
- But it's rare only one example

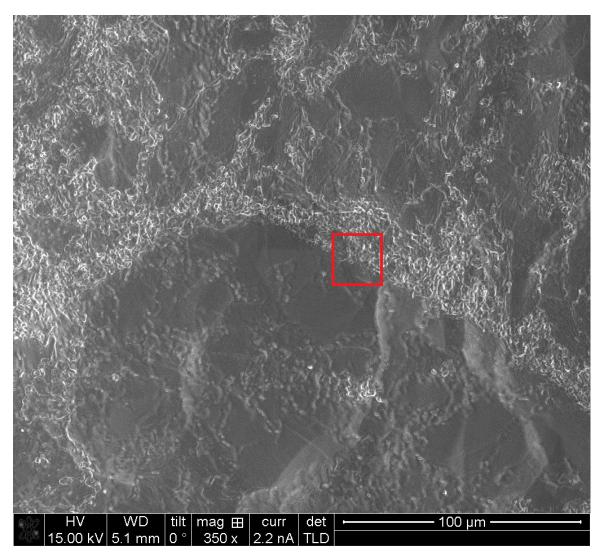




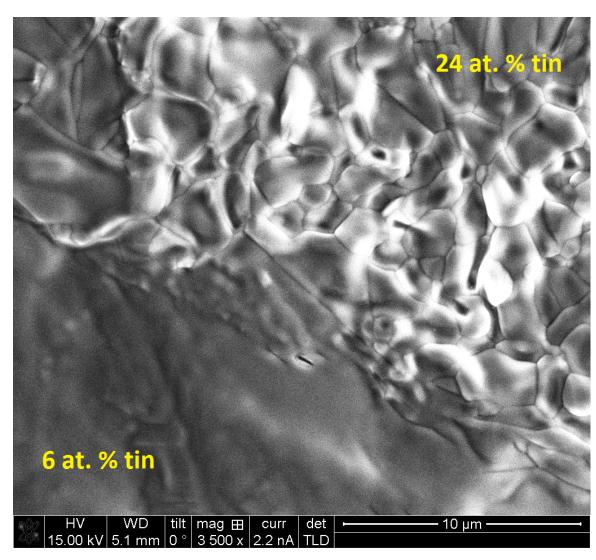
Green – good coverage

Red – poor coverage







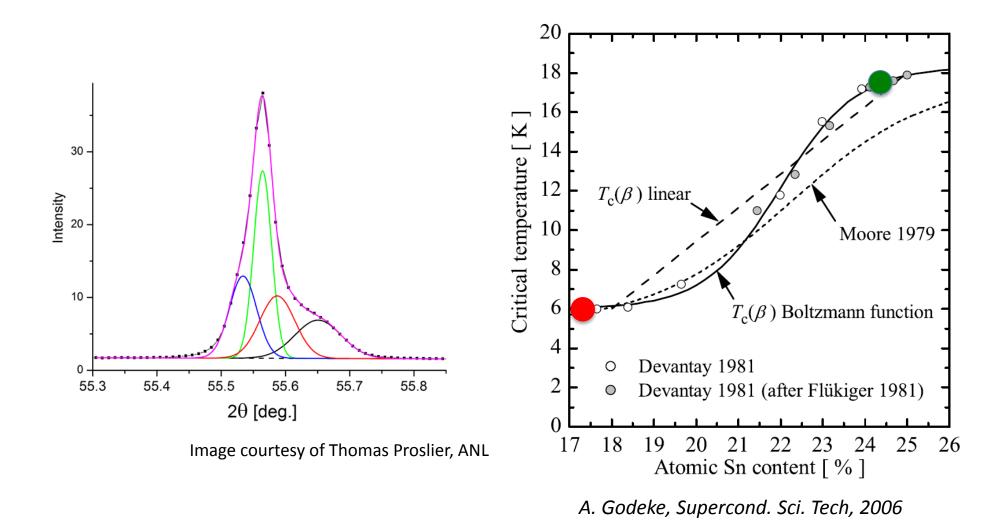


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See poster this afternoon – **TUPB049**

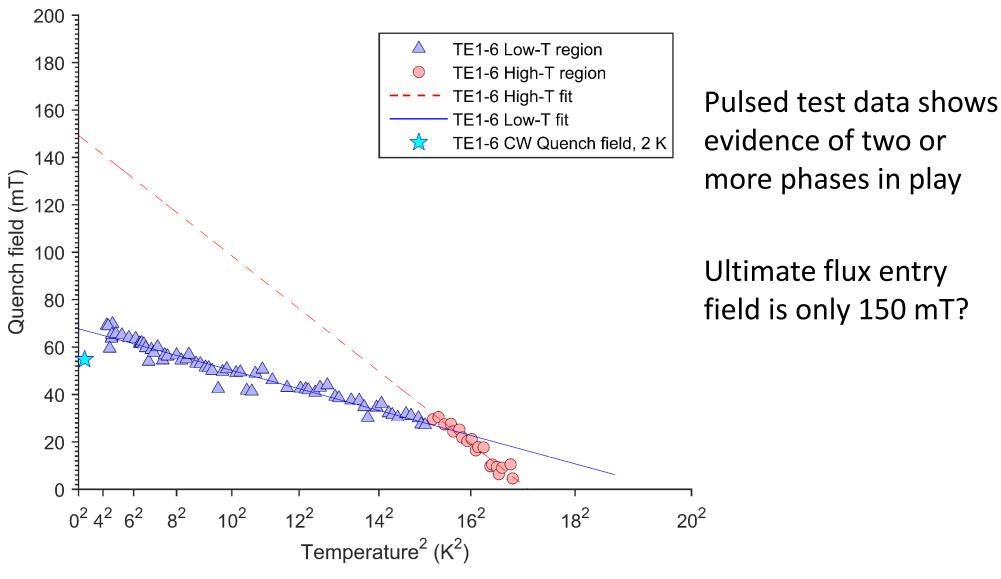


Why is tin-depletion bad?





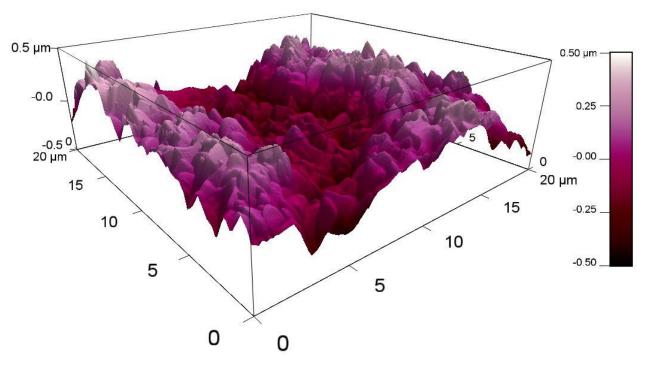
Measuring the flux-entry field







Surface roughness



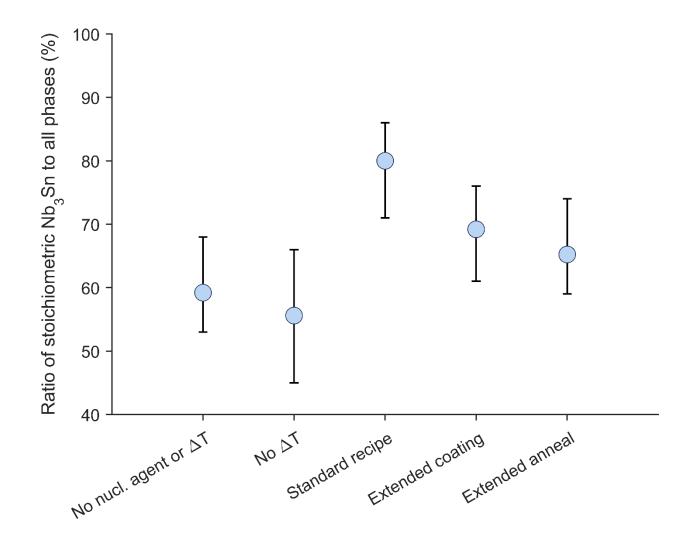
Surface is rough – it will cause field enhancement

Image courtesy of James Maniscalco, Cornell University

CCMR facilities are supported by NSF grant MRSEC 1120296



How do we avoid bad phases?





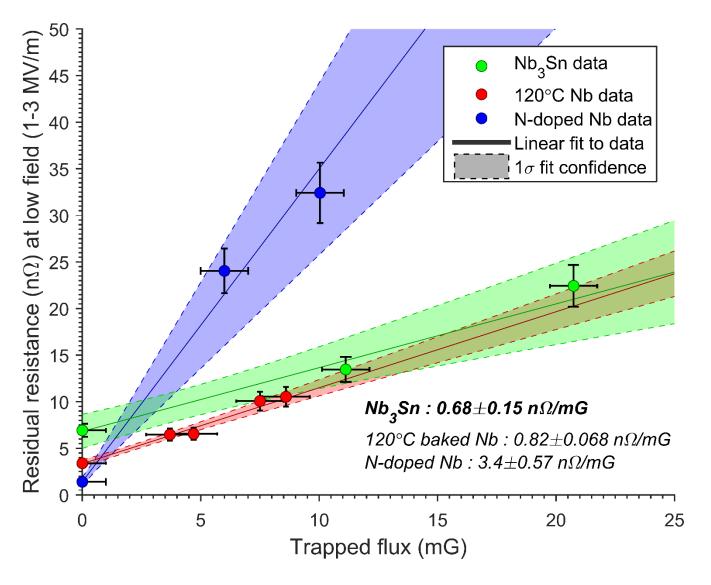
Nb₃Sn as a practical application

 Would cryomodule designs have to be modified to accommodate Nb₃Sn cavities?

 Is Nb₃Sn approaching a state where we would consider using it in an accelerator?



Sensitivity to trapped flux

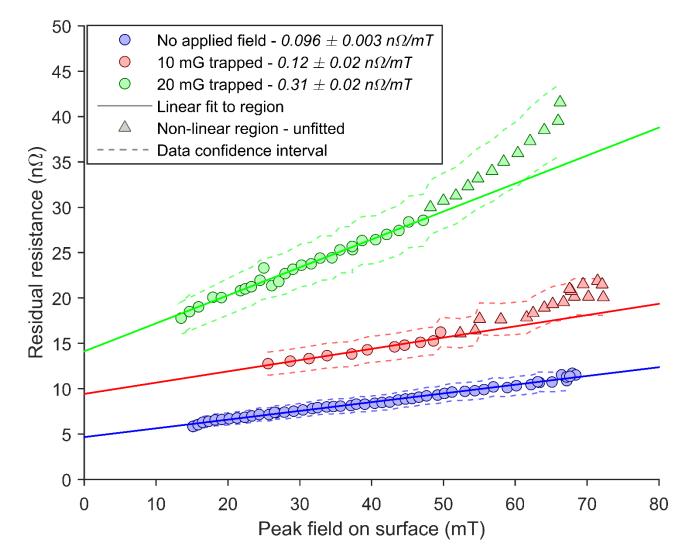


Nb₃Sn cavities have to be cooled slowly

No more sensitive to trapped flux than 120 °C baked niobium

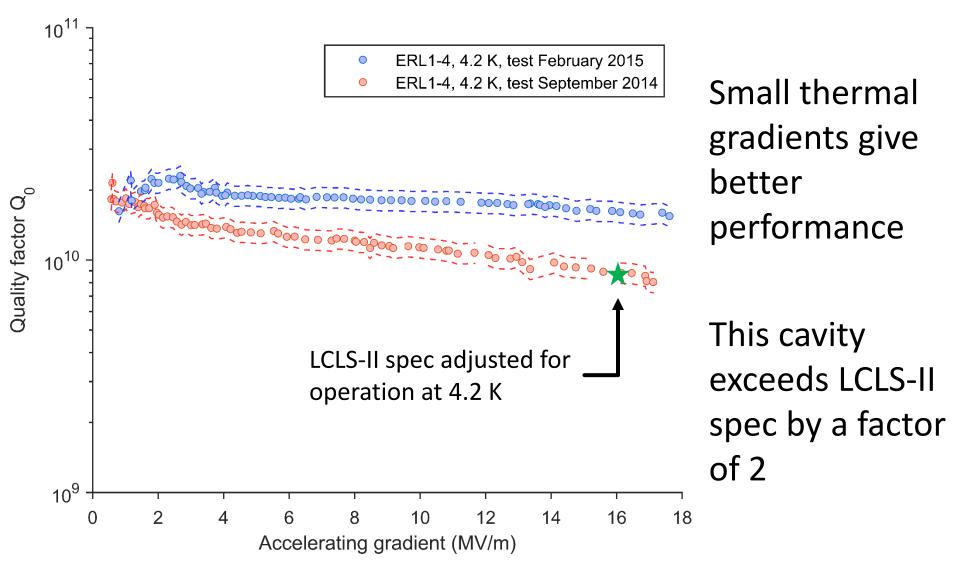


Trapped flux and Q-slope





Impact of the cooldown procedure





Summary and conclusions

- The current limitations of Nb₃Sn are due to its fabrication and are not fundamental to the material itself
 - Current limitation: stoichiometry, tin-depletion
- We are optimising the coating process

 Looks promising!
- Nb₃Sn cavities are not sensitive to losses from trapped flux
- The state of the art has exceeded the LCLS-II specifications by a factor of 2



The world's first Nb₃Sn accelerator?

Stay tuned

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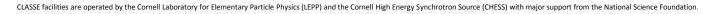
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Matthias Liepe Georg Hoffstaetter Sam Posen **Thomas Proslier Daniel Gonnella Byeonghee Yu** James Maniscalco





Fermilab Fermilab Nb₃Sn Program

Sam Posen SRF2015

- Fermilab: Nb₃Sn R&D program funded by LDRD
- Will build a coating chamber large enough for TeSLA 9-cell cavities or 650 MHz 5-cells
- Continue R&D to push performance and begin proof of concept for practical applications

