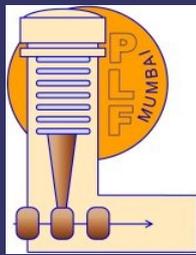


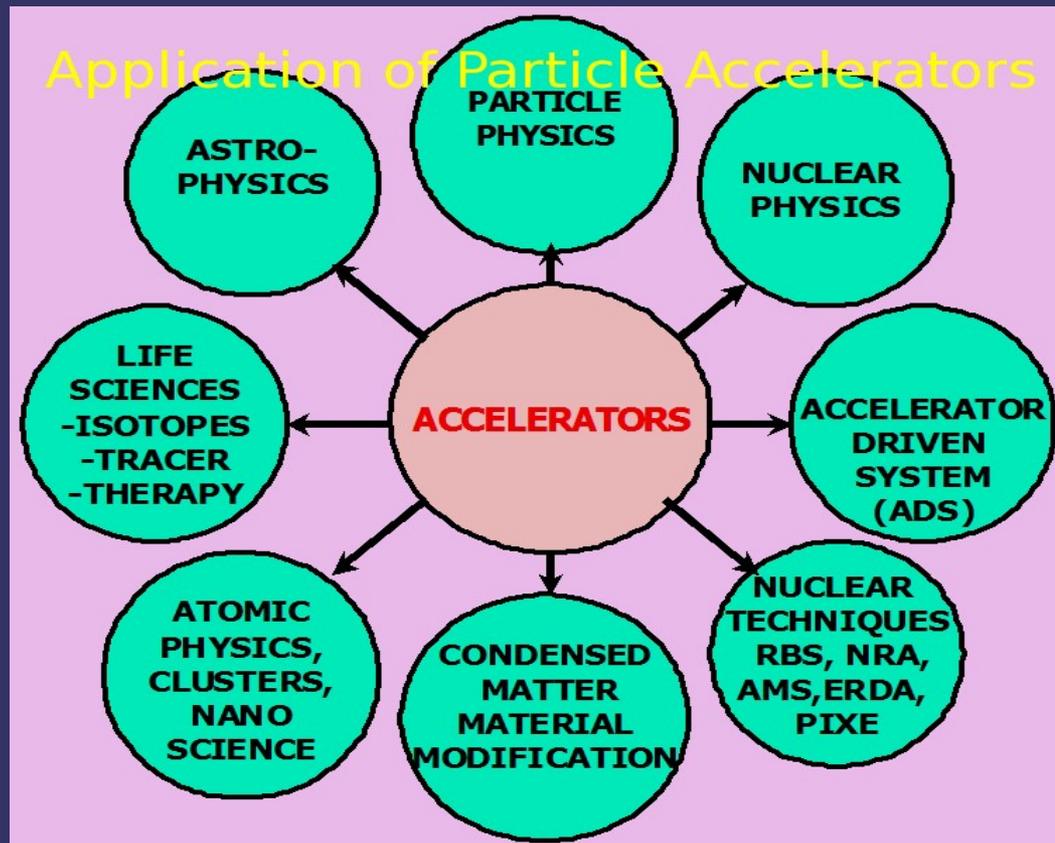
Control System for BARC-TIFR Pelletron

*S K Singh, Pitamber Singh, IADD
Jaydeep Gore, Sunil Kulkarni, NPD
BARC, Mumbai*



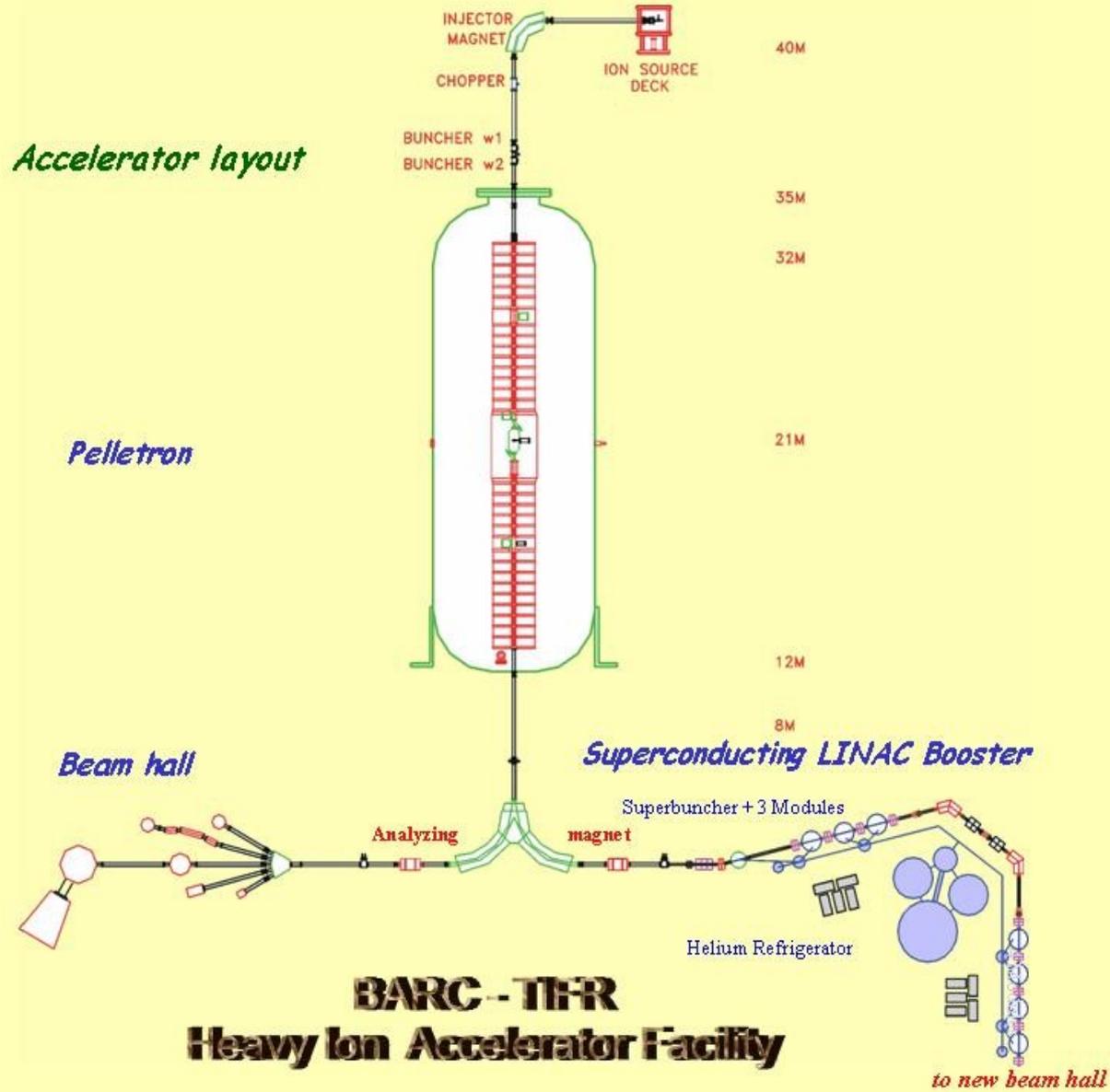
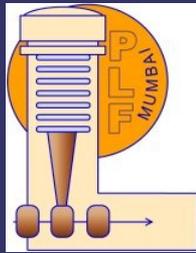
Particle Accelerator

- ➔ Device that accelerates a beam of fast-moving, electrically charged atoms (ions) or subatomic particles.



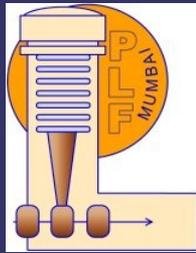


BARC-TIFR PLF





Control System and Data-acquisition System



Sensors > convert physical parameters to electrical signals.

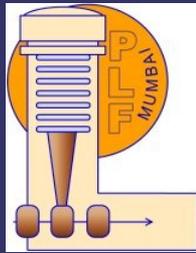
Signal conditioning Electronics>sensor signals into a form to be converted to digital

ADC> signals to digital values.

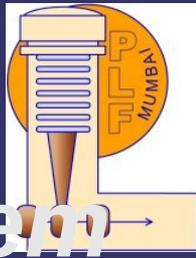
- Both DAQ and Control system will have data acq and control
- In Data acquisition system we need to set the detector biasing and other setting requiring control (Static)
Acquired data need to be stored for future analysis
Large data buffering and fast transfer is required
EX: LAMPS(CC2000,FERA, ECC), PHAST (MCA)
- In control system control is always dynamic
acquired data is mostly of interest at the time
Data can be stored for machine diagnostics and history/trend



Accelerator Control System

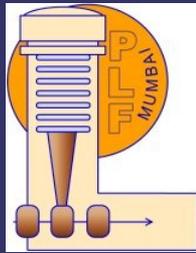


- Control system couples the operator to the accelerator.
- The Control System has to suit the plant not the reverse.
- The structure of the control system should reflect that of the plant.
- Ideally, each unit of the plant should have its own controller, interacting with the controllers of the other, related units, mirroring their physical interaction.



Demands of Accelerator Control System

- Remote Control
- On-line Monitoring and Control
- Audio Visual Alarm
- Interlock for machine protection
- Multiple Operator Consoles
- Reliable, Integration, expandable, Configurable
- On line machine configuration
- Different modes of operation (Normal, Diagnostics & Commissioning)

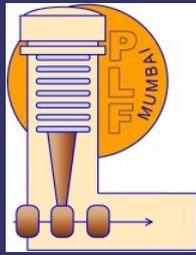


Scope

- Sensors and actuators
- Instrument controllers(Intelligent, Simple)
- Front End Instrumentation
(CAMAC, VME, PXI, PCI, cPCI)
- Digitization units ADC, DAC, DI/O, Special purpose units
- Operator front end units (CRO, PC graphics, Panel Meters, Knobs, Buttons)



Scope

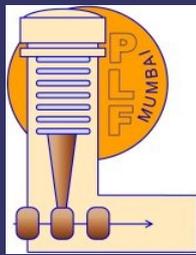


- **Field Bus** (Fieldbus is a generic-term which describes a new digital communications network which will be used in industry to replace the existing 4 - 20mA analogue signal.)
 - Profi-Bus,serial Highway,RS232/422/485
 - Current loops,SIB,Ethernet
- **Communication Bus**
 - Ethernet,Telephone lines,W-lan
- **Messaging Protocol/Middlelayers**
 - OPC,MOD-BUS,RPC,CORBA,Proprietorial
- **Communication Protocol**
 - TCP/IP,Proprietorial
- **Software Environment**
 - OS,Programming Language,Tools



Architecture of Control System

System Architecture



Starting from monolithic (Single layer) architecture the control system has matured to multilayer architecture. multilayer model can be further classified as centralized and distributed architecture. Monolithic architecture can fit itself only in centralized architecture of control system.

- Centralized
- Distributed
- Geographical
- Functional
- Systematic
- Mixed

Both are having different merits

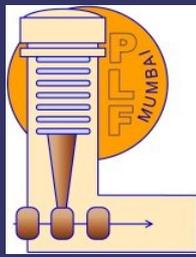
Centralized:

- Simple system
- Easy to meet different Constraints
- Difficult to realize for a developing system
- Reliability can be achieved by putting Standby system

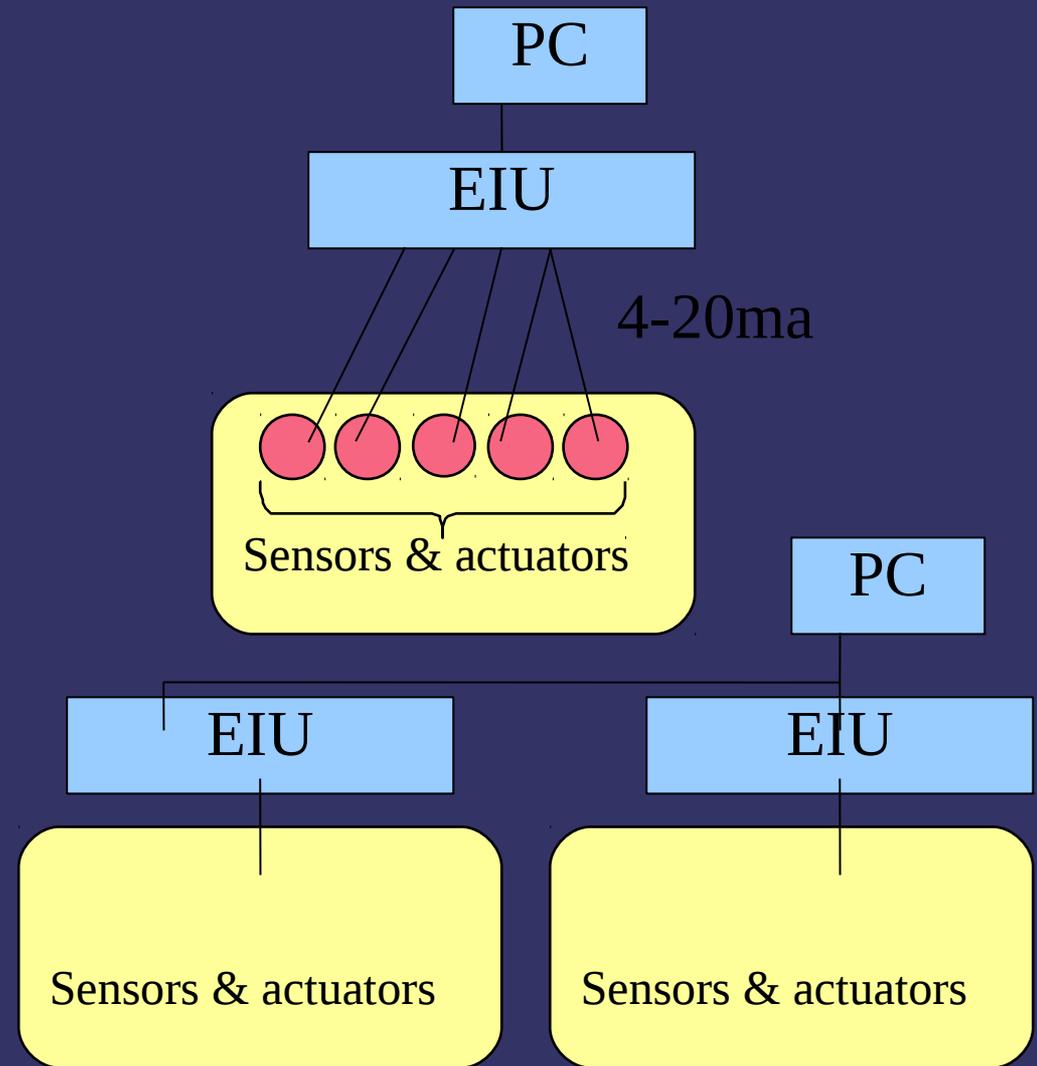
Distributed:

- More nearer to Real life domain
- Highly suitable for developing Machines (Extensibility)
- Inherent reliability is incorporated

Monolithic Architecture

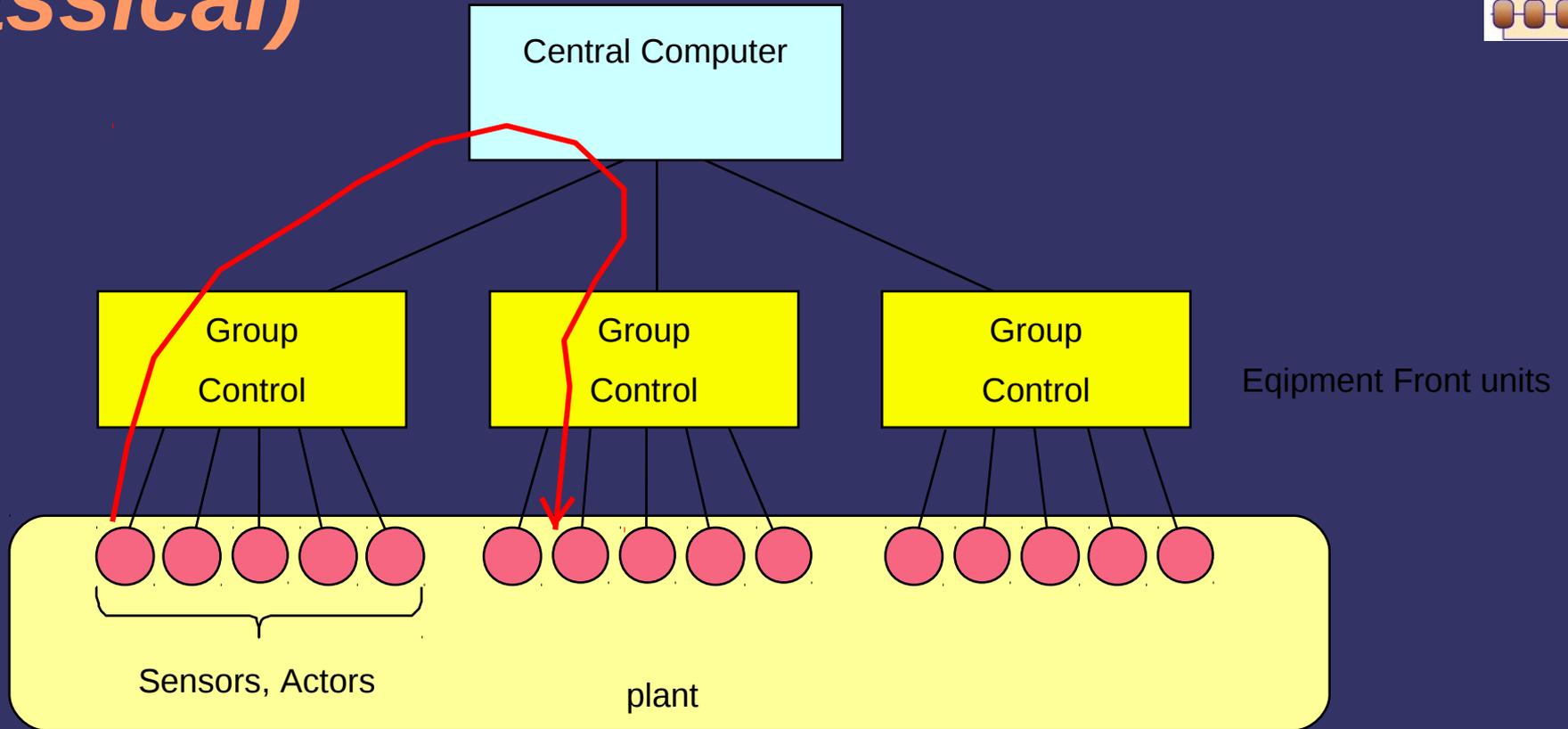
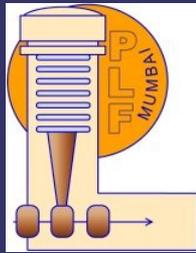


- Processing, data and the user interface all reside on the same system as a part of single software program.
- monolithic system are unsuitable to manage the complexity in the system as any mistake done to handle will put the whole system down it is always easier to handle complexity at small level.
- As the operator interface is also an integrated part of the system , it is directly exposed to the operator and any mistake will jam the whole system.
- unsuitable for incremental development during machine commissioning and installation phase as well as up gradation of either machine or control system itself.
- Difficult to add new features to the system





Centralized Control Architecture (classical)



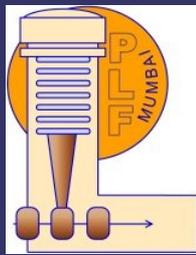
Classical, hierarchical, centralized architecture.

The central computer only monitors and forwards commands to the EFU's

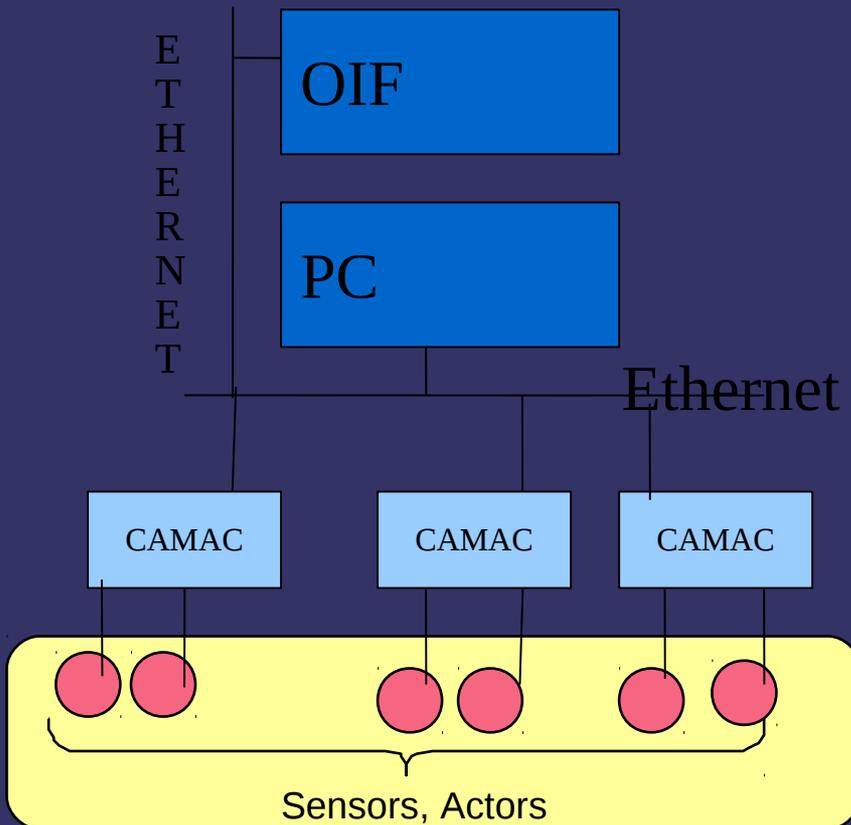


PELLETRON

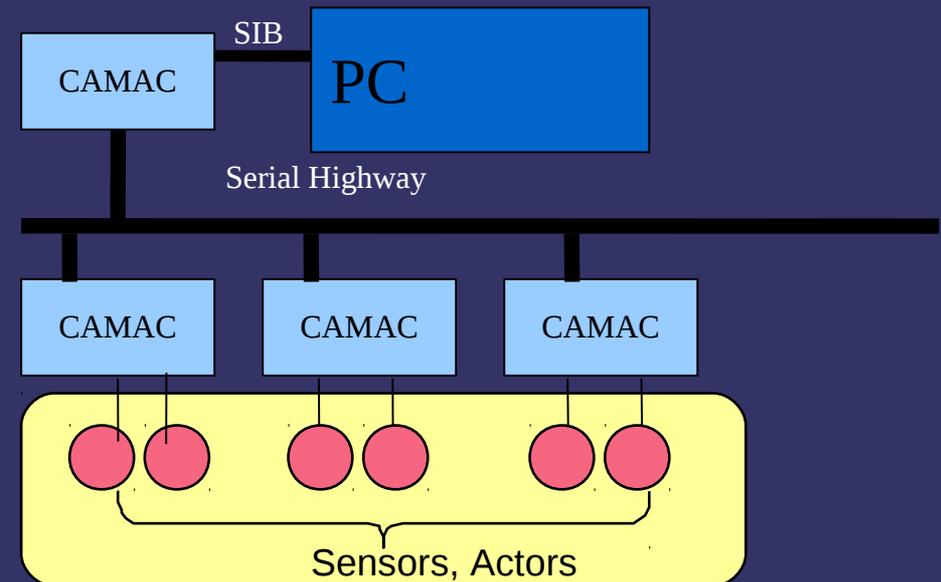
Control System Architecture (Centralized)



• Now

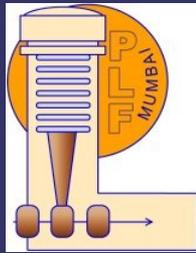


Before





Multilayer architecture



Operator Interface (PC running on Linux or MS Windows)

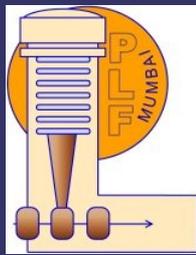
Device control Unit (PC running on Linux)

Equipment interface Unit (CAMAC with ECC)

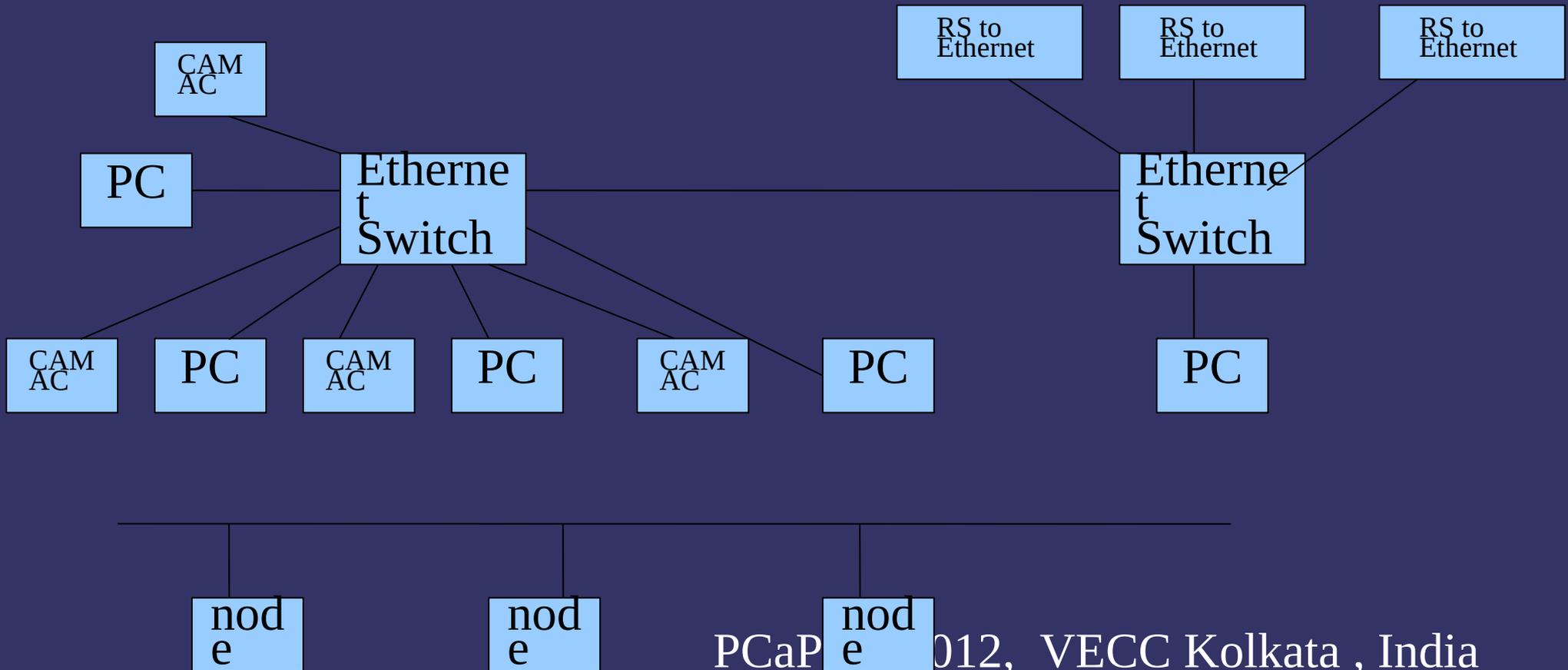
With ADCs, DACs, DI/DOs



Control System Communication Architecture

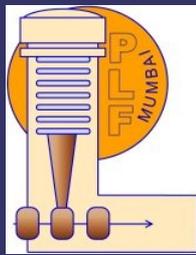


- ➔ Standard Control Model is a communication oriented System
- ➔ Ethernet can be made more suitable





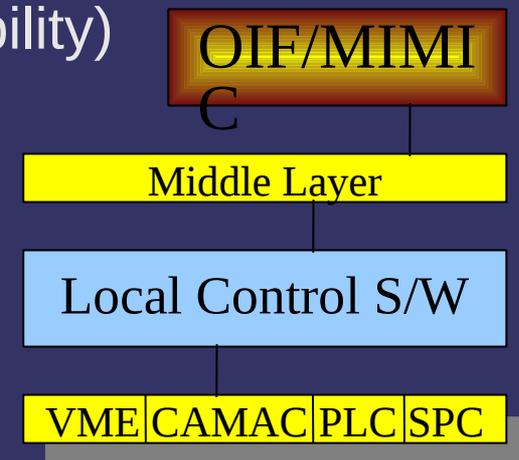
Software Architecture



Common Goals

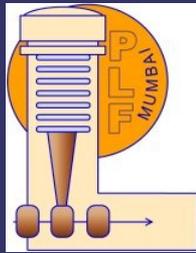
- Portability
- Extensibility
- Proper Engineering Practices Suitable for R&D Programs
- Single layer (Single s/w program)
 - +Inherently fast response time
 - +Simple to develop
 - Difficult to handle complexity
 - Difficult to meet reliability & Fault Tolerant
 - Difficult to provide multiple consoles
- Multiple layer (Multiple s/w Programs)
 - Hierarichal Control Software
 - +Variety of controllers can be selected(Extensibility)
 - +OIF/MMIC is independent of lower layer
 - +Easy to handle the complexity
 - +Incremental Development
 - +Reliable and Fault Tolerant
 - +Multiple Console and Expandability
 - -Requires Specialized Development Team

Operator Interface
Control program
I/O Access
Configuration Data
Run Time Database
Device Drivers
EFU Controllers

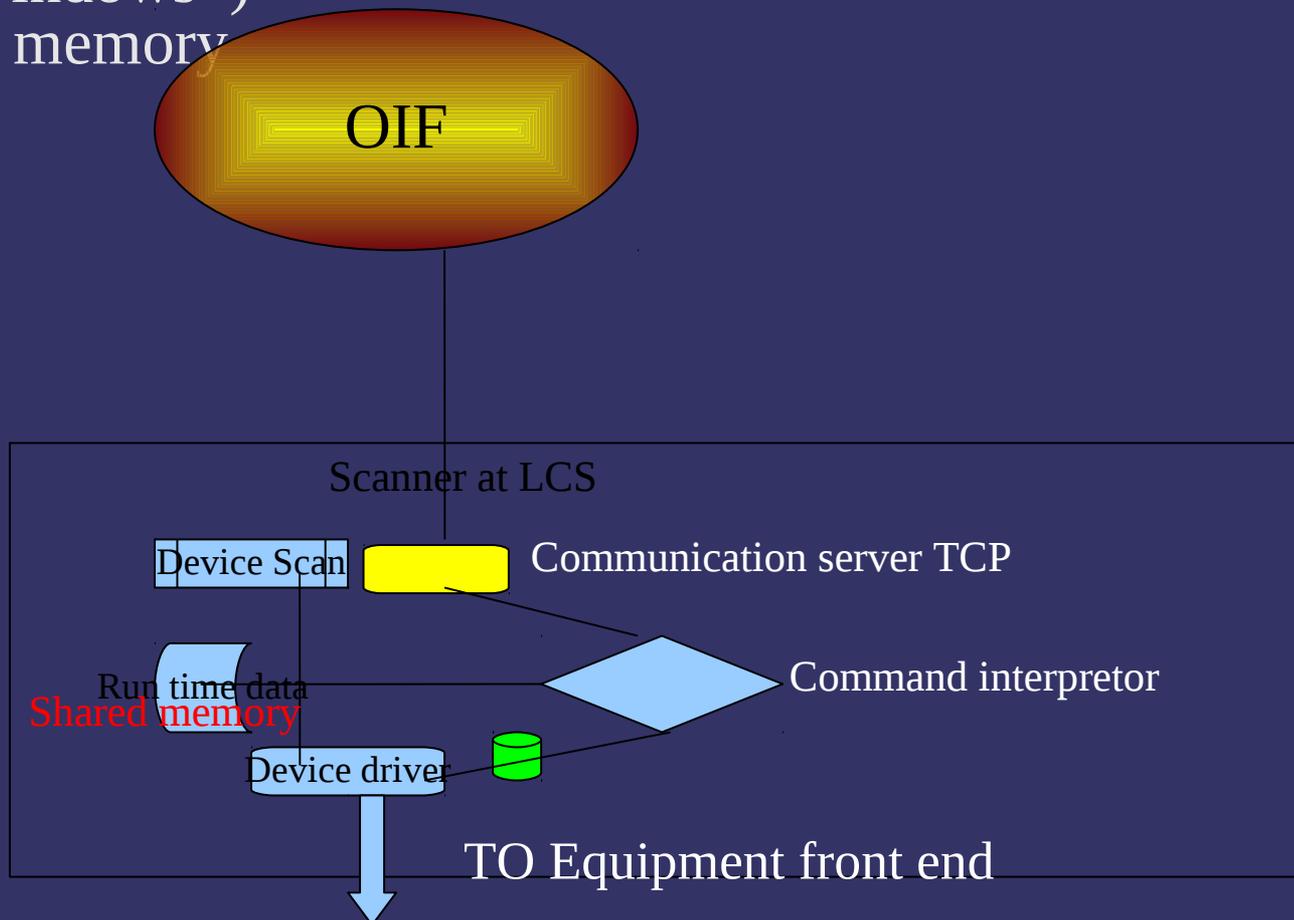


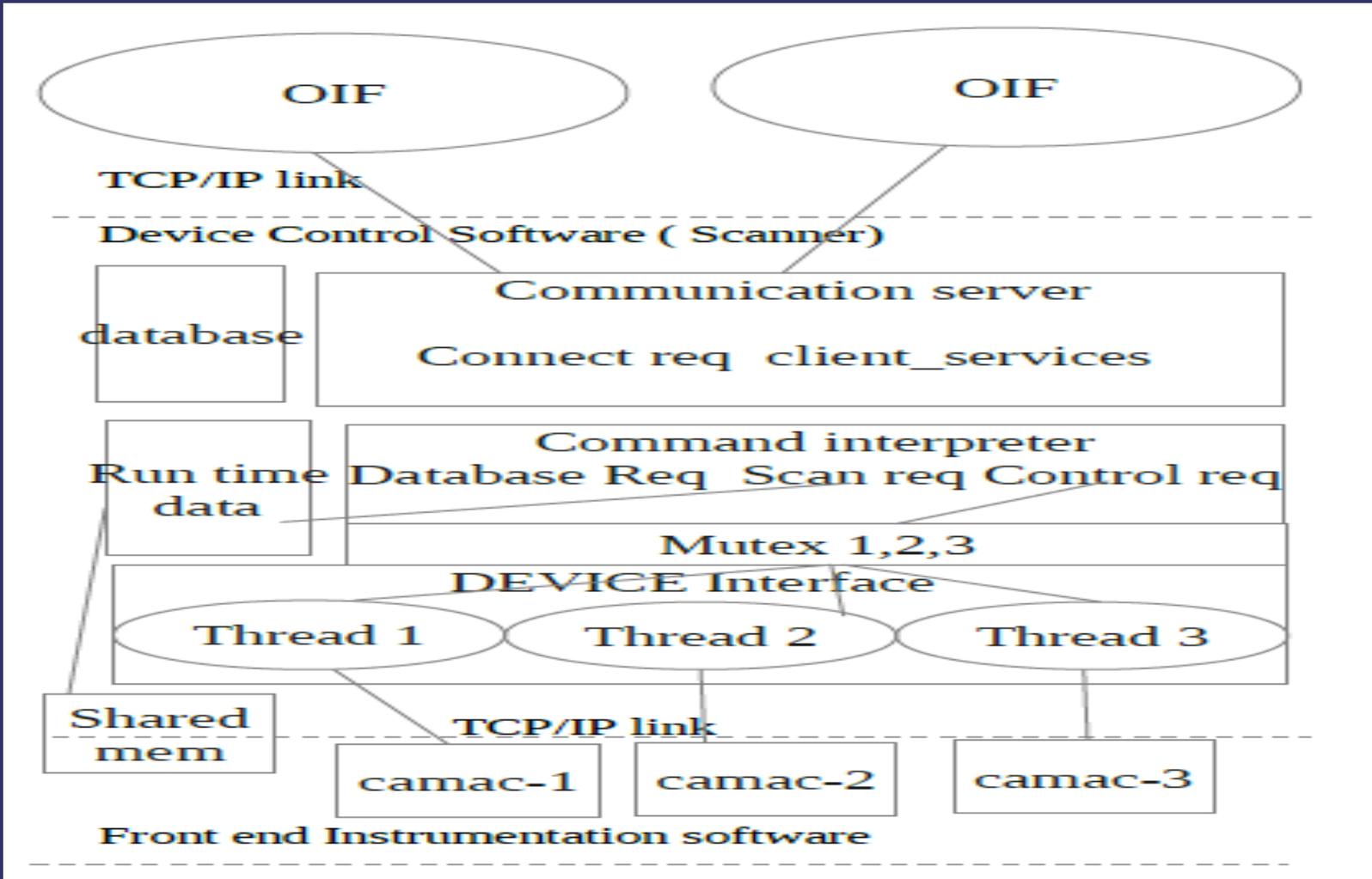
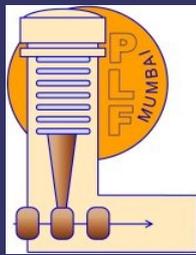


Software Architecture PELLETRON



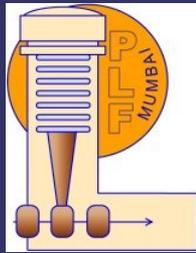
- Multi layer Control s/w (scanner,OIF)
- Equipment Interface Unit :Scanner (LINUX based)
- Operator Interface unit OIF (Source code portable on LINUX ,MS-windows)
- Shared memory







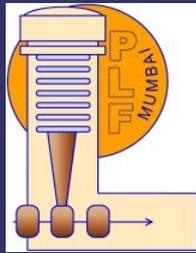
CONTROL Software



- Database Driven System
- Front End Instrumentation unit (QNX RTOS)
Runs on ECC
- Device control unit software is POSIX standard Multithreaded software runs on Linux. Is three layer architecture
 - Device Interface layer
 - Command interperator Layer
 - Communication Layer
- Shared memory is used as persistent memory bock



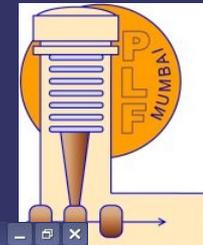
LINUX For Control System



- From long Linux has been in the wishlist for control system
- Security (less prone to virus and malwares)
- Stability and Reliability
- Open source and less cost\
- Wide applicability from servers to tiny embedded systems and realtime applications
- Availability of development tools



Pelletron MIMIC



FOTIA Control and Information System

File | [0] [14] COUNT BYTES = 2 0

Parameter	AnalogOut	AnalogOut	AnalogImp	AnalogImp	DigitalIn -1	DigitalIn -2	DigitalIn -3	DigitalOut	DigitalOut	DigitalOut
LHTR								ON		
FIL	5.605 Amp	5.14 Amp	0.86 V					ON		
EXTR	2.824 KV	2.83 KV	0.37 mA					ON		
FOC	0.839 KV	0.83 KV	0.05 mA							
CAT	-1.011 KV	-1.09 KV	0.06 mA							
OVEN								OFF		
DECK	0.000 kV	0.88 kV								
ESX1	2.851 KV	0.29 KV	0.69 KV					ON		
ESY1	2.963 KV	0.30 KV	0.50 KV					ON		
ESX2	2.926 KV	0.29 KV	0.54 KV					ON		
ESY2	3.037 KV	0.30 KV	0.33 KV					ON		
ESX3	3.055 KV	0.44 KV	0.29 KV					ON		
ESY3	3.111 KV	0.54 KV	0.30 KV					ON		
IMAG	0.000 Amp	0.00 Amp						ON		
DEFL	0.000 KV	0.00 KV	0.00 -KV					ON		
EQT1	0.919 KV	0.89 KV	-0.89 -KV					ON		
EQT2	1.242 KV	1.21 KV	-1.21 -KV					ON		
CPS-	0.000 KV	0.02 KV	0.00 mA					ON		
CPS+		0.00 KV	0.00 mA					ON		
TRV	0.000 MV									
Temp		-273.00	0C-273.00	0C				OFF		
TIP		0.0e+00								
FMAG	0.000 Amp	0.00 Amp						OFF		
FC1		1.0e-10			IN	NOP	ON	ON		
FC2		27.35 nAm			NOP	OUT	OFF	ON		
FC3		0.00 nAmp			NOP	OUT	ON	ON		
FC4		0.00 nAmp								

FCB1	0	1	2	3	4	5	6	7	8	9	10	dump	xx	xx	xx	xx	FCB2	0	50S9	50S10	50Sd	25S9	25S10	25Sd	25H9	25H10	25Hd	50H9	50H10	50Hd	xx	xx	xx
BPME	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	BPME	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15

EQT2

339 EQT

Rate [F/M/C]

FREE

0.0 unit

Rate [F/M/C]

FREE

0.0 unit

Rate [F/M/C]

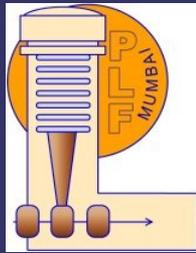
FREE

0.0 unit

Rate [F/M/C]



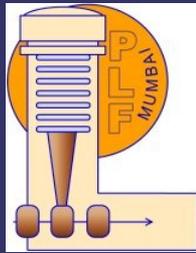
Features



- ➔ Configurable pages from database
- ➔ Named Pages
- ➔ Assignable multi function sliders for analog out control.
- ➔ Multiple ways of interaction using mouse and keyboards
- ➔ Assignable meters for analog read backs
- ➔ Developed in Trolltech's QT 4 which is source code portable on MS windows and linux



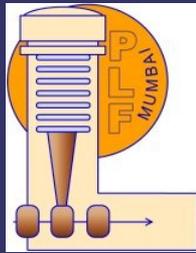
PORTABLE API QT



- C++ Based Source code portable API
- Windows, MAC, LINUX/X11/SOLARIS, EMBEDDED LINUX, QNX, VxWorks
- SIGNALS and SLOTS
- High runtime performance and small footprint
- Gdb and CDB debugger
- Qt Creator IDE can be embedded with Kdevelop and Visual studio
- Version control
- Integrated UI builder
- Epics qt framework (<http://sourceforge.net/projects/epicsqt>)
- qt.digia.com



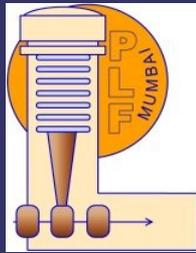
SCADA Systems



- ➔ Many SCADA systems are available (PVSS, WINCC, PROFICY)
- ➔ Unsuitable for accelerators
- ➔ EPICS (Experimental Physics and Industrial Control Control System SCADA)
ANL LANL, ORNL (SNS), SLAC , JLAB (CEBAF), DESY, BESSY, PSI, KEK
It's free & Open Source
 - The computer can be: - VME based, running vxWorks (only choice until Release 3.14) or RTEMS
 - PC running Windows, Linux, RTEMS
 - Apple running OSX
 - UNIX Workstation running Solaris



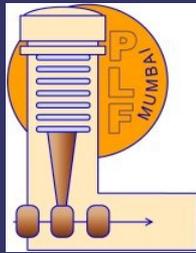
Conclusions & Future Scope

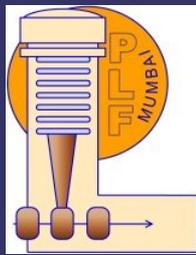


- Are there chances for standardization of control system interface, hardware solutions?
- Would enhance software sharing, reduce resources needed to build the basic system and perhaps result in better solutions
- Specific applications and needs that cannot be standardized will always arise
- Every accelerator has a different flavour
- The tasks are common, the solutions resemble (but are still different)
- Move towards automated tuning
- Use of expert system & AI techniques for better life
- Incorporation of new Technologies (use of cellphones)



- ACKNOWLEDGEMENTS
- Shri Kuldeep Jha, Mrs Anita Behere, Shri M P Diwaker ELD , BARC
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- DR S Kailash Director Physics Group





Thank you

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