



# FESA

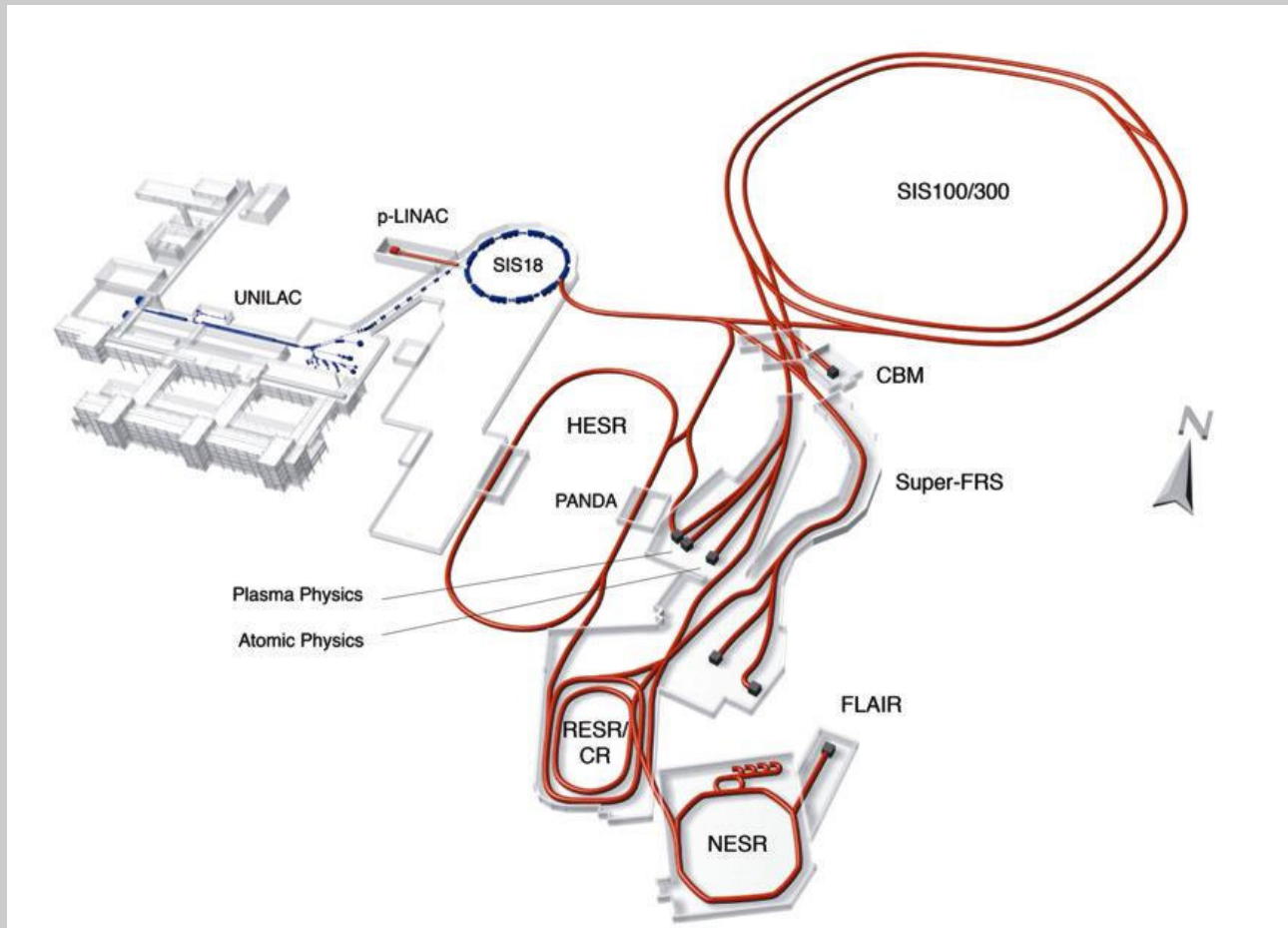
## Front-End Software Architecture at FAIR

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# FAIR

## Facility for Antiproton and Ion Research

International Project  
Min. 14 members  
9 new accelerators  
 $1.2 \times 10^9$  € Costs



## Why a new control system ?

- Change from control system to a control- and data acquisition system
- New hardware and technologies for high performance (e.g. Imaging)
- Standardization due to:
  - external partners ( in-kind contributions)
  - many many distributed devices
  - maintenance

# What is FESA?

- FESA:** Front-End Software Architecture  
Roots in CERN Beam Diagnostics group
- Aim:** Create a FESA class (executable) for real-time  
DAQ on a front-end CPU
- Runs on:** VME, cPCI/PXI, VXI, PCI,  
PLC (Schneider & Siematic)  
RT-Linux/Linux/LynxOS
- CPU:** Motorola, PowerPC, Intel

More information and pictures (Java Gui) at the poster

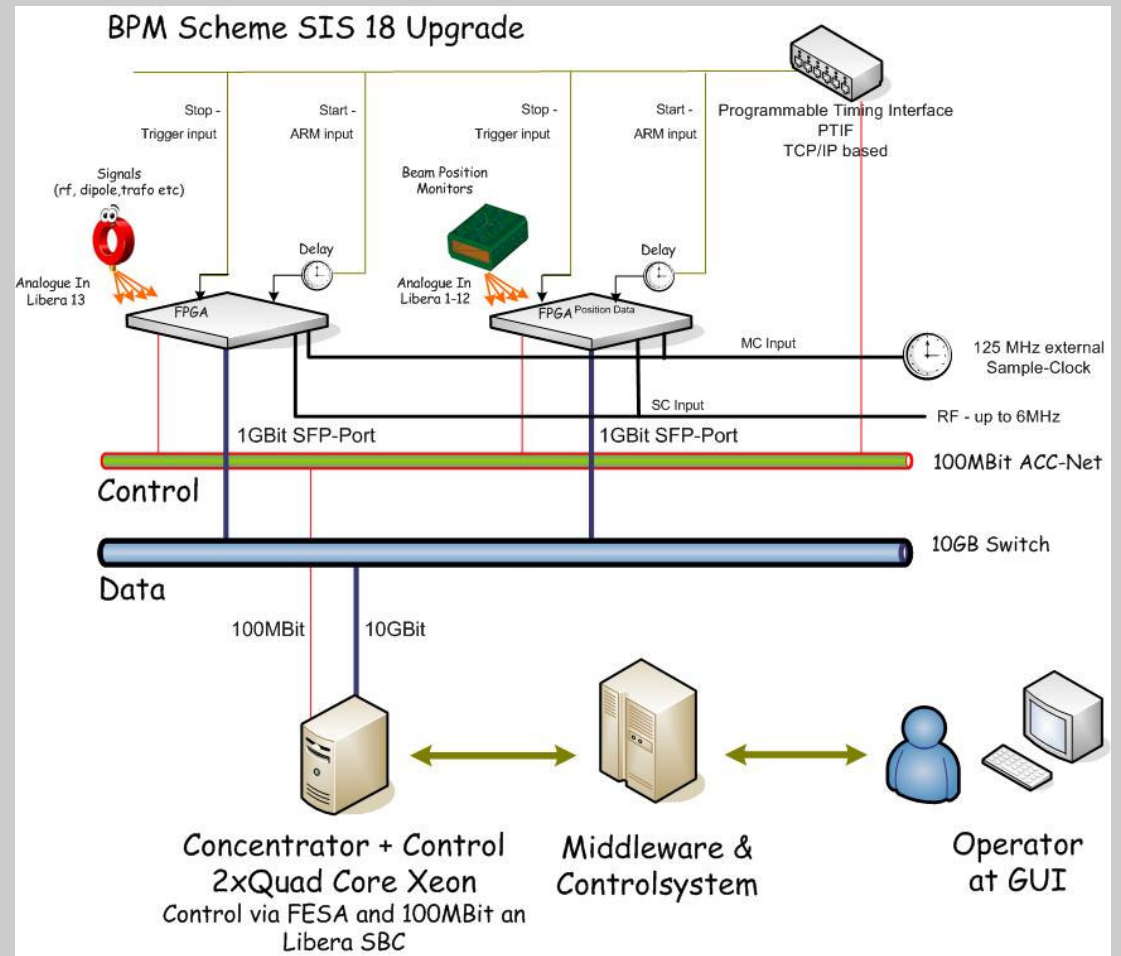
# FESA Test Project

## SIS18 Bunch-to-bunch BPM system

### Objective:

Check complete FESA at GSI

- 12 Libera Hadron (I-Tech)
- 10Gbit network
- Installation of application level (Java-GUI)
- Functional and performance checks of complete hierarchy
- Porting FESA and Timing Receiver Library to 64Bit





## Why FESA?

- CERN is similar to FAIR  
(parallel beam operation and heavy ions)
- Already there, experiences can be collected
- Not only Control- but Data Acquisition System  
(beam diagnostics fully integrated)
- All other advantages on the poster!!

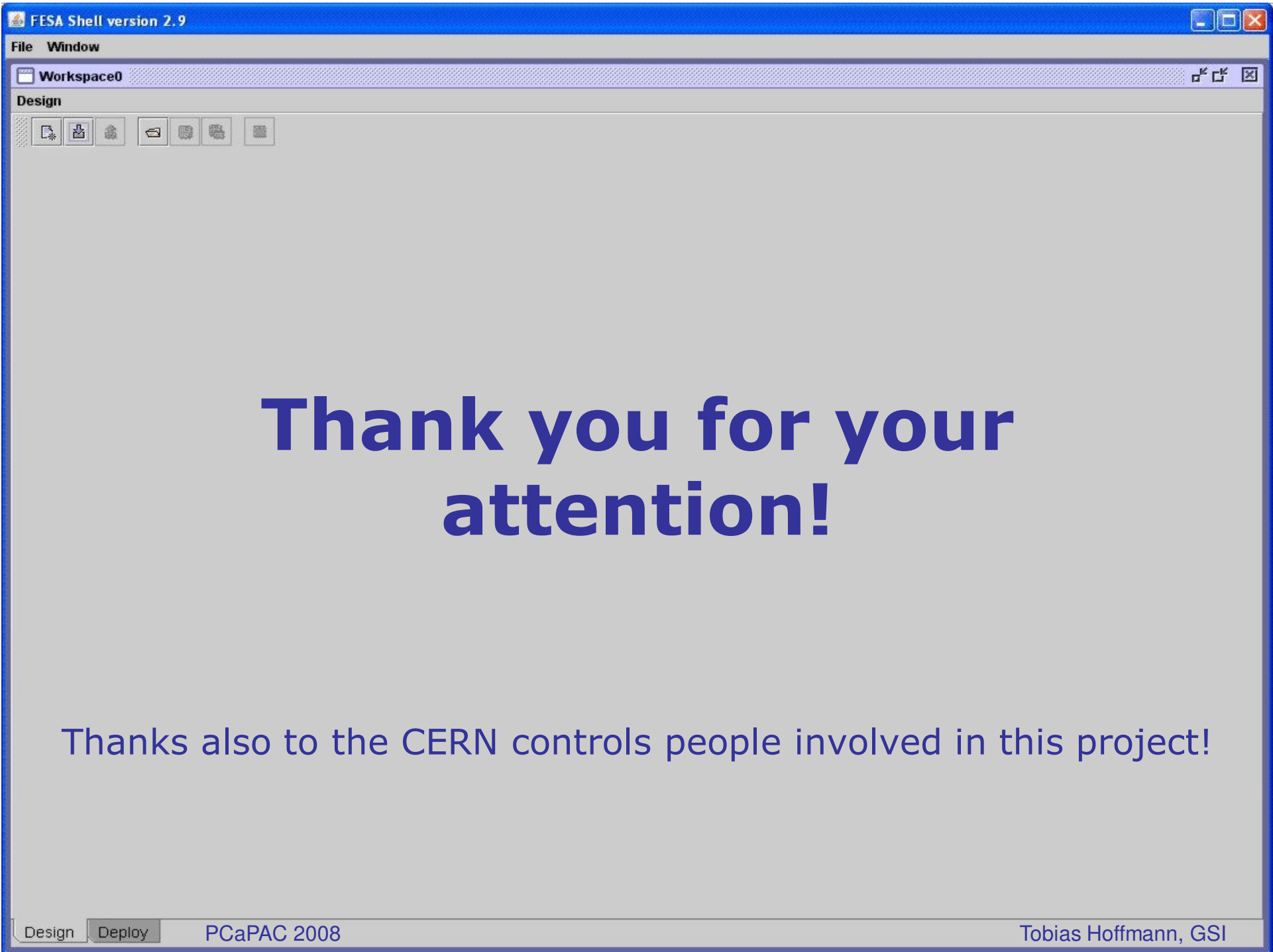


# Outlook

- Test project ends Spring 2009
- Installation of application level (GUI)
- Evaluation of other control system parts (Timing, LSA, Unicos)
- Creation of a FESA competence centre at GSI

Spend time and energy on

**Learning Learning Learning!**



**Thank you for your  
attention!**

Thanks also to the CERN controls people involved in this project!