STORAGELESS RESONANT CONVERTER FOR ACCELERATOR MAGNETS

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Abstract

Elettra Sincrotrone Trieste, a specialized research centre generating high quality synchrotron radiation, has been in operation since 1993 and was revised in 2009. Recently, Elettra has been funded for a complete renewal of the storage ring. For the new machine, it is planned to employ state of the art converters, mostly of which will be designed in-house.

For this purpose, it has been decided to evaluate the performance of a storage-less resonant converter, proposed by Dr. Slobodan Ćuk, which is a step down DC/DC converter consisting of four switches, one resonant capacitor and two resonant inductors. For this purpose, the voltage conversion ratio of the converter has been derived. The topology was confirmed with simulation and a PCB layout has been designed, which is still to be tested.

ELETTRA SINCROTRONE TRIESTE

Low current magnets have been designed for the new 6BA design of the storage ring, so that each one will be independently energized. With a larger number of power supplies, the main goal is to get high efficiency to keep the facility environmentally friendly and raise the MTBF by lowering the thermal stresses on active devices. The following converter is fulfilling our two requirements.

STORAGELESS RESONANT CONVERTER

The examined storage-less resonant converter proposed by Dr. Slobodan Ćuk [1], illustrated in Fig. 1.a, mainly relies on the energy transfer by resonance, instead of storing energy in inductors such as in buck converters.

As it can be seen in Fig. 1, the converter is composed of four switches (Q1 to Q4), two resonance inductors (Lr1 and Lr2), one resonant capacitor (Cr) and one output filter capacitor (Co).

The basic operation principle of this converter is to charge the resonant capacitor (Cr) by means of a resonance circuit formed with one of the inductors and to discharge the same capacitor through the other resonant inductor. This way, the transferred energy does not need to be stored in an inductive element, such as the case of a buck converter.

Although the structure is similar to switched capacitor converters [2], the addition of resonant inductances changes the operation of the converter and requires two of the switches, namely Q3 and Q4, to operate like diodes. This also in return, enables the converter to operate at higher efficiencies for a wider range.

Basic Operation Principle

The operation principle of the converter will be explained over the simplest case, where resonance inductors have the same value, \( L_{r1} = L_{r2} \), switching frequency is equal to the resonance frequency, \( f_r \), and the duty cycle, \( D \), is equal to 50%. Here \( D \) is defined as

\[
D = \frac{T_1}{T_f}
\]

Where \( T_1 \) is the time interval where the switches Q1 and Q3 are conducting.

As it can be seen in Fig. 1.a and Fig. 1.b, there are two main operational states of the converter. In the first state, the resonant capacitor, \( C_r \), is charged through the switches Q1 & Q3 and \( L_{r1} \). In this state, the current is supplied by the input voltage source and \( C_r \) is being charged.

In the second state, the switches Q1 & Q3 are turned off, while the switches Q2 and Q4 are turned on. In this state, the power for the load is provided by the resonant capacitor, which is being discharged. As seen above, the switch pairs Q1,3 and Q2,4 are operated complementary to each other.

![Figure 1: (a) Storage-less Resonant Converter and operation intervals of the converter; while (a) resonant capacitor is charging and (b) is discharging.](image)
For mentioned states, the following voltage equations can be written, which are basically second order ordinary differential equations:

\[
V_g - V_o = V_{cr} + V_{Lr1} = \frac{1}{C_r} \int i_{Lr1} dt + L_{r1} \frac{di_{Lr1}}{dt} \quad (2)
\]

\[
V_o = V_{cr} - V_{Lr2} = \frac{1}{C_r} \int i_{Lr2} dt - L_{r2} \frac{di_{Lr2}}{dt} \quad (3)
\]

The solutions for these equations are sinusoidal waveforms, which are as follows:

\[
i_{Lr1}(t) = \frac{(V_g - V_o)}{\sqrt{L_{r1}/C_r}} \sin(\omega_1 t) \quad (4)
\]

\[
i_{Lr2}(t) = \frac{V_o}{\sqrt{L_{r2}/C_r}} \sin(\omega_2 t) \quad (5)
\]

Where,

\[
\omega_1 = \frac{1}{\sqrt{L_{r1}C_r}} \quad (6)
\]

\[
\omega_2 = \frac{1}{\sqrt{L_{r2}C_r}} \quad (7)
\]

The current waveforms on the resonant inductors are illustrated in Fig. 2. As it can be seen, the current waveforms are sinusoidal and the magnitudes of the currents at switching instances are equal to zero, which enables soft switching Zero Current Switching (ZCS) for all four switches.

Constant Off-Time Variable On-Time Control

The operation of the converter can be extended to duty cycles that are different than 50%, which in turn governs the output voltage. However, it is obvious that if the switches are turned on/off with a period different than the resonant frequency, the ZCS feature will not be achieved anymore. Thus, the efficiency will decrease.

For maintaining the ZCS feature as much as possible, it is better to use a constant off-time and variable on-time control strategy, where the on time of the switches Q2 and Q4 are fixed to be equal to the half of the second resonance period T2 and the on-time of switches Q1 and Q3 are varied.

\[
T_2 = 2\pi/\omega_2 \quad (10)
\]

This way, the switches Q2 and Q4 always operate at ZCS turn-on and ZCS turn-off. Moreover, it is also obvious that for preventing any abrupt change of current on Lr1, the Q3 has to behave like a diode. Thus, it has been connected in a reverse way, so that the body diode will always ensure diode operation. The current waveforms for this operation are shown in Fig. 3.

Utilizing the constant-off time strategy, switches Q1 and Q3 still maintain the ZCS turn-on operation, whereas the switch Q1 has to turn off while it is conducting a high amount of current.

![Resonant inductor current waveforms at D = 0.5.](image)

Figure 3: Resonant inductor current waveforms at D = 0.5.

The analysis of the converter under constant off-time control results in the following polynomial equation as the expression of output voltage:

\[
V_o^2 = \frac{V_o^2}{4} \left[ 4.K - \frac{4.A.C_r}{\left(1 - A/2\right)} \frac{T_o}{R} \right] + V_o^2 \left[ \frac{2.A.C_r.V_o}{\left(1 - A/2\right)} - 4.V_o.K \right] + V_o^2 \cdot K \quad (11)
\]

Where \(T_o=T_1+T_2/2\) is the switching period,

\[
K = \frac{\sin(\omega_1 T_o)^2}{(1-A/2)} \cdot \frac{C_r.L_{r2}}{2.L_{r1}} \quad (12)
\]

\[A = 1 - \cos(\omega_1 T_o)\]
The solution of the quadratic polynomial function for the case of \( L_{r1}=L_{r2} \) (meaning \( f_1=f_2=f_r \)), at various loads are illustrated in Fig. 4. As given previously, here the duty cycle is \( D=T_{1}\cdot f_r \), and normalized output voltage is \( V_{on} = V_o/V_g \).

**SIMULATIONS**

According to the parameters in Table 1, a simulation model, shown in Fig. 5, has been built via PSIM 11, for checking the behaviour of the converter.

To ensure low current ripple at the input and output, it has been decided to employ an LC filter at the input and Pi filter at the output.

**Table 1: Designed Circuit Parameters**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Voltage, ( V_g )</td>
<td>48 Volts</td>
</tr>
<tr>
<td>Max. Output Voltage, ( V_{out} )</td>
<td>24 Volts</td>
</tr>
<tr>
<td>Max. Output Current, ( I_{out} )</td>
<td>60 Amperes</td>
</tr>
<tr>
<td>Resonance Frequency, ( f_{res} )</td>
<td>159 kHz</td>
</tr>
<tr>
<td>Resonant Capacitor, ( C_r )</td>
<td>5 ( \mu F )</td>
</tr>
<tr>
<td>Resonant Inductor 1, ( L_{r1} )</td>
<td>200 nH</td>
</tr>
<tr>
<td>Resonant Inductor 2, ( L_{r2} )</td>
<td>200 nH</td>
</tr>
<tr>
<td>GaNFETs</td>
<td>EPC2020</td>
</tr>
<tr>
<td>Gate Driver</td>
<td>Si8274</td>
</tr>
<tr>
<td>Synch. Rect. Controller</td>
<td>UCC24612-1</td>
</tr>
</tbody>
</table>

**PCB DESIGN**

The chosen GaNFET is particularly small and adds some manufacturing issues for the width of the pads (270 \( \mu m \)) which are replicated on all 8 layers of 35 \( \mu m \) thickness PCB, shown in Fig. 6. Vias with the hole of 100 \( \mu m \) are needed on the pads to split the current (60 Amps) flowing on the drains of the transistors. The overall size of this converter is 165 x 75 mm (including the 30 mm gap for current probe) for a 1400 W device. Two of the GaNFETs are controlled with synchronous rectifier controllers so that they work like diodes. Thermal losses are predicted to be maximum 30 W.

The PCB has been employed with multi-layer ceramic capacitors only for size shrinking. The used inductors are quite small, since they are only 200 nH.

**REFERENCES**
