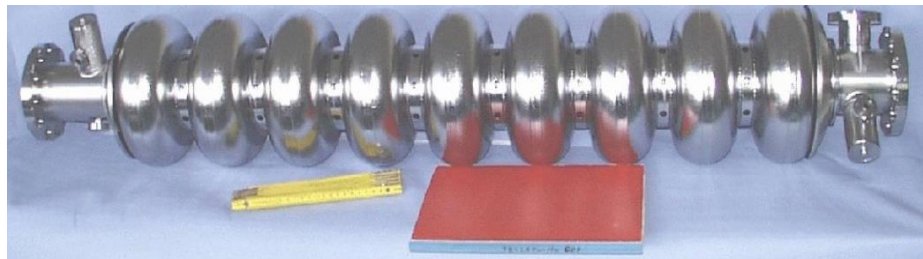
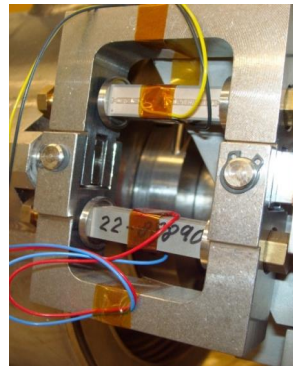


# MicroTCA.4 based Single Cavity Regulation

including Piezo Controls

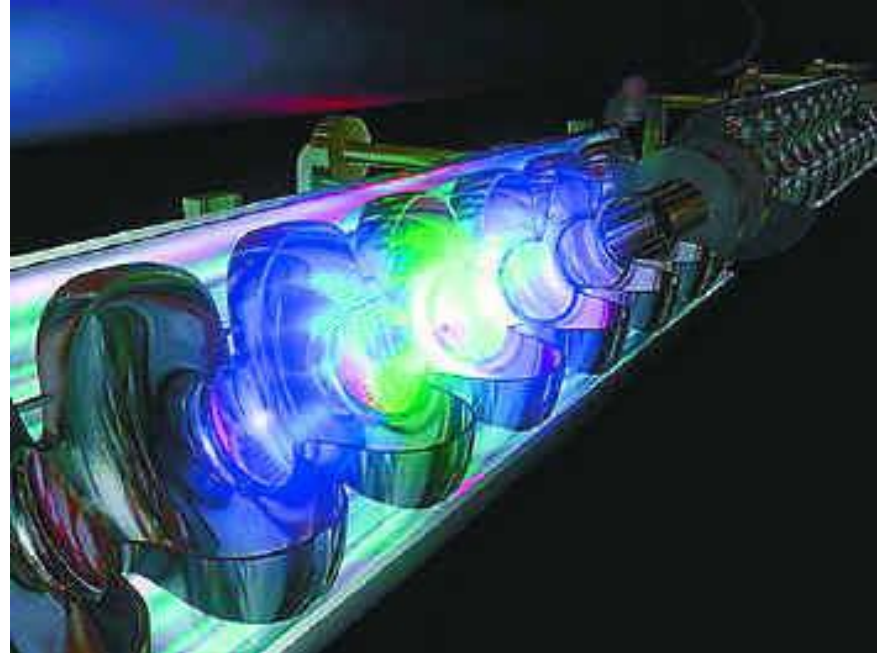


Dr. Konrad Przygoda

Busan, Korea, 08-13.05.2016  
on behalf of MSK Group, DESY,  
Warsaw University of Technology  
and Helmholtz Zentrum Berlin

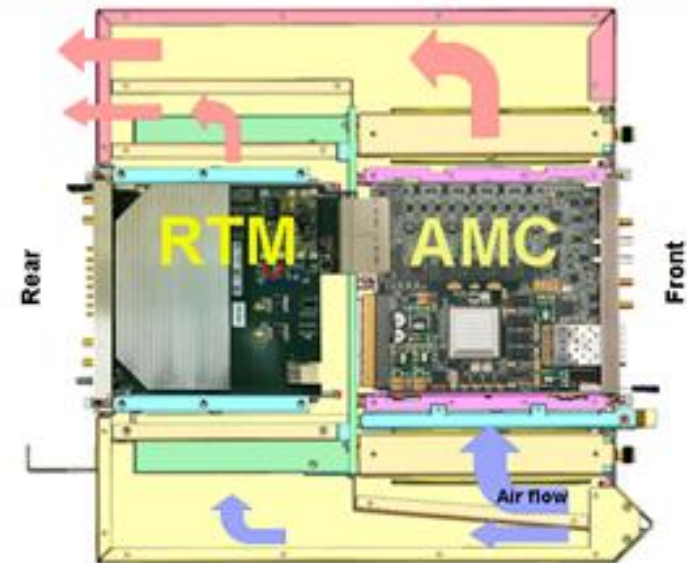
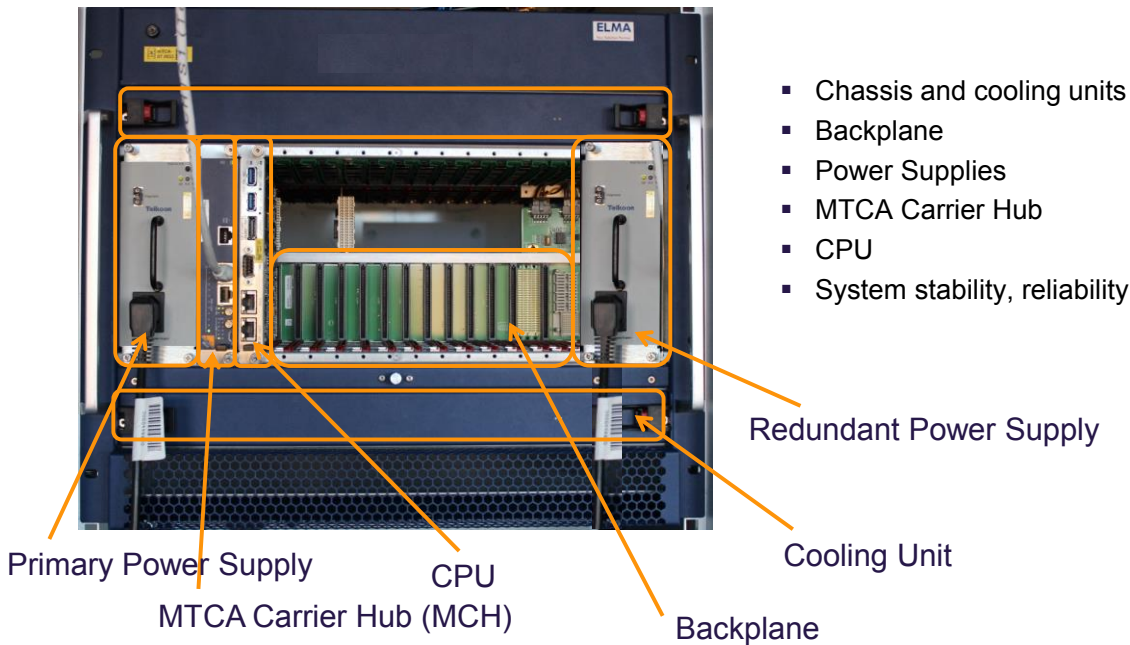
# Outline

- MTCA.4 Standard
- DESY developments
- RF Cavity
- Firmware
- Software
- Why not CW?
- CW experiment
- Future plans



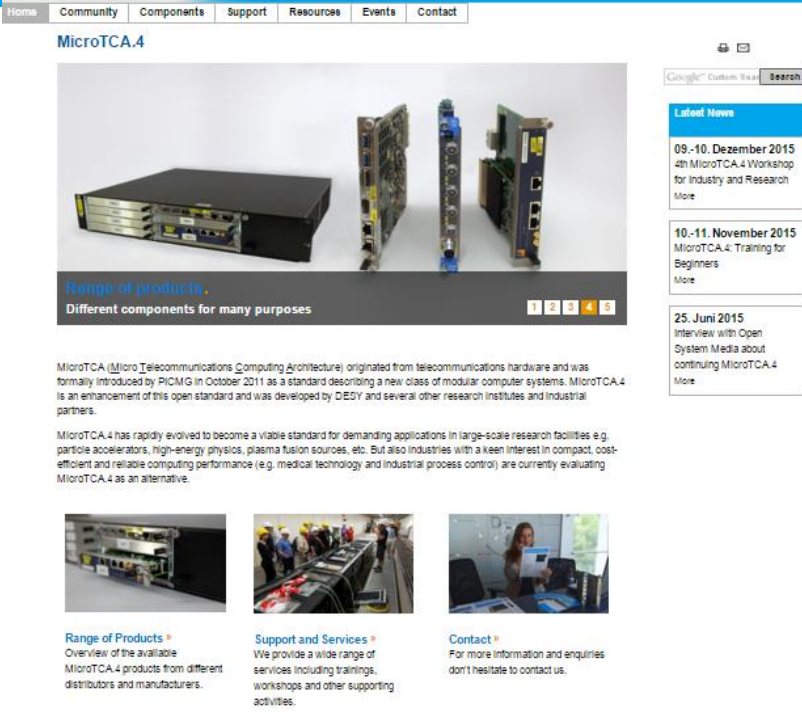
# MTCA.4 Standard

- **Modular + modern architecture**
  - Reusability + PCIe + Ethernet
- **High availability**
  - Redundant power, MCH and CU
  - Well defined remote management
  - Hot plug support
- **High digital and analog performance**
  - Very low analog distortions (diff.lines)
  - 4 lanes PCIe (Gen3): 1 GByte/s/lane



- > FMC carriers:
  - DAMC-FMC20
  - DAMC-FMC25
- > FMC modules:
  - DFMC-MD22
  - DFMC-SFP4
  - DFMC-AD16
  - DFMC-UNIO
- > Backplanes:
  - RF Backplane

- > AMC
  - DAMC-TCK7
  - DAMC-DS800
- > RTM
  - DRTM-DWC10
  - DRTM-DWC8VM1
  - DRTM-DS8VM1
  - DRTM-VM
  - DRTM-uLOG
  - DRTM-AD84
  - DRTM-PZT4



The screenshot shows the MicroTCA.4 website with a navigation menu (Home, Community, Components, Support, Resources, Events, Contact) and a main content area. The main content features a large image of MicroTCA.4 hardware with the heading "Range of products" and the subtext "Different components for many purposes". Below this, there are three columns of text: "Range of Products" (overview of available products), "Support and Services" (training, workshops, etc.), and "Contact" (information and enquiries). On the right side, there is a "Latest News" section with three entries: "09.-10. Dezember 2015 4th MicroTCA.4 Workshop for Industry and Research", "10.-11. November 2015 MicroTCA.4 Training for Beginners", and "25. Juni 2015 Interview with Open System Media about continuing MicroTCA.4".

**15 boards developed (industry licensed or direct sale),  
several more under development**



# RF Cavity

## > RF cavity parameters (i.e. XFEL):

- Resonance frequency  
 $f_0 = 1.3 \text{ GHz}$
- Loaded quality factor range  
 $Q_L \approx 3e6 \div 1.5e7$
- Bandwidth range  
 $B.W \approx 433 \div 87 \text{ Hz}$
- Accelerating gradient range  
 $E_{acc} \approx 3 \div 42 \text{ MV/m (DESY record!)}$
- Fine tuning with piezos ( $C_L \approx 4 \mu\text{F}@2\text{K}$ )
- Coarse tuning with motorized stage  
**Sanyo Denki**



## > Goals:

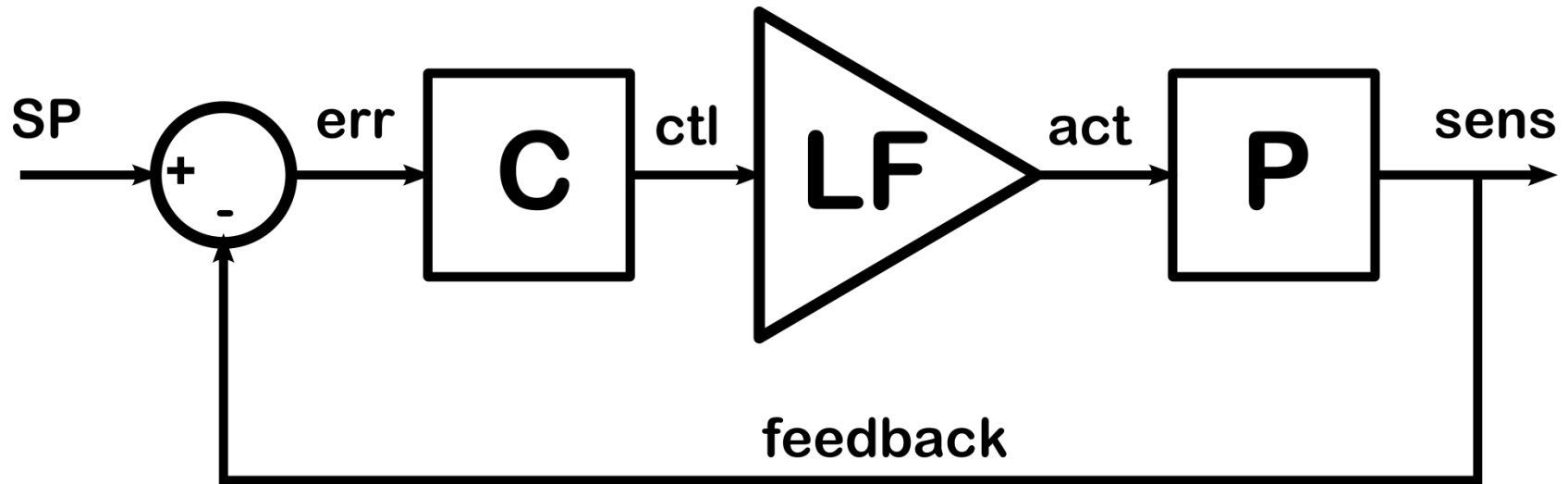
- Sense 1.3 GHz RF signals and drive 1.3 GHz high voltage RF source
- Stabilize RF field  
( $dA/A > 0.01\%$ ,  $dP \sim 0.01 \text{ degrees}$ )

## > Problems:

- Lorentz force detuning ( $\Delta f < 1 \text{ kHz}$ ), **SP mode**
- Microphonics ( $\mu < 10 \text{ Hz}$ ), **CW mode**

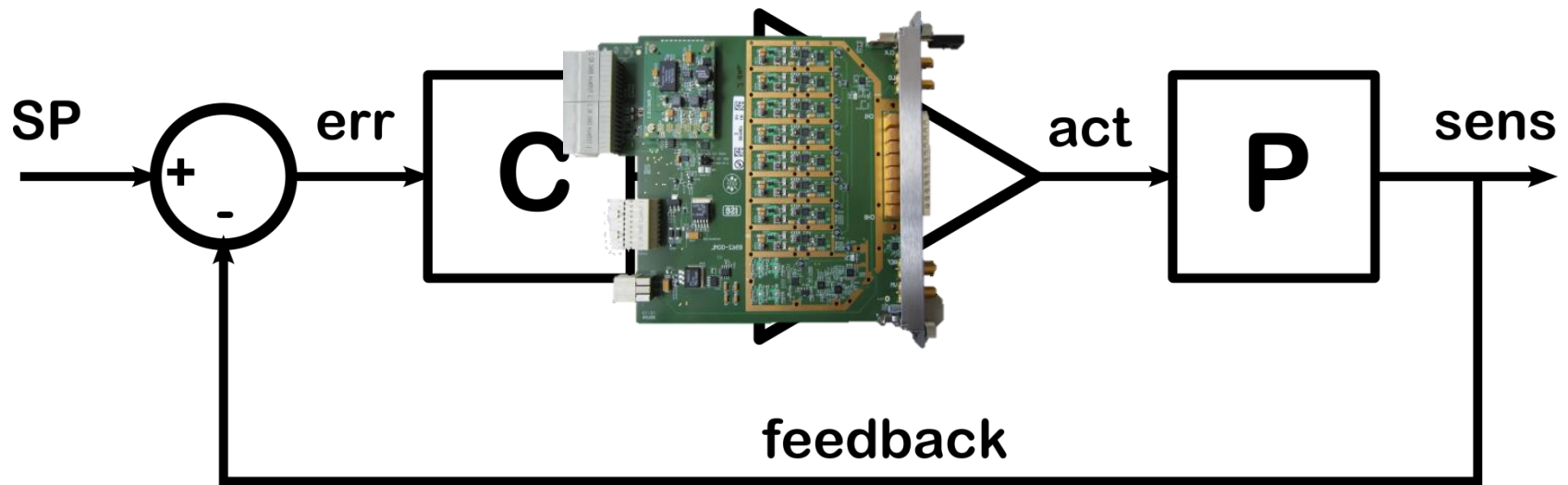
# RF Cavity Controller Design

## > Control Theory Point of View



# RF Cavity Controller

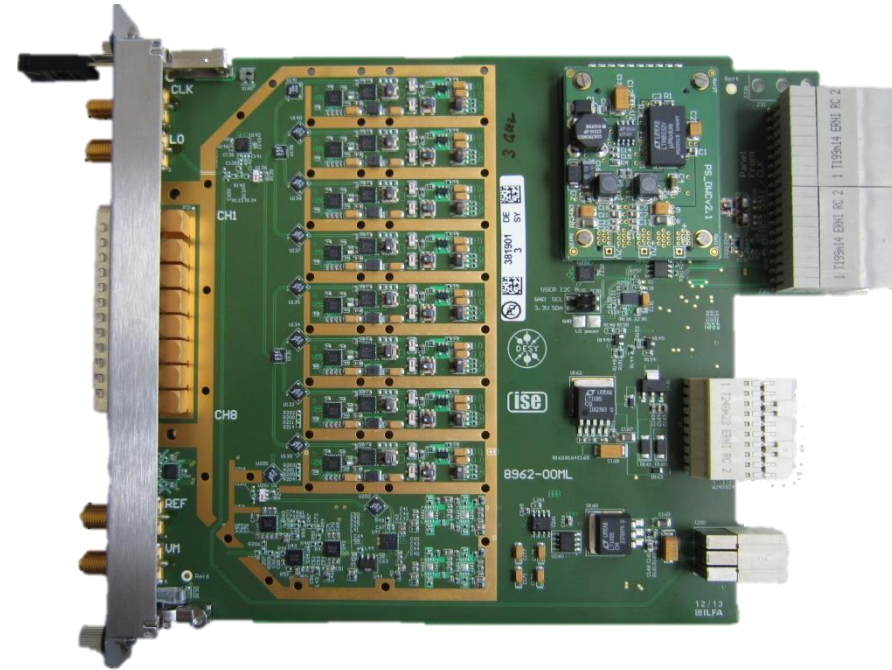
- RTM sensor and actuator



# MTCA.4 Electronics: RTM

## 8 Channel Down-Converter 1 channel Vector-Modulator

- Double width MTCA.4, Mid-Size Rear-Transition Module (RTM), Class A1.0, A1.1, A1.2 compatible
- Features:
  - 8 down-conversion input channels (AC) with programmable attn.
  - LO input for analog down-conversion 1.3 GHz
  - 2 analog general purpose inputs (DC)
  - 1 up-conversion output channel (AC) with programmable attn.
  - REF input for analog up-conversion 1.3 GHz
  - ADC clock input (AC) up to 125 MHz
  - Interlock signal support



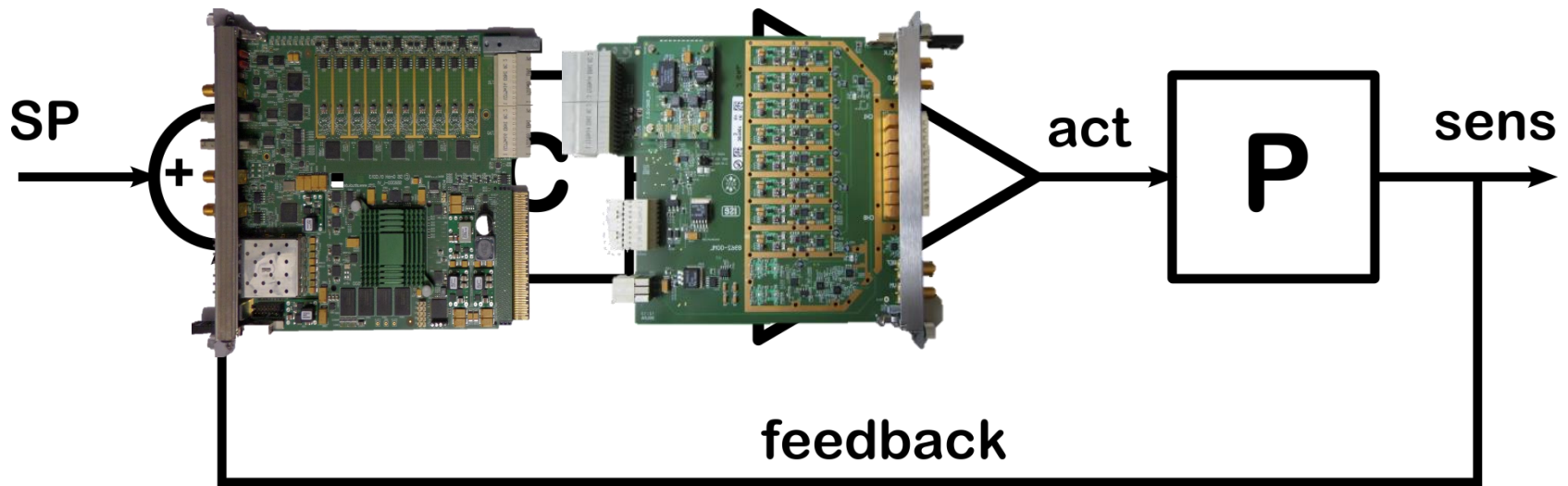
DRTM-DWC8VM1

licensed by Struck Innovative Systems



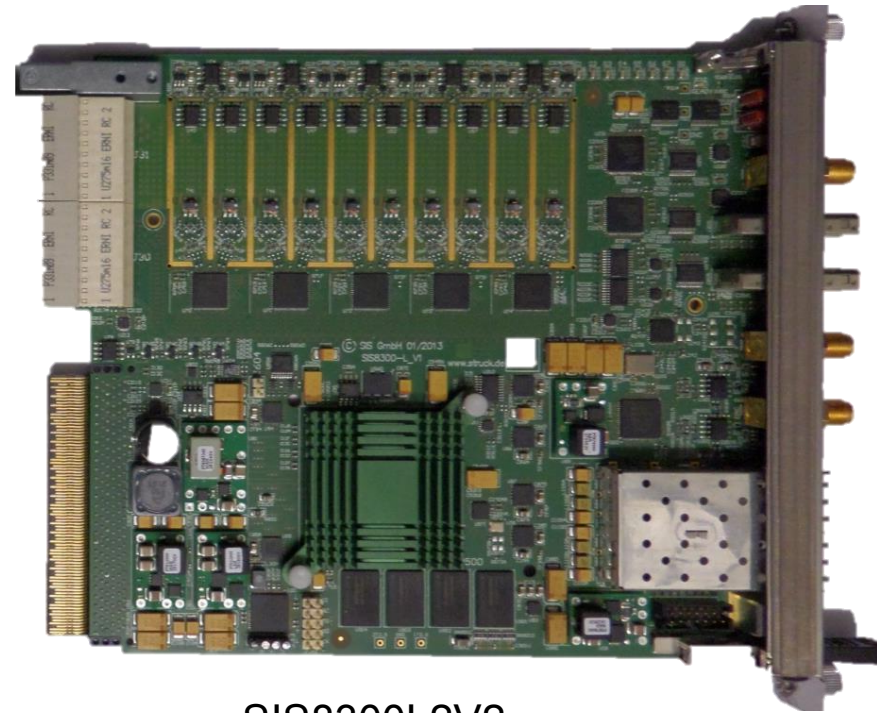
# RF Cavity Controller

- AMC data processor



# MTCA.4 Electronics: AMC Fast Digitizer

- Double width MTCA.4, Mid-Size Advanced Mezzanine Card (AMC), Class A1.0, A1.1 compatible
- Features:
  - 10x Analog Inputs: ADC 125 MSPS
  - 2x Analog Outputs: DAC 125 MSPS
  - RTM linked to Virtex 6 FPGA
  - RTM hotplug support
  - PCIe 4x => Virtex 6 FPGA
  - 6 MGTs (4xLLL + 2x SFP) => up to 10 Gbps AMC backplane connection on ports 12-15
  - Interlock signal support

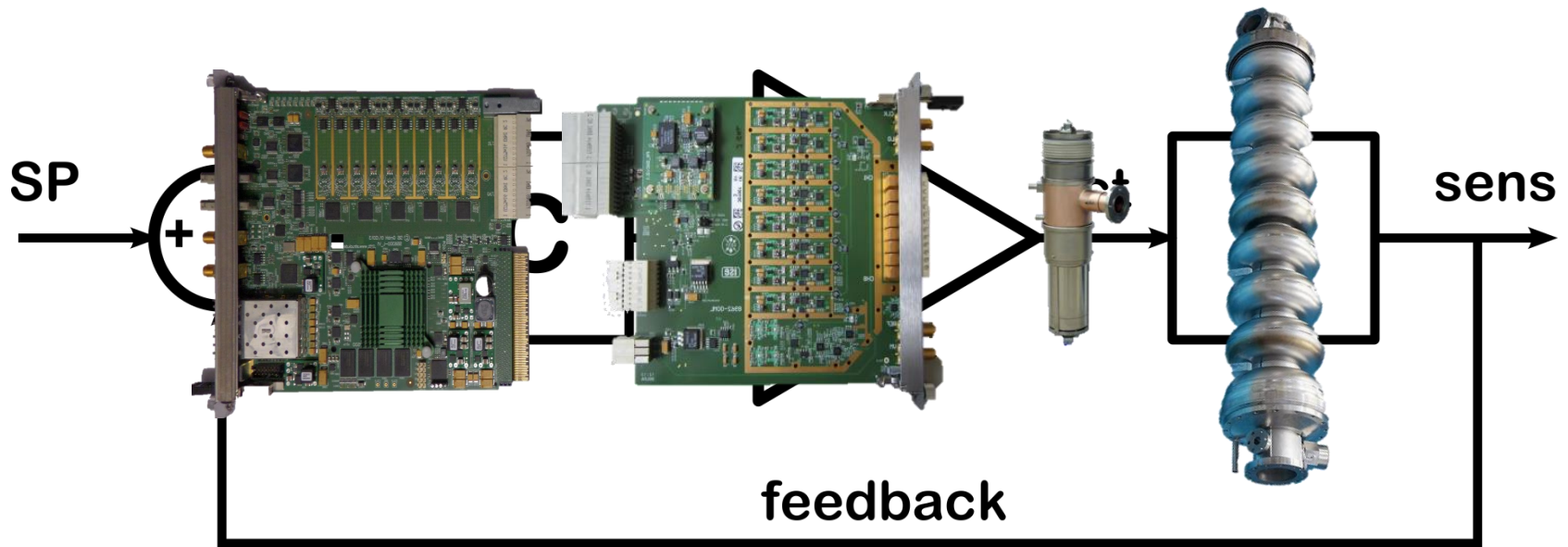


**SIS8300L2V2**

Struck Innovative Systems  
with DESY collaboration

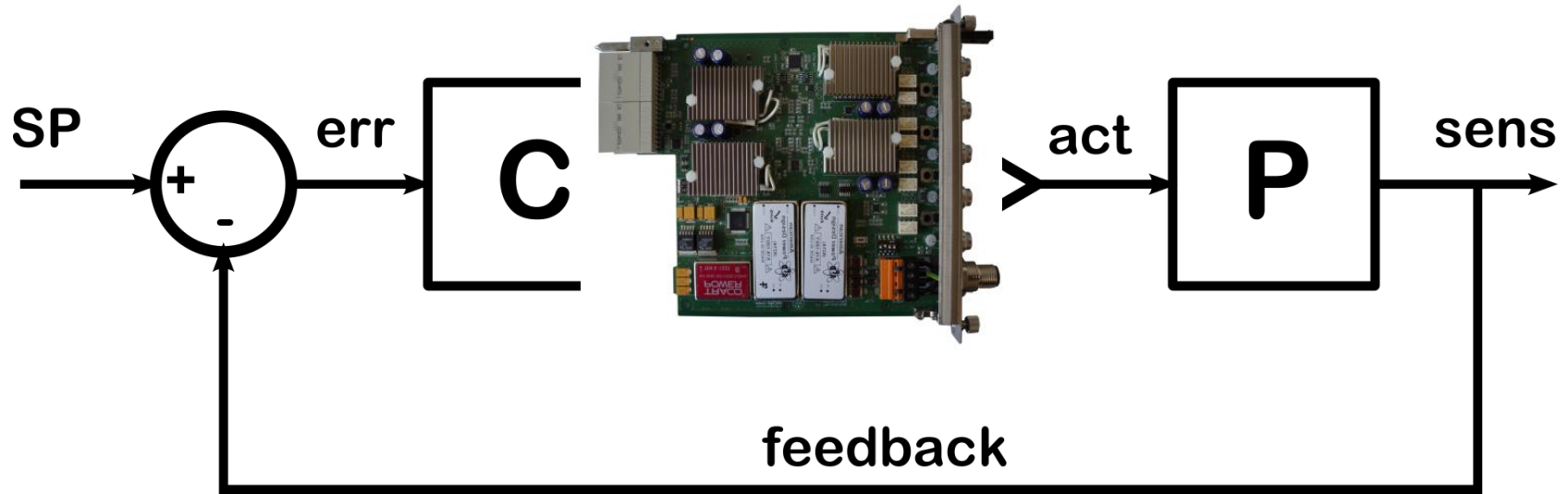
# RF Cavity Controller

- RF cavity with IOT



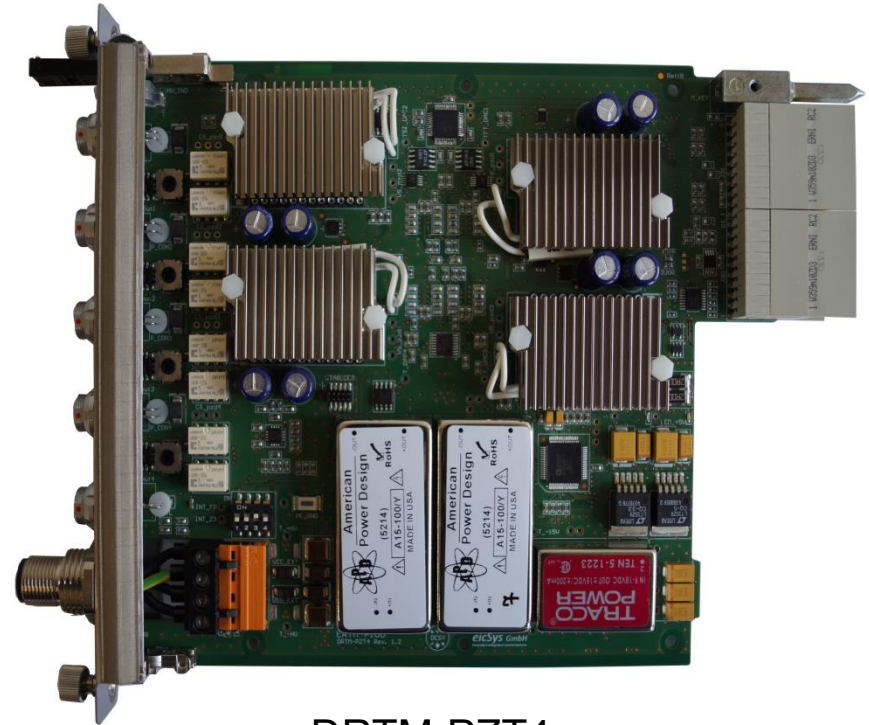
# RF Cavity Detuning Controller

- RTM sensor and actuator



# MTCA.4 Electronics: RTM 4 Channel Piezo Driver

- Double width MTCA.4, Mid-Size Rear-Transition Module (RTM), Class D1.0, D1.1, D1.2 compatible
- Features:
  - Supports 4-channel Piezo Drivers and Piezo Sensors
  - Remotely switchable actuator and sensor functionality
  - Remotely switchable driving input source (ext./int.)
  - 4x DAC 18-bit up to 0.5 MSPS
  - 16x ADC 18-bit up to 100 kSPS
  - Unipolar: 0..+100 V and bipolar:  $\pm 100$  V piezo power supplies (ext./int.)
  - Interlock signal support

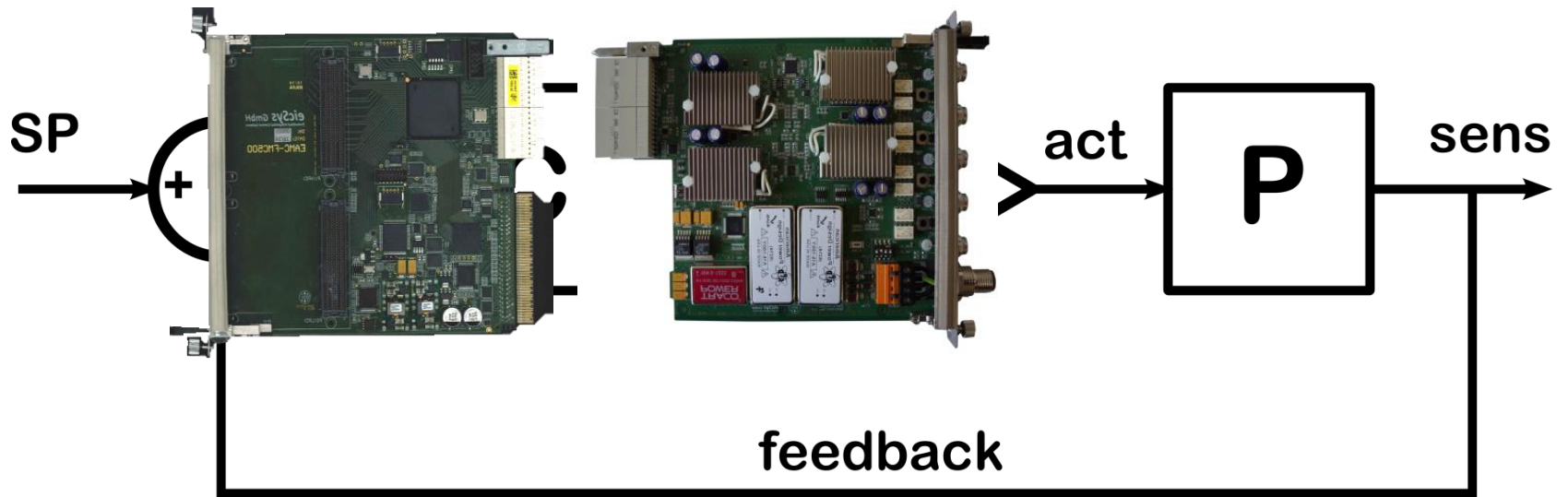


DRTM-PZT4  
direct sale by DESY



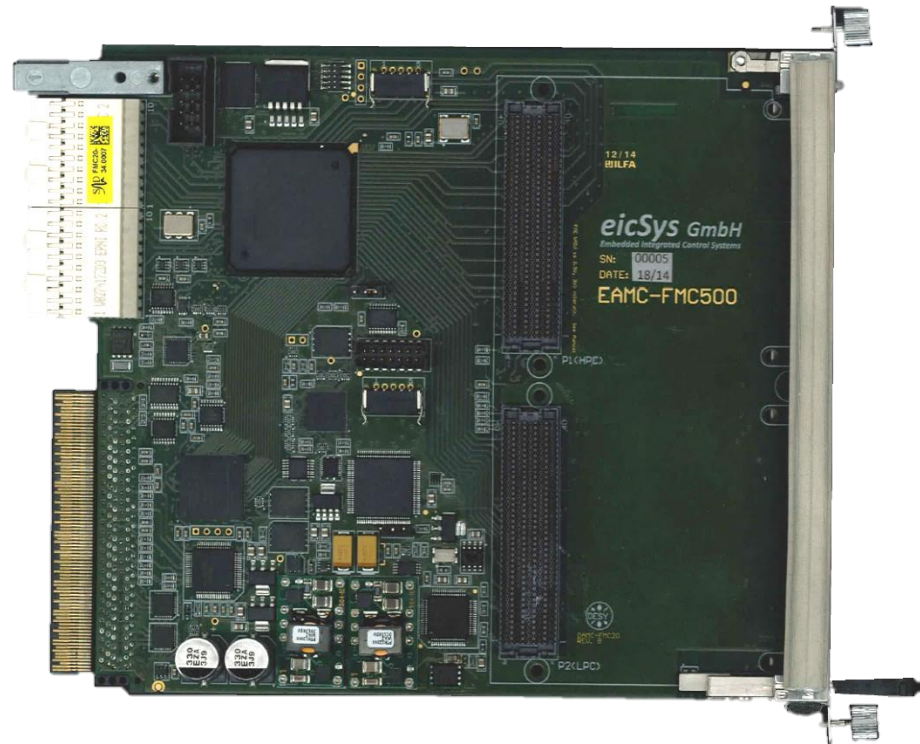
# RF Detuning Controller

- AMC data processor



# MTCA.4 Electronics: AMC Dual FMC Carrier Board

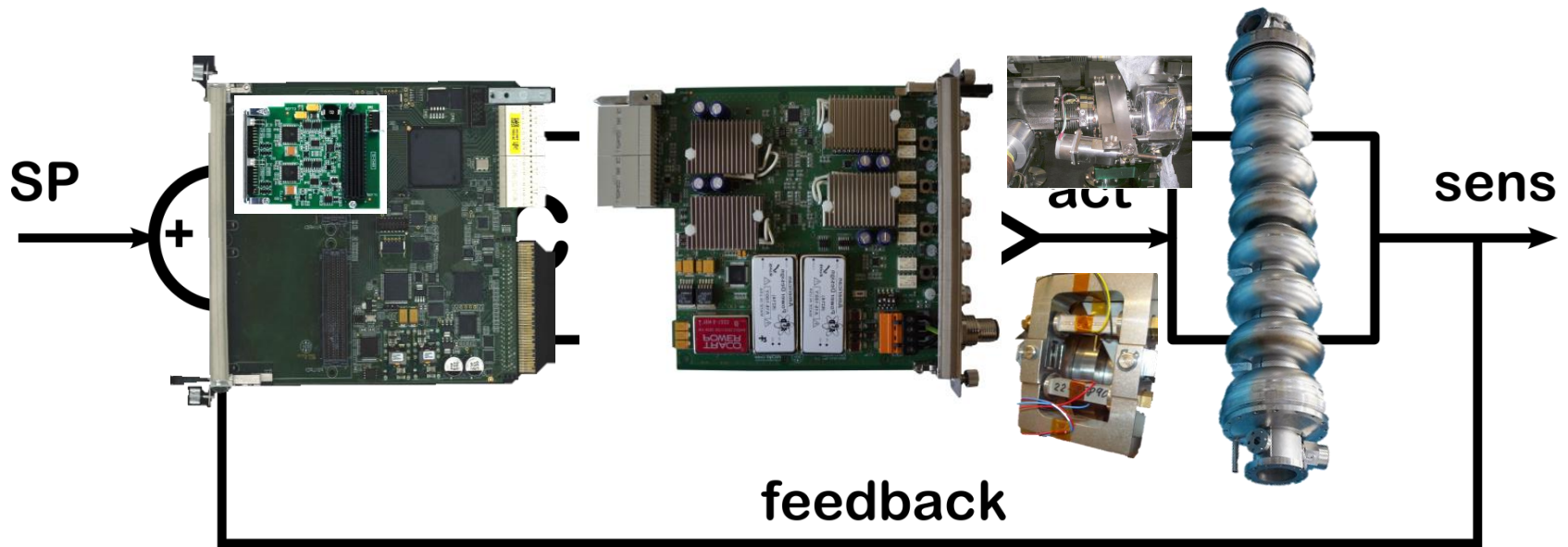
- Double width MTCA.4, Mid-Size Advanced Mezzanine Card (AMC), Class D1.0 compatible
- Features:
  - 1x HPC and 1x LPC FMC linked to Spartan 6 150 FPGA
  - RTM linked to Spartan 6 150 FPGA
  - RTM hotplug support
  - PCIe 1x => Spartan 6 45 FPGA
  - 1x MGT => up to 3 Gbps AMC backplane connection on ports 12-15
  - Interlock signal support



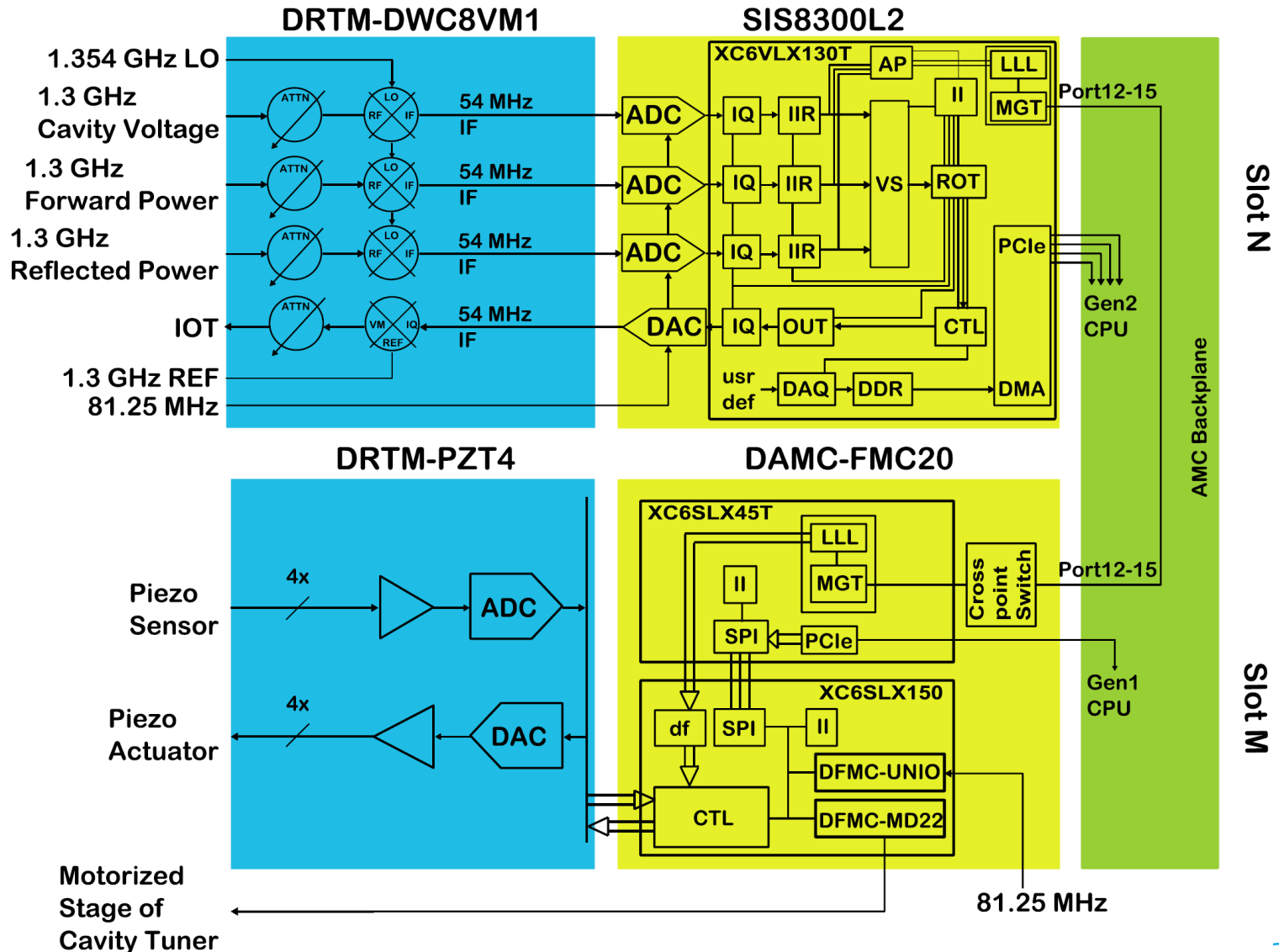
DAMC-FMC20  
licensed by Eicsys

# RF Detuning Controller

## ➤ Cavity Tuner with Piezos



# RF Cavity and Detuning Controller (Firmware)



# RF Cavity and Detuning Controller (Software)

```
rf_c=mtca4u('SISL_6')  
pzt_c = mtca4u('FMC20_5')
```

```
rf_ctl_init(rf_c)
```

```
rf_ctl_ff(rf_c,val)
```

```
rf_ctl_sp(rf_c,val)
```

```
rf_ctl_fb(rc_c,val)
```

The screenshot shows the QtHardMon software interface. The 'Devices' list on the left includes 'SISL\_6' and 'FMC20\_5'. The 'Modules/Registers' list shows various registers, with 'WORD\_TIMING\_FREQ' selected. The 'Register properties' panel displays details for 'WORD\_TIMING\_FREQ', including its address (152), width (32), and values table.

	raw (dec)	raw (hex)	double
0	81250000	0x4d7c6d0	81250000...
1	8	0x8	8.0000
2	8	0x8	8.0000
3	8	0x8	8.0000
4	8	0x8	8.0000
5	8	0x8	8.0000
6	8	0x8	8.0000
7	550	0x226	550.0000
8	0	0x0	0.0000
9	0	0x0	0.0000
10	0	0x0	0.0000

```
piezo_ctl_init(pzt_c)
```

```
piezo_ctl_ff(pzt_c,val)
```

```
piezo_ctl_sp(pzt_c,val)
```

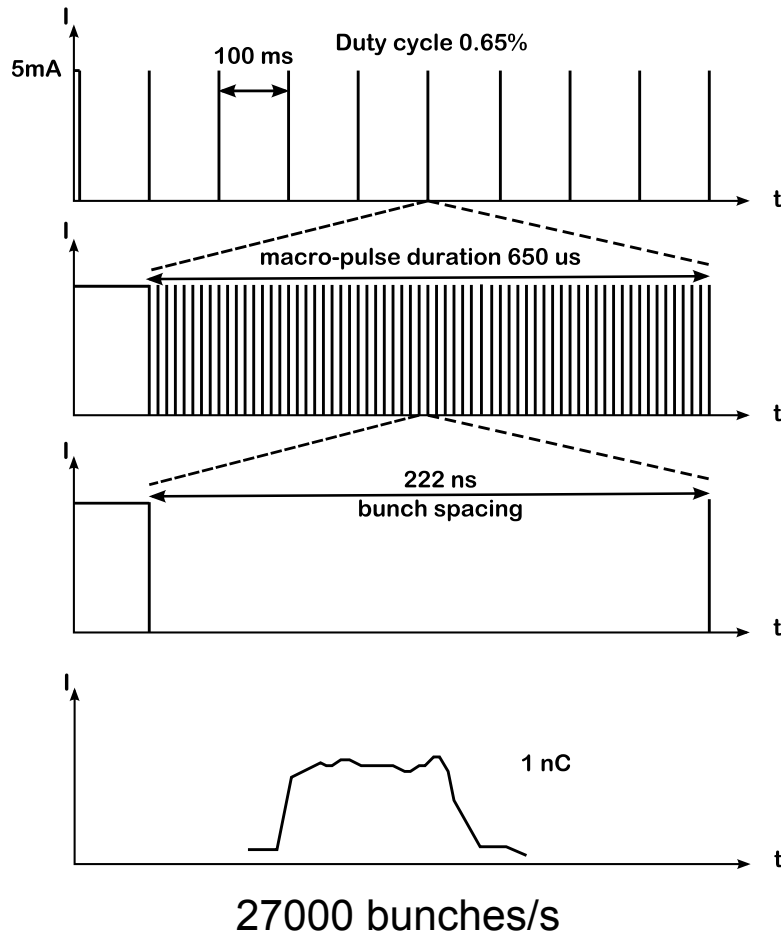
```
piezo_ctl_fb(pzt_c,val)
```

```
[a b] = read_dma_daq({rf_c;pzt_c},no_chan, no_samp, addr)
```

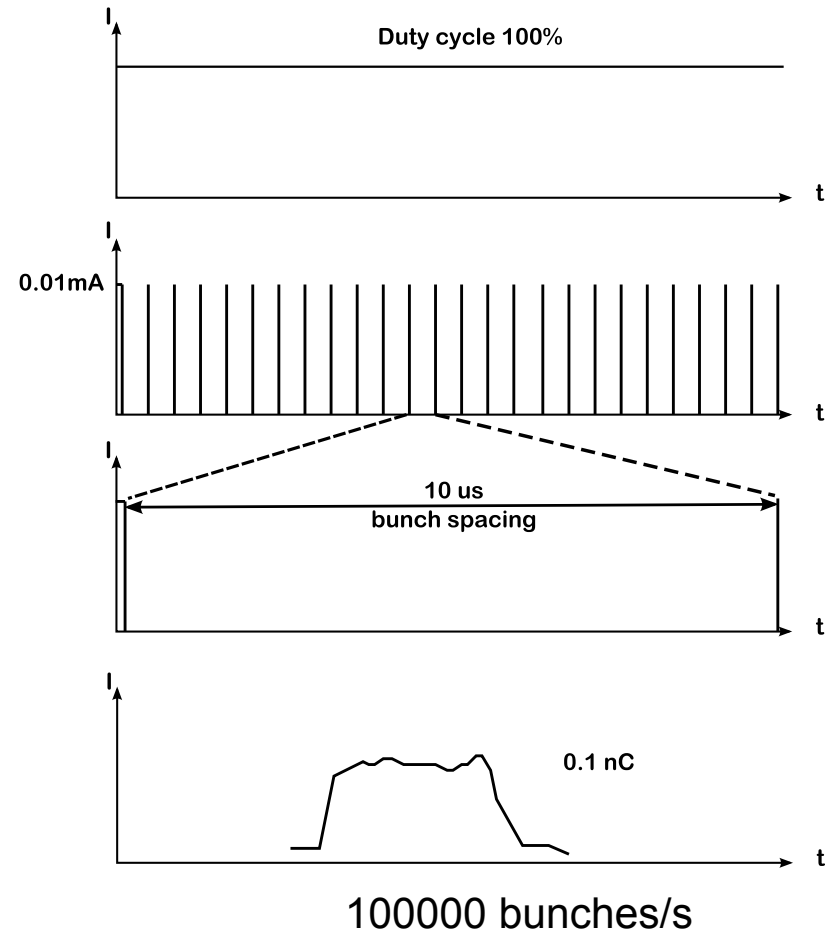


# Why not CW?

## ➤ Short Pulse mode (default)



## CW mode (possible upgrade)

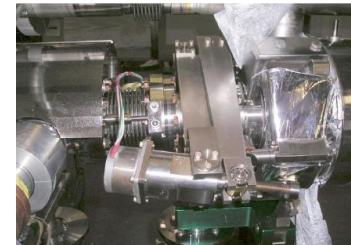


# Cryo Module Test Bench Facility and CW experiment



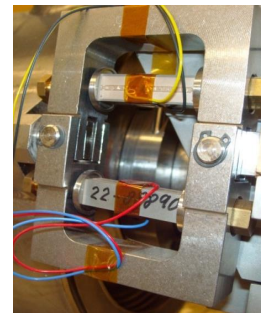
## Environment:

- 1.3 GHz 9-cell SRF cavities
- $Q_L \sim 1.5 \cdot 10^7$  @ 2K
- B.W.  $\sim 87$  Hz
- CW operation up to several MV
- High voltage power source: 120 kW IOT tube
- Cavity mechanical tuner (Saclay II model)
  - Sanyo motorized stage for cavity coarse tuning
  - Physik Instrument piezo elements ( $\sim 4 \mu\text{F}$ ) for cavity fine tuning



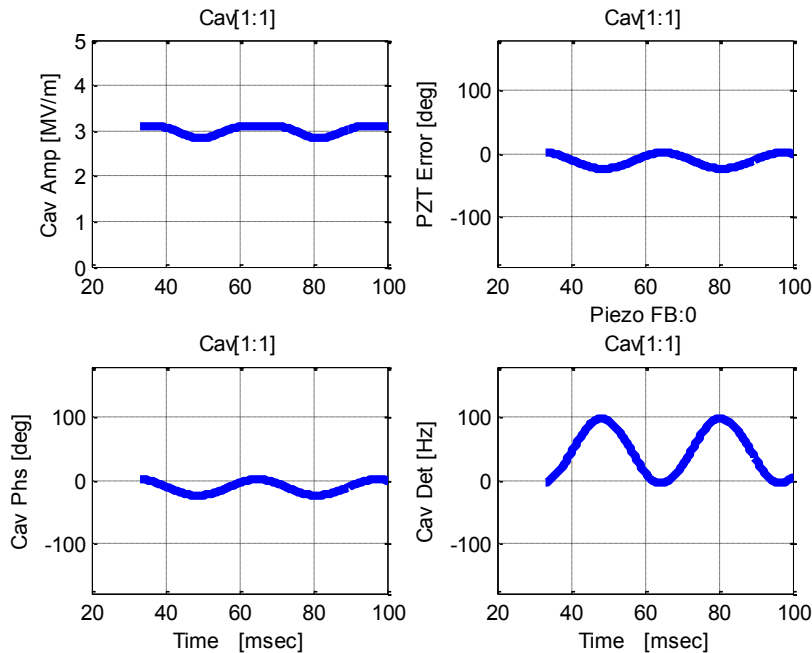
## Goal:

- Stabilize RF field amplitude and phase
- Minimize microphonics effect



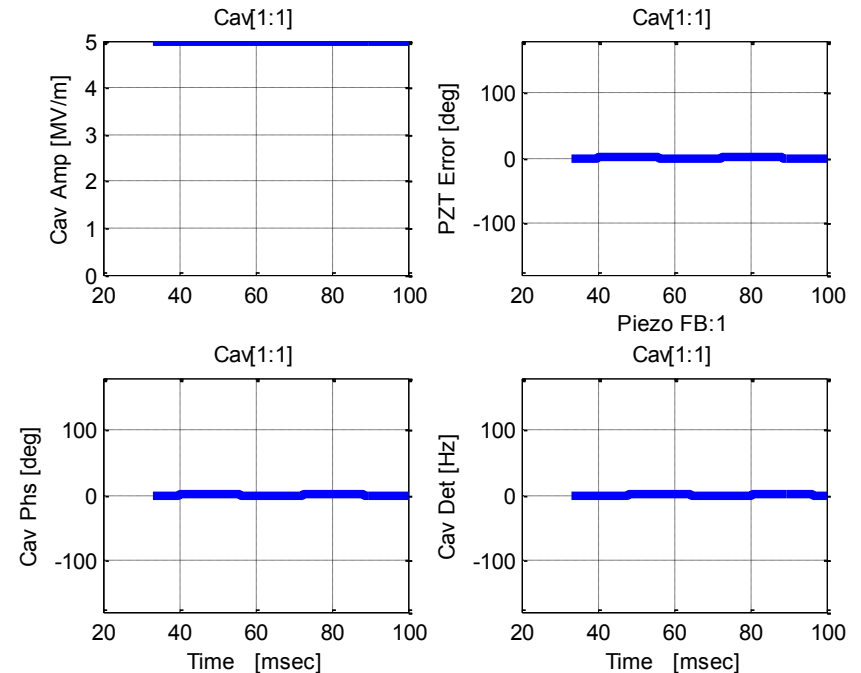
# RF Cavity and Detuning Controller (Simulation)

## > RF/Piezo Feedbacks off



```

for ii=1:Ncav
    if NOISE && dw_m(ii).ena
        det(ii) = det(ii)+ 2*pi*dw_m(ii).ampl*sin(2*pi*dw_m(ii).freq*n*DT);
    end
end
    
```

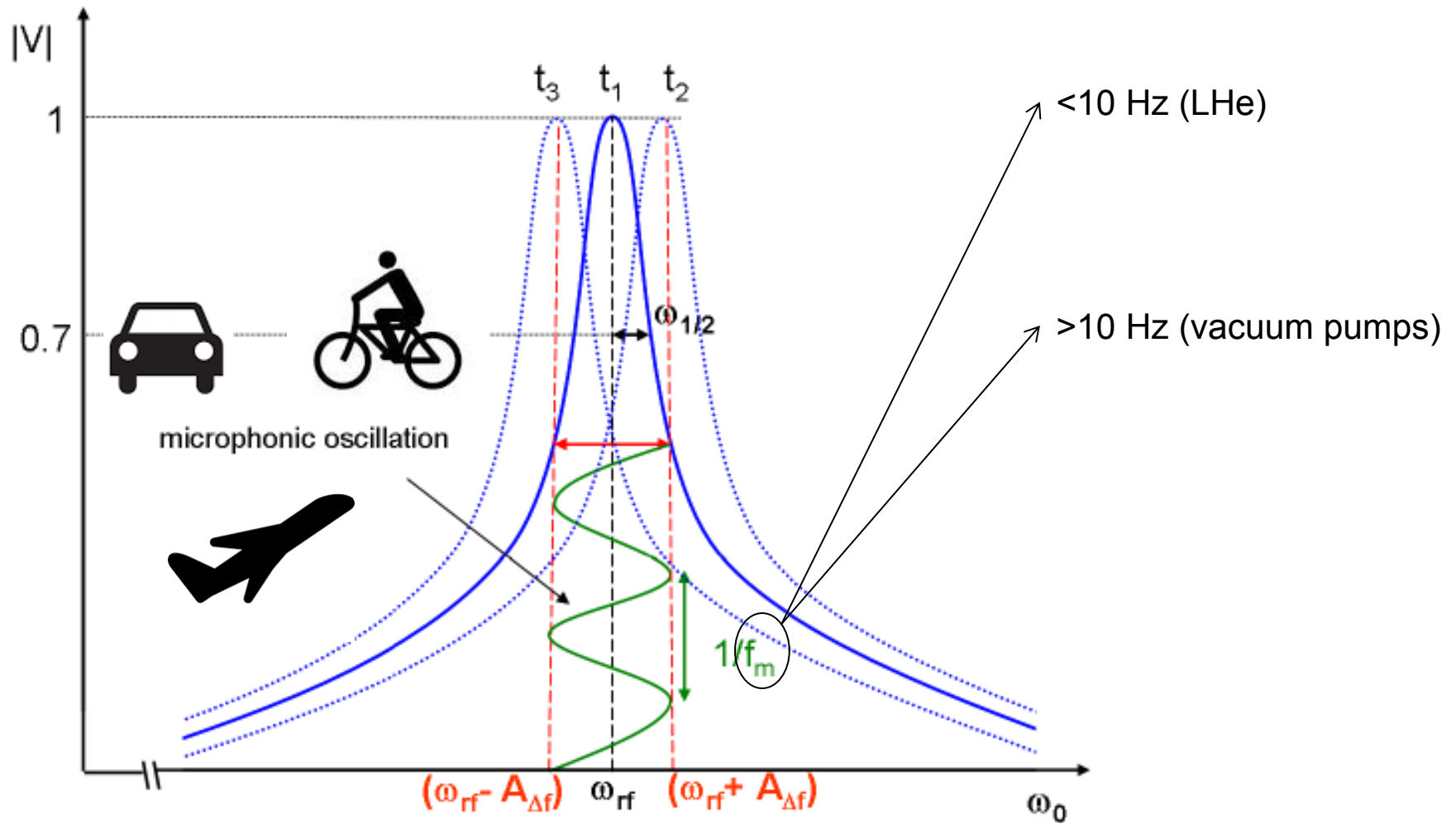


## > RF/Piezo Feedbacks on

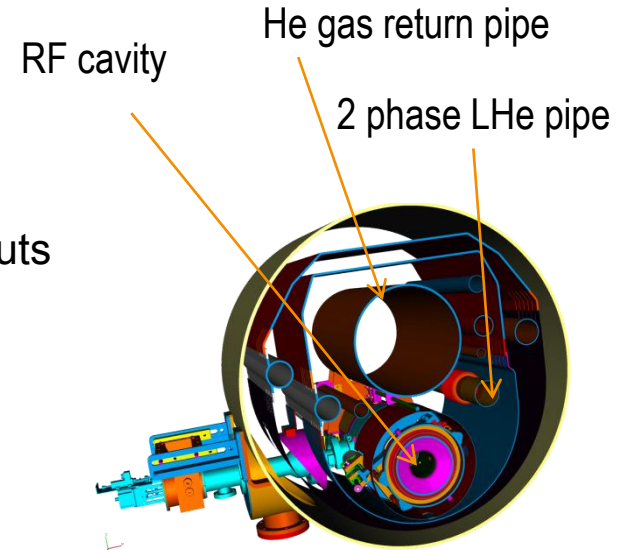
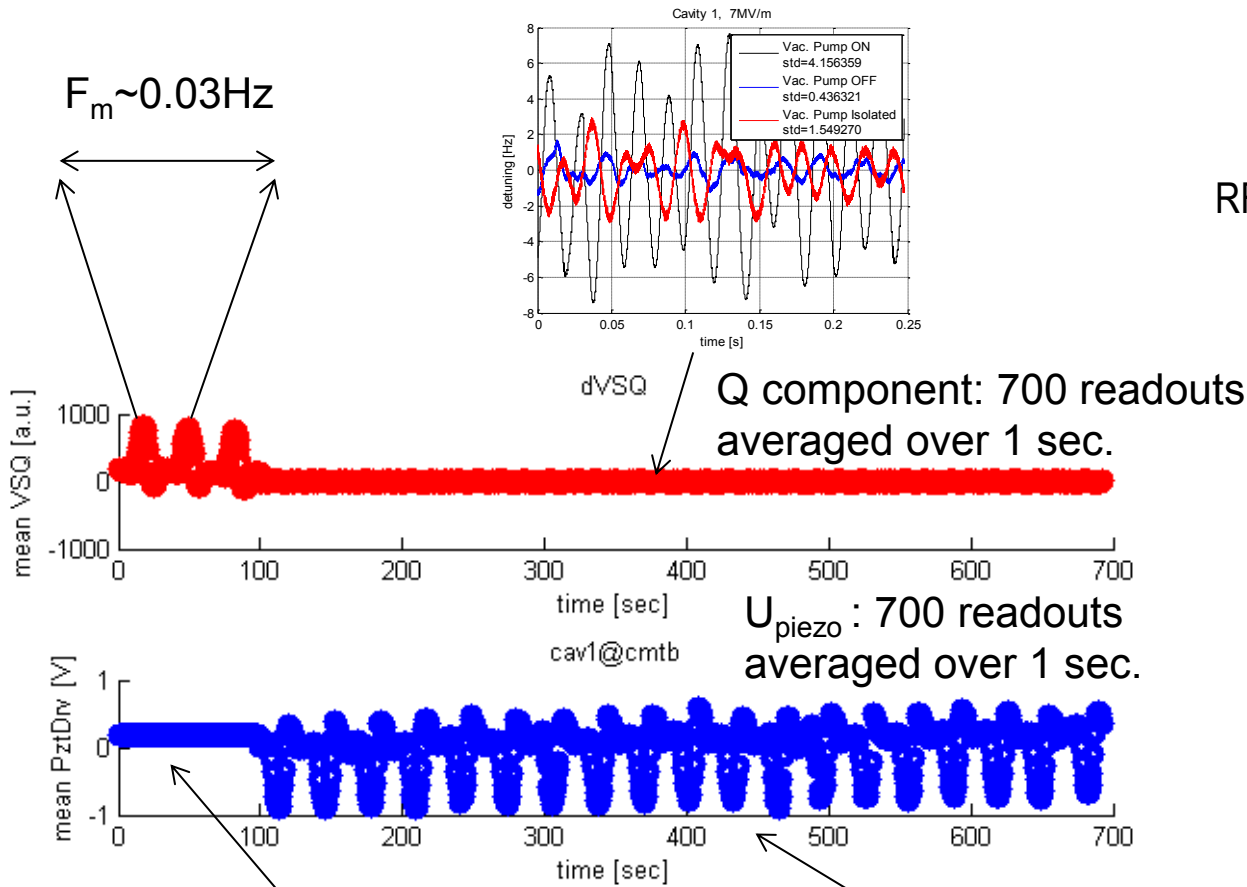
```

for ii=1:Ncav
    Vcav(n,ii)=(Vcav(n-1,ii)+DT*w0*rho*Ifwd*exp(1i*phig))...
        /(1+DT*(w12+1i*det(ii)));
end
    
```

# Microphonics Sources



# Slow Microphonics Compensation (<10 Hz)

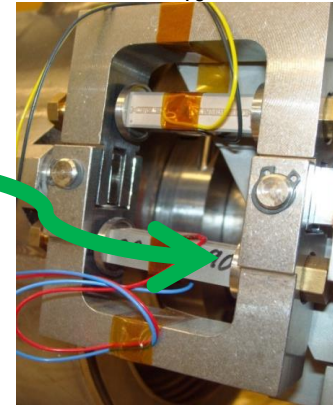
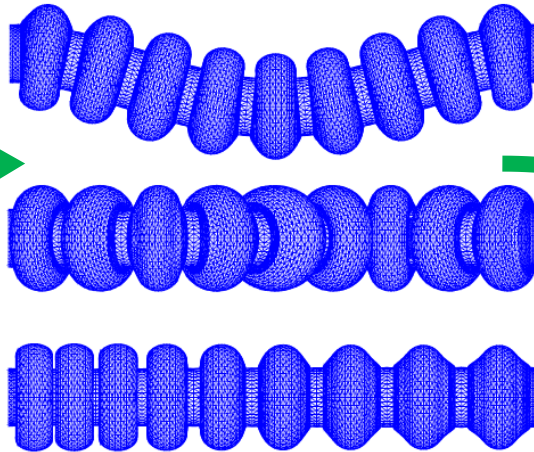
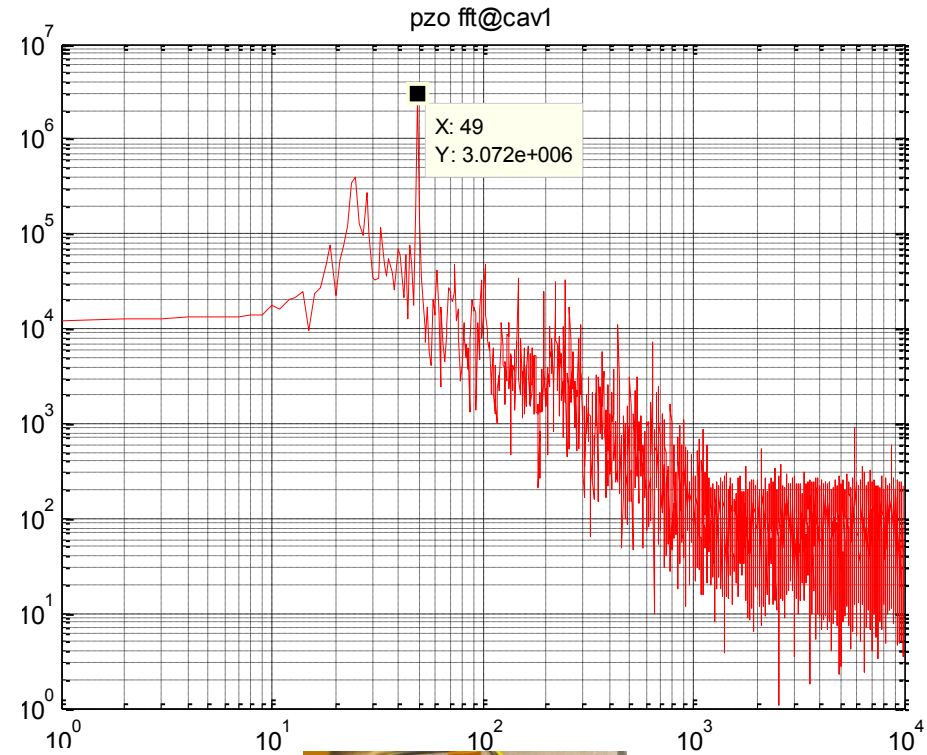
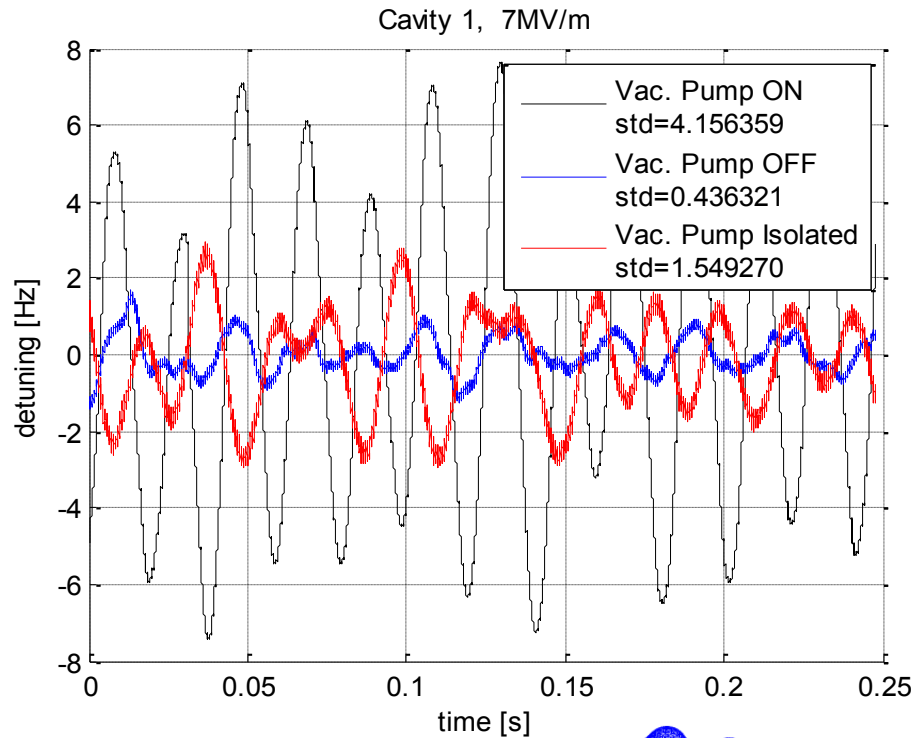


Piezo compensation off

Piezo compensation on  
(PI controller)

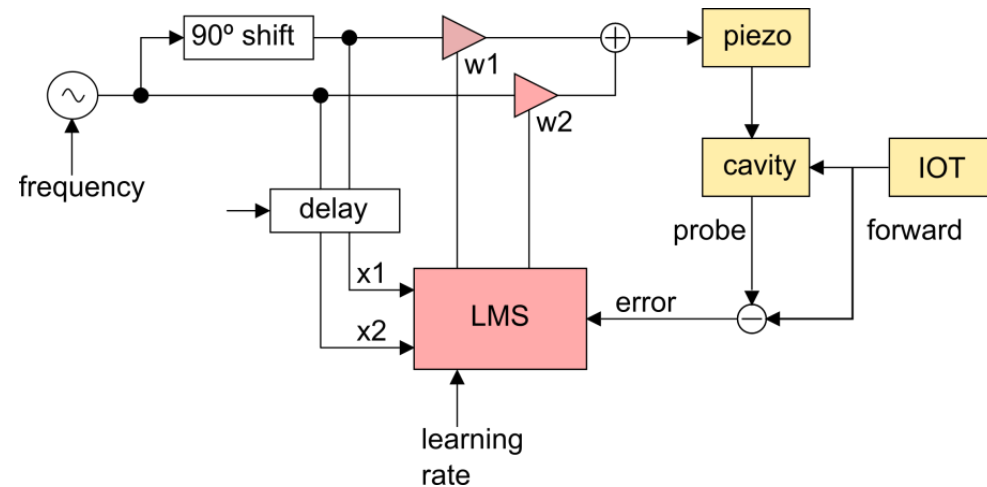
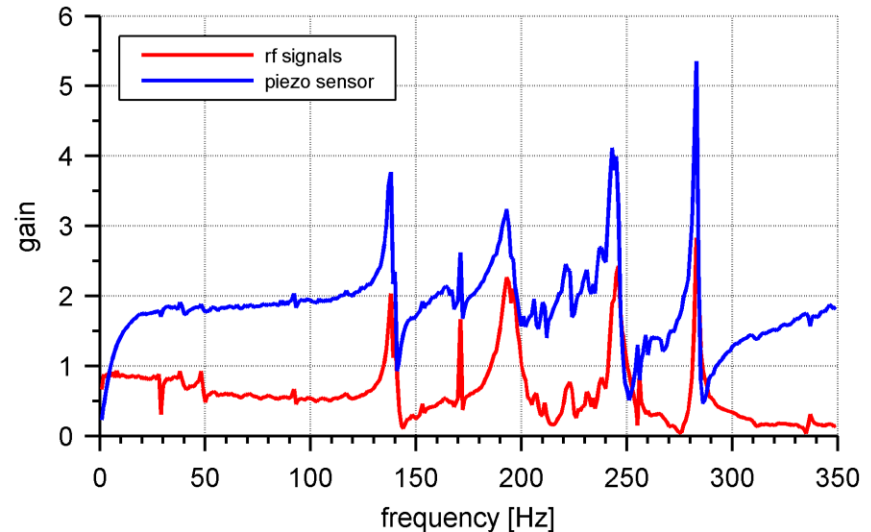


# Fast Microphonics Source (>10 Hz)



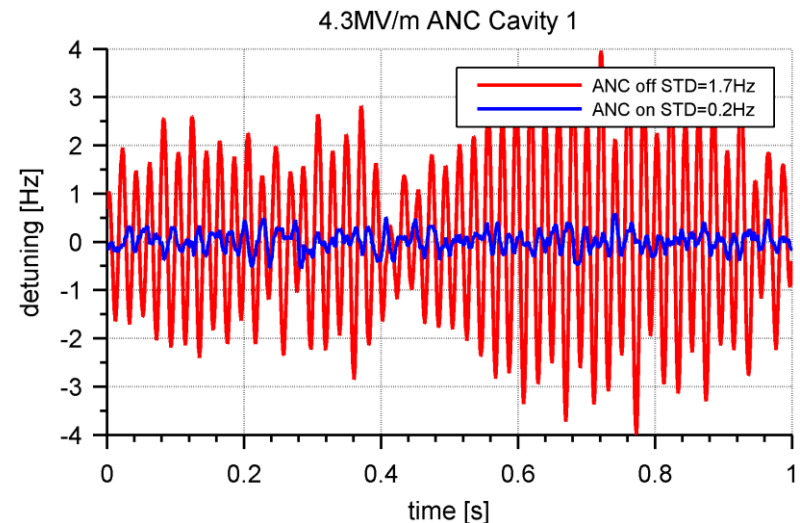
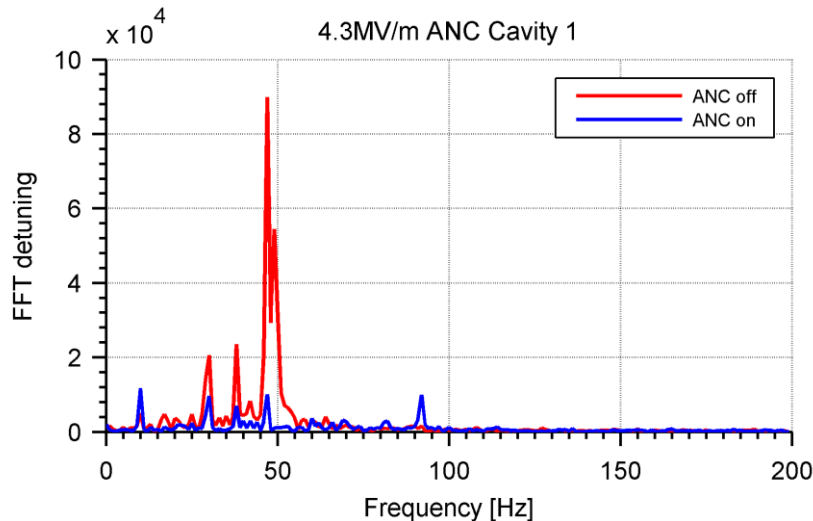
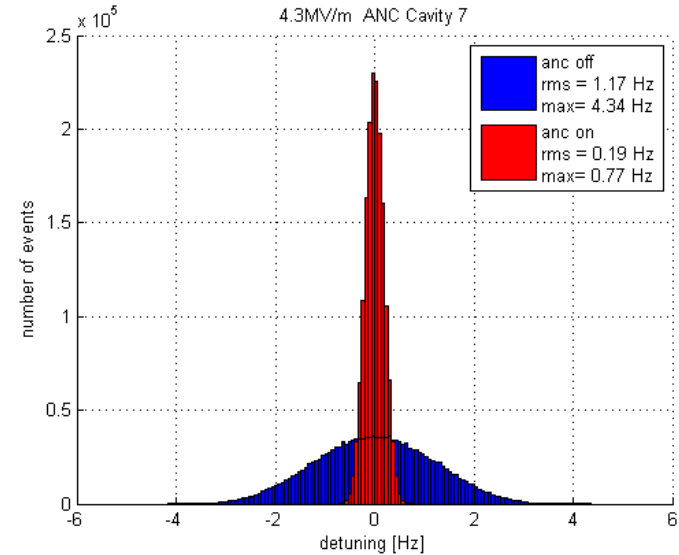
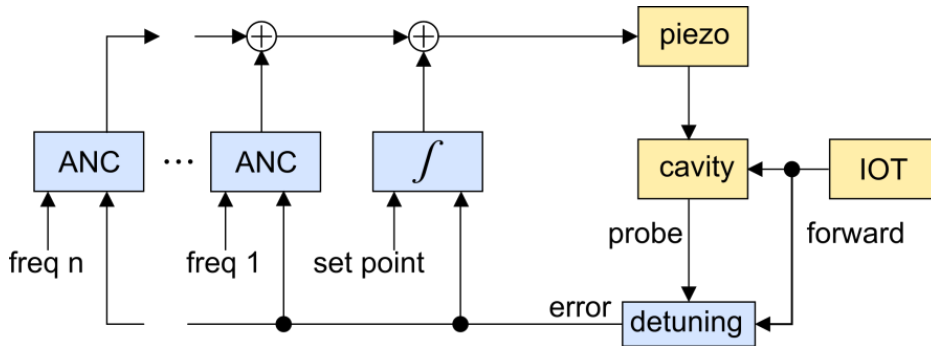
# Microphonics compensation strategy

- Conventional PI control insufficient due to complicated piezo->detuning transfer function (<10Hz)
- Narrowband Active Noise Control algorithm for the dominating disturbances
  - Adaptive feed forward
  - LMS update:  
 $w(i+1) = w(i) + \text{learning\_rate} * \text{error} * x(i)$
  - FPGA implementation based on the CORDIC algorithm
  - Optionally multiple frequencies compensation
  - Accurate transfer function is not required

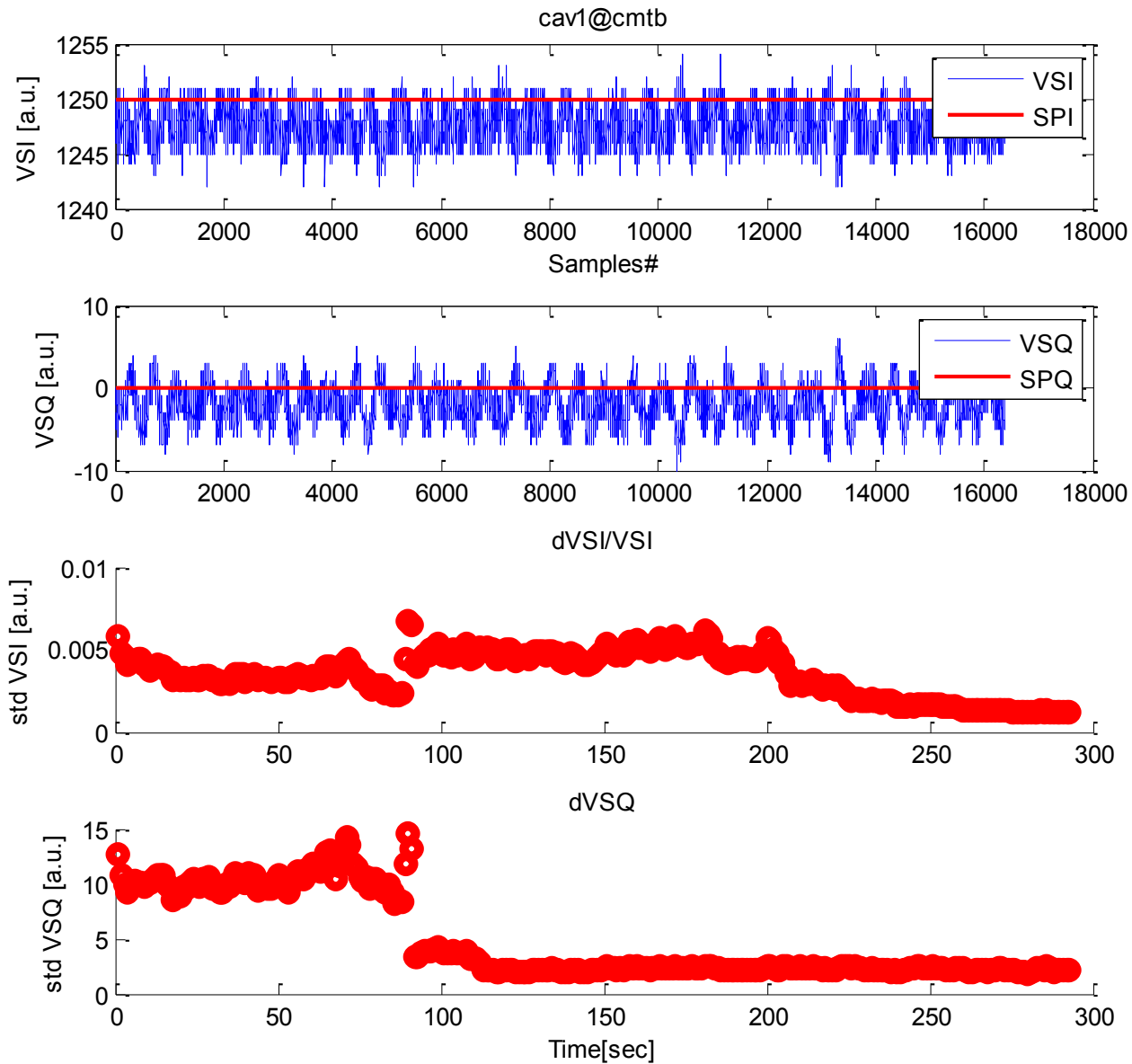


# PI+ANC Combined Controller Results

> 30+49Hz ANC for dominating disturbances

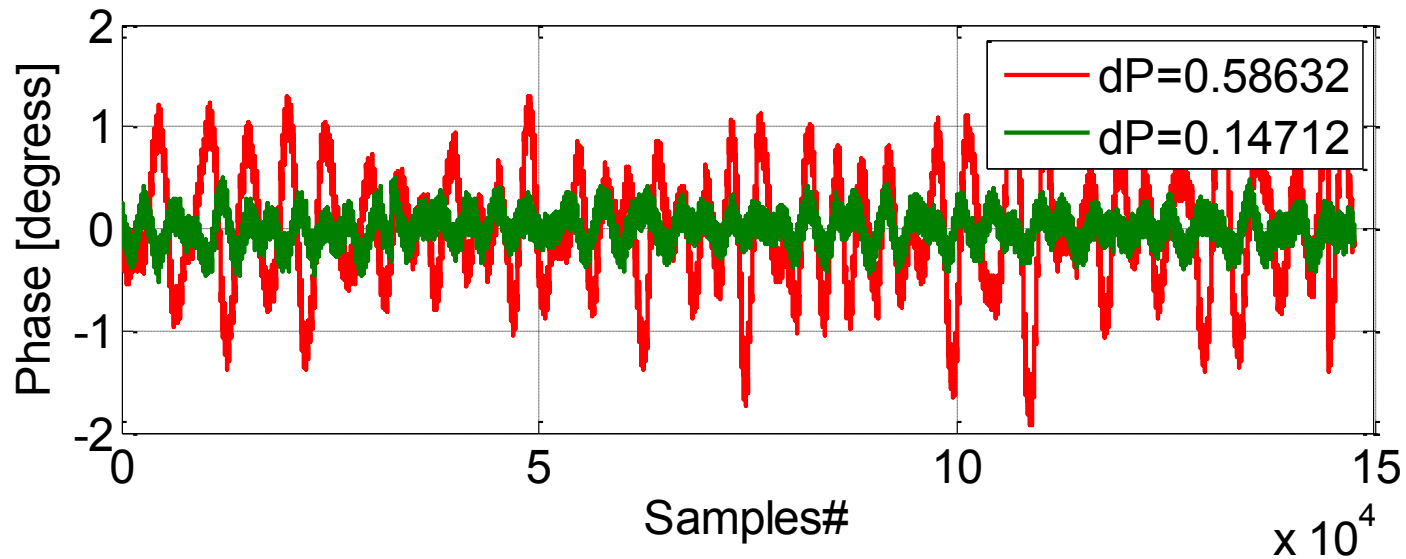
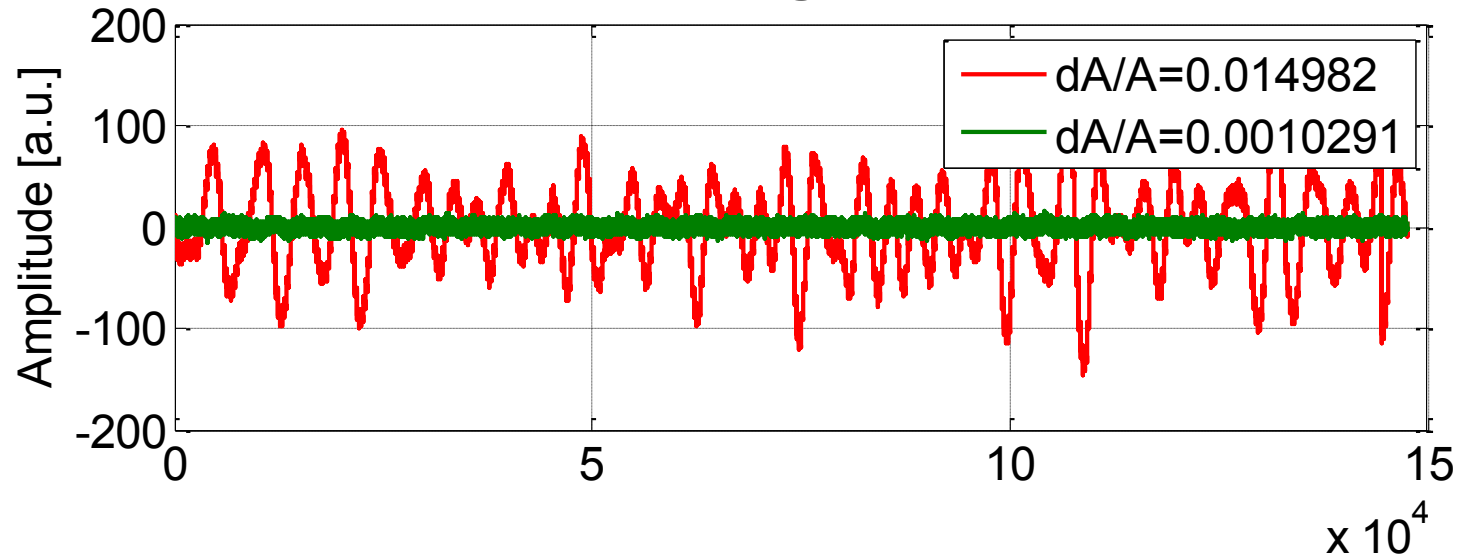


# RF and Piezo Feedbacks (long term measurements)



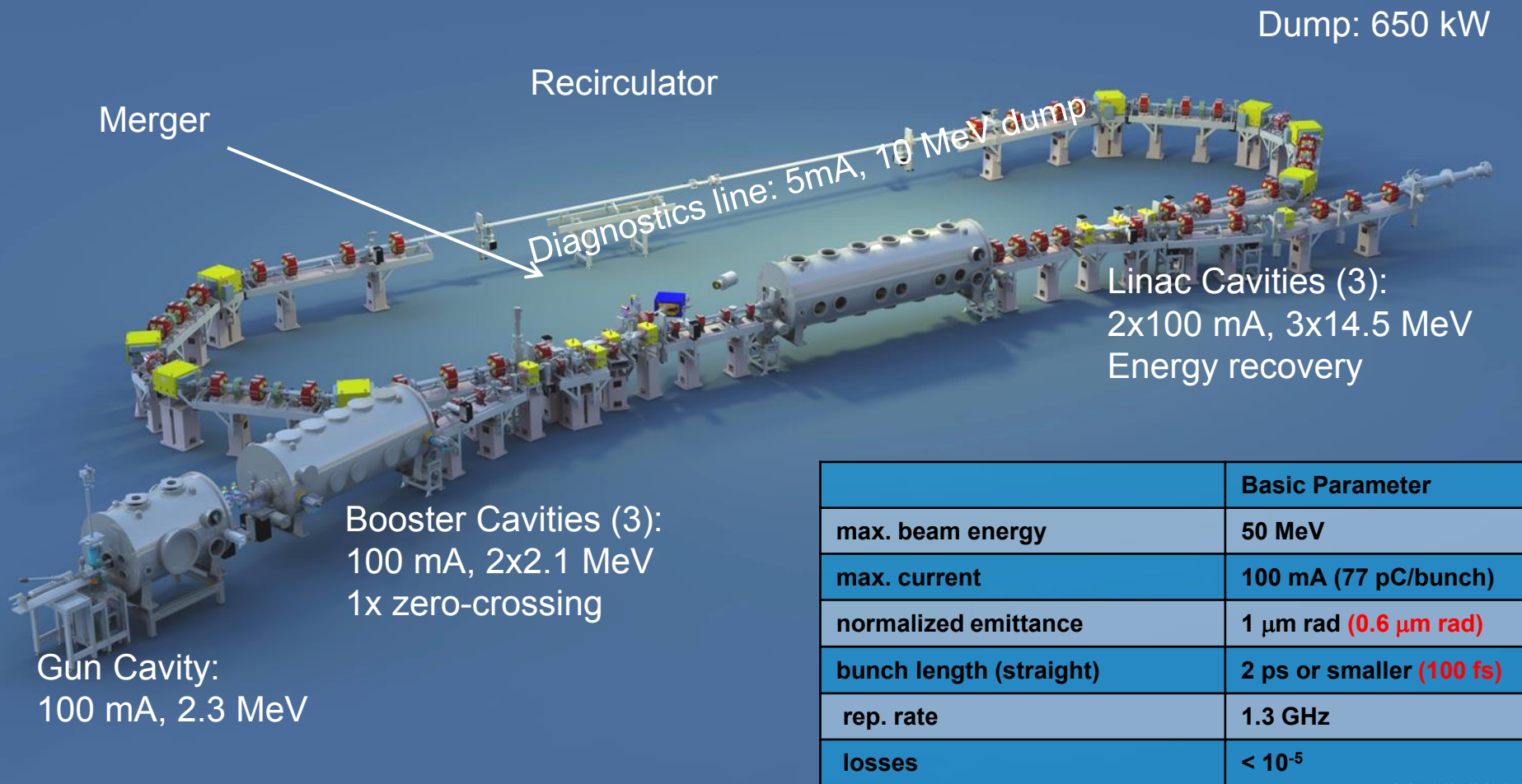
# RF and Piezo Feedbacks (peak performance)

cav1@cmtb



# Possible Application (Parameters)

- > Berlin Energy Recovery Linac Project **bERLinPro** at the Helmholtz Zentrum in Berlin:



Courtesy by P. Echevarria



# Possible Application (Goals)

## > Berlin Energy Recovery Linac Project **bERLinPro** at the Helmholtz Zentrum in Berlin:

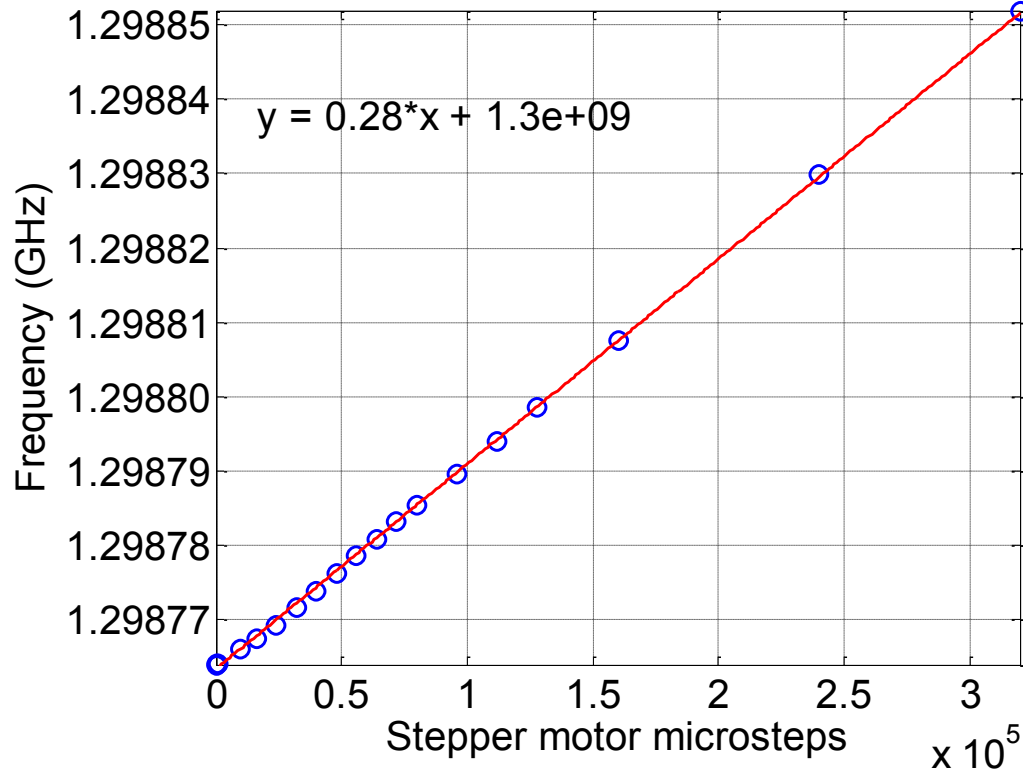
- project goal is the generation of a high current (100 mA), low emittance (below 1 mm mrad) CW electron beam at 2 ps rms bunch duration
- The LLRF control system will be implemented using the MTCA.4 technology and due to the fact each cavity of the accelerator will be fed by its own RF power source (klystrons for the gun and booster and SSA for the linac), the single cavity approach will be used.
- The precise RF amplitude and phase control needed due to beam recovery process
- The microphonics compensation needed due to narrow bandwidth of the cavities (especially at the linac module)
- All of the cavities will be equipped with a blade tuner which will be driven by a stepper motor for slow coarse tuning and **four piezo actuators** for a fine fast compensation



Courtesy by P. Echevarria

# Firsts Results with MTCA.4 Electronics

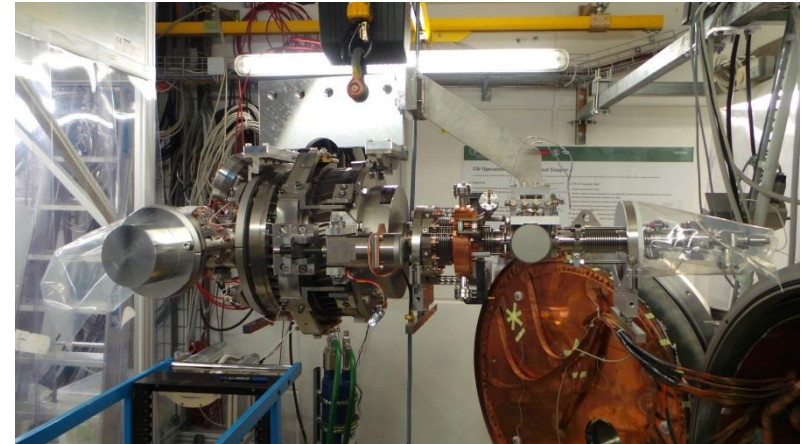
Frequency Vs. Stepper motor microsteps



More details by A. Ushakov at:

**TUPOW035**

**“First LLRF Tests of BERLinPros Gun Cavity Prototype”**



DFMC-MD22

# People Involved

## > DESY

Valeri Ayvazyan

Christian Schmidt

Martin Killenberg

Konrad Przygoda

Radoslaw Rybaniec

Holger Schlarb

Jacek Sekutowicz

Robert Wedel

## > HZB

Pablo Echevarria

Jens Knobloch

Oliver Kugeler

Axel Neumann

Andriy Ushakov

# Thank You for Attention

