

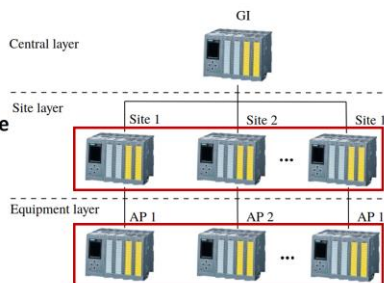
APPLYING MODEL CHECKING TO HIGHLY-CONFIGURABLE SAFETY CRITICAL SOFTWARE: THE SPS-PPS PLC PROGRAM

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THE SPS PERSONNEL PROTECTION SYSTEM (PPS)

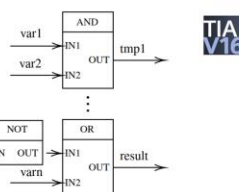
SPS-PPS Hardware architecture

PLC-based control system
S7-1516F



SPS-PPS Site PLC program architecture

Set of Functions (FC) and
Function Blocks (FBs)



- Highly distributed and configurable system
- Site PLCs: common PLC program and different configurations per access zone

- TIA Portal programming environment
- Function Block Diagram (FBD) programming language
- Complex and large PLC logic
- Complex specifications

THE SITE SPS-PPS PLC PROGRAM AND SPECIFICATIONS

Complex requirement specifications based on
if-else conditional statement that contains Boolean formulas

```
if (
  N_i
  [ (I_EISa_Pos_Stat[i] ∨ I_EISa_Bypass[i])
  ∧
  [ (I_EISa_Pos[i]=1 ∧ SC-S[j]) | i=1
  ∧
  [ (I_EISa_PU_Stat[i] ∨ I_EISa_Bypass[i])
  ∧
  [ (I_EISa_PU[i]=1 ∧ SC-S[j]) | i=1
  ∧
  S0_AP_Pos[j] ∧ S0_AP_PU[j] ∧ S0_AP_Key_Distrib[j]
  ) then
    N_EISa_Safe[j] ← 1
  else
    N_EISa_Safe[j] ← 0
  end if
```

Example of SIF-X1 behavior

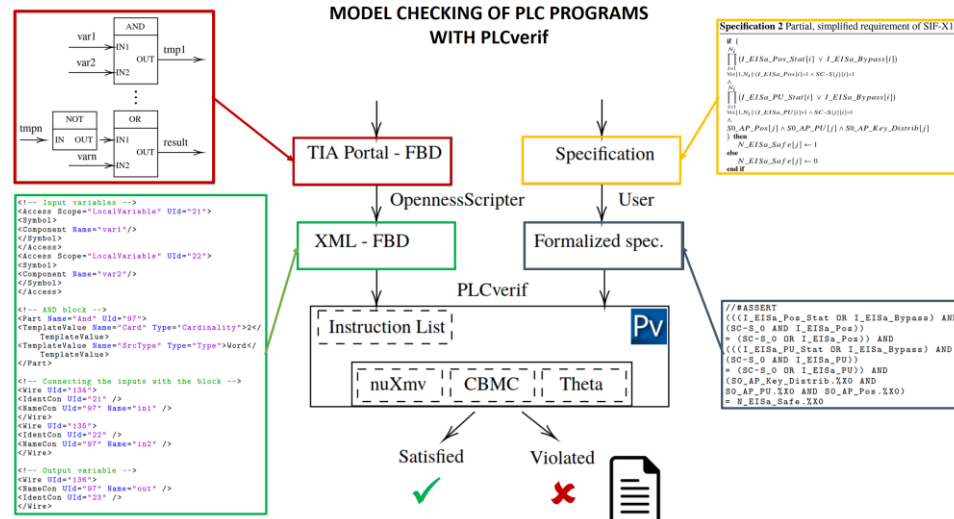
Source	Variable	Value
Input	EISa_Pos_Stat	0000 0000 0000 1001
Input	EISa_PU_Stat	0000 0000 0000 0001
...
Input (AP PLC)	S0_AP_Pos	0000 0000 0000 1001
...
Configuration	EISa_Pos	0000 0000 0000 1001
Configuration	EISa_PU	0000 0000 0000 0001
...
Configuration	SC - S_0	0000 0000 0000 0001
Configuration	SC - S_1	0000 0000 0000 1000
...
Output	N_EISa_Safe	0000 0000 0000 1001

Lots of configuration variables (SIF-X1 example)

Configuration variables	94 WORD (16-bit) and 4 BOOL
Input variables	21 WORD (16-bit) and 2 BOOL
Potential State Space (nb. of combinations to be checked)	$5.0 \cdot 10^{555}$

Impossible to validate the PLC program **only** with testing
Model checking is recommended by the Functional Safety standards

MODEL CHECKING OF PLC PROGRAMS WITH PLCverif



Model Checking (MC) is recommended by the Functional Safety standards
(IEC 61508 and IEC 61511)

Conclusions of using MC in this project

detect bugs in the PLC program before commissioning

identify deficiencies in the specification

help experts to better understand the behaviour of the program for all possible configurations (corner cases)

Future work

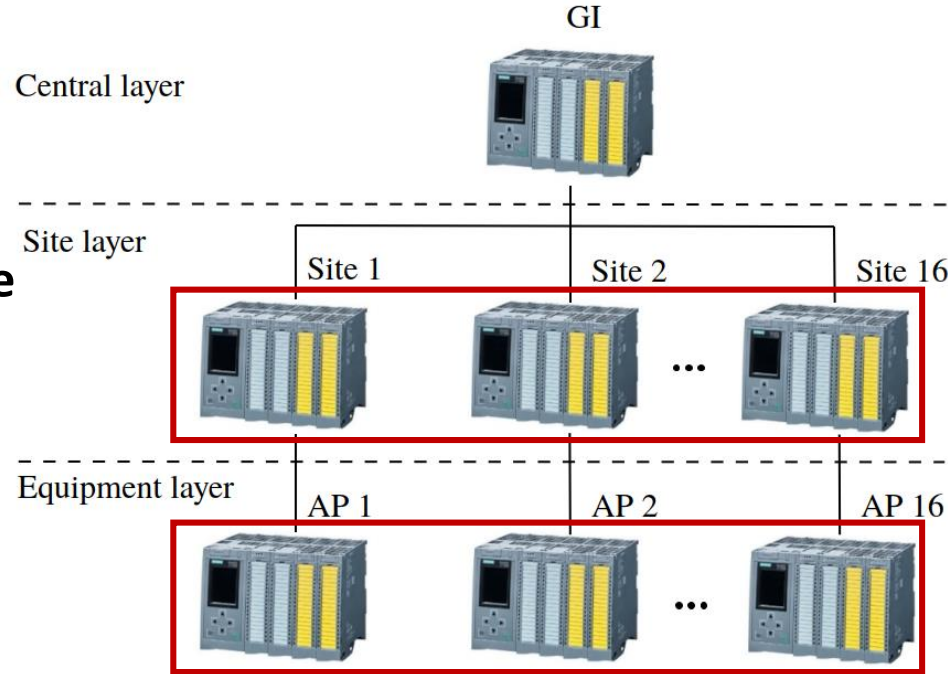
integration of model checking in PLC programming environments (e.g. TIA portal)

improvement of verification performance with better algorithms and abstraction techniques



PLCverif allows to apply model checking to PLC program and hides the complexity associated to formal methods

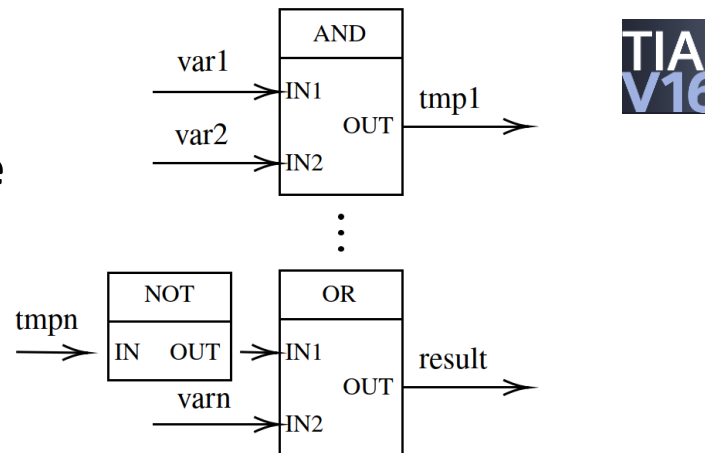
THE SPS PERSONNEL PROTECTION SYSTEM (PPS)



- **Highly distributed and configurable system**
- **Site PLCs:** common PLC program and different configurations per access zone

SPS-PPS Site PLC program architecture

Set of *Functions (FC)* and *Function Blocks (FBs)*



- **TIA Portal** programming environment
- **Function Block Diagram (FBD) programming language**
- Complex and large PLC logic
- Complex specifications

THE SITE SPS-PPS PLC PROGRAM AND SPECIFICATIONS

Complex requirement specifications based on
if-else conditional statement that contains Boolean formulas

```
if (  
   $\prod_{i=1}^{N_i} (I\_EISa\_Pos\_Stat[i] \vee I\_EISa\_Bypass[i])$   
   $\forall i \in [1, N_i] : (I\_EISa\_Pos[i]=1 \wedge SC-S\{j\}[i]=1)$   
   $\wedge$   
   $\prod_{i=1}^{N_i} (I\_EISa\_PU\_Stat[i] \vee I\_EISa\_Bypass[i])$   
   $\forall i \in [1, N_i] : (I\_EISa\_PU[i]=1 \wedge SC-S\{j\}[i]=1)$   
   $\wedge$   
   $S0\_AP\_Pos[j] \wedge S0\_AP\_PU[j] \wedge S0\_AP\_Key\_Distrib[j]$   
) then  
   $N\_EISa\_Safe[j] \leftarrow 1$   
else  
   $N\_EISa\_Safe[j] \leftarrow 0$   
end if
```

Example of SIF-X1 behavior

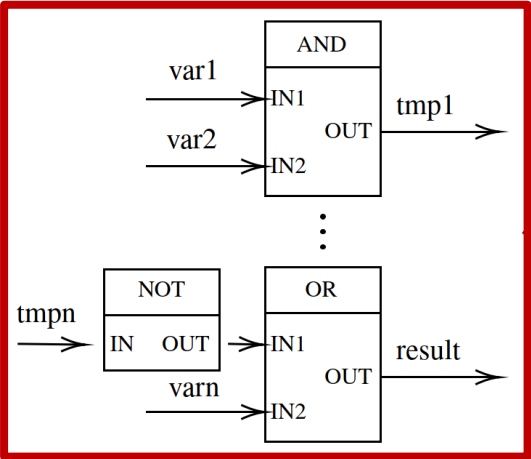
Source	Variable	Value
Input	$EISa_Pos_Stat$	0000 0000 0000 1001
Input	$EISa_PU_Stat$	0000 0000 0000 0001
...
Input (AP PLC)	$S0_AP_Pos$	0000 0000 0000 1001
...
Configuration	$EISa_Pos$	0000 0000 0000 1001
Configuration	$EISa_PU$	0000 0000 0000 0001
...
Configuration	$SC - S_0$	0000 0000 0000 0001
Configuration	$SC - S_1$	0000 0000 0000 1000
...
Output	N_EISa_Safe	0000 0000 0000 1001

Lots of configuration variables (SIF-X1 example)

Configuration variables	94 WORD (16-bit) and 4 BOOL
Input variables	21 WORD (16-bit) and 2 BOOL
Potential State Space	$5.0 \cdot 10^{555}$
(nb. of combinations to be checked)	

Impossible to validate the PLC program **only** with **testing**
Model checking is recommended by the Functional Safety standards

MODEL CHECKING OF PLC PROGRAMS WITH PLCverif



```
<!-- Input variables -->
<Access Scope="LocalVariable" UId="21">
<Symbol>
<Component Name="var1"/>
</Symbol>
</Access>
<Access Scope="LocalVariable" UId="22">
<Symbol>
<Component Name="var2"/>
</Symbol>
</Access>

<!-- AND block -->
<Part Name="And" UId="97">
<TemplateValue Name="Card" Type="Cardinality">2</
TemplateValue>
<TemplateValue Name="SrcType" Type="Type">Word</
TemplateValue>
</Part>

<!-- Connecting the inputs with the block -->
<Wire UId="134">
<IdentCon UId="21" />
<NameCon UId="97" Name="in1" />
</Wire>
<Wire UId="135">
<IdentCon UId="22" />
<NameCon UId="97" Name="in2" />
</Wire>

<!-- Output variable -->
<Wire UId="136">
<NameCon UId="97" Name="out" />
<IdentCon UId="23" />
</Wire>
```

Specification 2 Partial, simplified requirement of SIF-X1

```
if (
   $\prod_{i=1}^{N_i} (I\_EISa\_Pos\_Stat[i] \vee I\_EISa\_Bypass[i])$ 
   $\wedge$ 
   $\prod_{i=1}^{N_i} (I\_EISa\_PU\_Stat[i] \vee I\_EISa\_Bypass[i])$ 
   $\wedge$ 
   $\forall i \in [1, N_i]: (I\_EISa\_Pos[i]=1 \wedge SC-S[j])[i]=1$ 
   $\wedge$ 
   $\forall i \in [1, N_i]: (I\_EISa\_PU[i]=1 \wedge SC-S[j])[i]=1$ 
   $\wedge$ 
   $S0\_AP\_Pos[j] \wedge S0\_AP\_PU[j] \wedge S0\_AP\_Key\_Distrib[j]$ 
) then
  N_EISa_Safe[j]  $\leftarrow$  1
else
  N_EISa_Safe[j]  $\leftarrow$  0
end if
```

TIA Portal - FBD

Specification

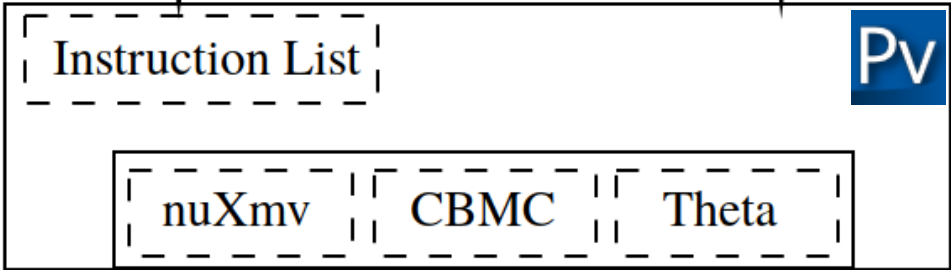
OpennessScripter

User

XML - FBD

Formalized spec.

PLCverif



```
// #ASSERT
(((I_EISa_Pos_Stat OR I_EISa_Bypass) AND
(SC-S_0 AND I_EISa_Pos))
= (SC-S_0 OR I_EISa_Pos)) AND
(((I_EISa_PU_Stat OR I_EISa_Bypass) AND
(SC-S_0 AND I_EISa_PU))
= (SC-S_0 OR I_EISa_PU)) AND
(SO_AP_Key_Distrib.%X0 AND
SO_AP_PU.%X0 AND SO_AP_Pos.%X0)
= N_EISa_Safe.%X0
```

Satisfied

Violated



Model Checking (MC) is recommended by the **Functional Safety standards** (IEC 61508 and IEC 61511)

Conclusions of using MC in this project	Future work
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identify deficiencies in the specification	improvement of verification performance with better algorithms and abstraction techniques
help experts to better understand the behaviour of the program for all possible configurations (corner cases)	



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