**TUPV018** 

## KEK LUCX facility Laser-to-RF&RF-to-RF stability study and optimization

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Figure 4. LUCX trigger system block diagram Figure 3. Block diagram of LUCX control



Figure 5. 2856 MHz klystron drive signal phase stability from 1Hz to 100MHz



Figure 7. Laser-to-RF 100 counts (short-term jitter, 4.7ps)



Figure 6. Nd:YAG laser oscillator phase stability from 10Hz to 40MHz



Figure 8. RF-to-RF 100 counts (short-term jitter, 1.5ps)

## **Rough RF-to-RF jitter estimation**

All drive and reference signals jitter go to Klystron outputs. So, the rough estimation of existing jitter is  $\sigma_{RFtRF} = \sqrt{\sigma_{RF}^2 + \sigma_{RF}^2} = \sqrt{(1.487ps)^2 + (1.487ps)^2} = 2.103ps$ . It is very high.

## Rough Laser-to-RF jitter estimation

All drive and reference signals jitter go to Klystron output and laser output signal. So, the rough estimation of existing jitter is  $\sigma_{LtRF} = \sqrt{\sigma_L^2 + \sigma_{RF}^2} = \sqrt{(1.487ps)^2 + (1.920ps)^2} = 2.428ps$ . It is very high.

All measurements were systematically made from 11:00 o'clock till 24:00 o'clock. Measurement results deviation is only 50 fs. Jitter values are the same for Ti:Sapphire laser system.

Laser –to-RF jitter was improved from 4.7ps to 3.2ps after Master Oscillator replacement from old HP SG to new Agilent SG. Also, RF-to-RF jitter was improved from 1.5ps to 900fs. All results were crosschecked with down conversion technique. 2856 MHz signals were down converted to 10 MHz.



Figure 9. Tektronix DPO7354 Oscilloscope



Figure 10. Gun-to-Booster panorama screenshot





Figure 11. Gun-to-Booster short-term phase stability at direct sampling (300 machine cycles)

120

100

60

80

RF – gun phase, degree

140

Figure 12. Gun-to-Booster long-term phase stability at direct sampling (1000 machine cycles)





According to datasheet of ICT and Low Noise preamplifier, the measurement resolution of electron bunch charge with ICT1s is 1 pC.





Input parameters								
Laser pulse RMS length at the photocathode	100fs							
Laser pulse RMS transverse size at the photocathode	500µm x 500µm							
Dynamics type	on-axis							
Bunch charge, Q	50pC							
Space Charge	enabled							
Schottky effect (Phase scan)	enabled							
Laser-to-RF gun jitter, ps	3.2 ps							
RF-to-RF jitter, ps	900 fs							
RF-gun phase, degree	23 deg							



Figure 18. Event Generator & Event Receiver based trigger system

Only first module uses coaxial cable. The rest of connection between EVG and EVR uses optical fibers. There are not noisy fluctuations because temperature fluctuations and radiation interfere with High Power devices.





Figure 22. RedPitaya SIGNALlab 250-12 FPGA board



Figure 19. SINAP STD-EVO module (Event Generator)

Figure 20. SINAP STD-EVE module (Event Receiver)

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Further distribution. There are not necessity to adjust jitter for fast timing and misfire problems. SINAP Event Generator is Line Sync, first TD module to get synchronized RF and Trigger signals, as well as Digital Time Delay and Gate Generator.

Figure 21. Stanford Research Systems DG535 Line Sync



Figure 23. LUCX timing system control window written at PyQt with EPICS PV

<ul> <li>TimingWindow</li> </ul>								-	2	×		
LUCX timing, digital delay "digdel-2.rfgtb.kek.jp" (IP 192.168.13.136). Highland Technology T564												
Channel settings		DigDel2 module settings										
	Status	Delay, ns	Width, ns	Ch A, cmd_val, ps								
Ch A					Set Delay	Set Width	Set POS	Set NEG	Set ON	Set OFF		
	Status	Delay, ns	Width, ns	Ch B, cmd_val, ps								
Ch B					Set Delay	Set Width	Set POS	Set NEG	Set ON	Set OFF		
	Status	Delay, ns	Width, ns	Ch C, cmd_val, ps								
Ch C					Set Delay	Set Width	Set POS	Set NEG	Set ON	Set OFF		
	Status	Delay, ns	Width, ns	Ch D, cmd_val, ps								
Ch D					Set Delay	Set Width	Set POS	Set NEG	Set ON	Set OFF	ļ	

The accelerator control software is based on EPICS and has a various useful tools written using python, Qt. **SINAP STD-EVO and STD-EVE:** event clock delay, 1/20 event clock delay, 5ps/step delay; RF clock frequency range is 60 – 500MHz; Embedded IOC installed (uclinux 2.6.30.4) Every channel is independently programmed (delay, width, polarity). Number of NIM channels is 8: Number of TTL channels is 20;

Figure 24. LUCX timing system delay values setup window written at PyQt with EPICS PV