Revisiting the Bunch-Synchronized Data Acquisition System for the European XFEL Accelerator

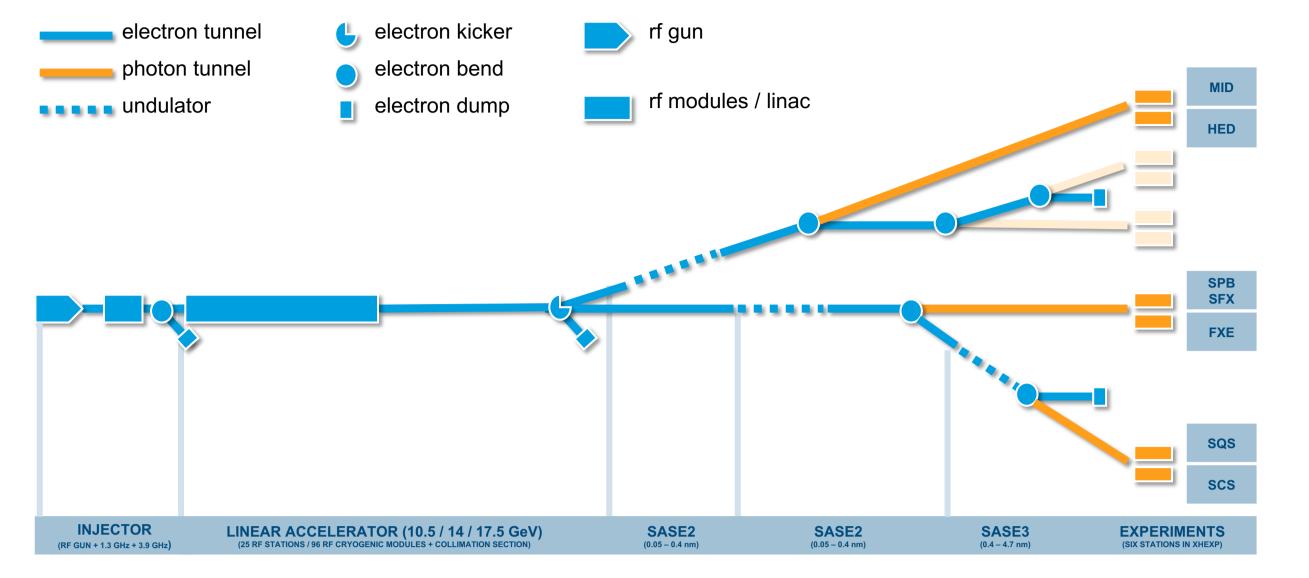
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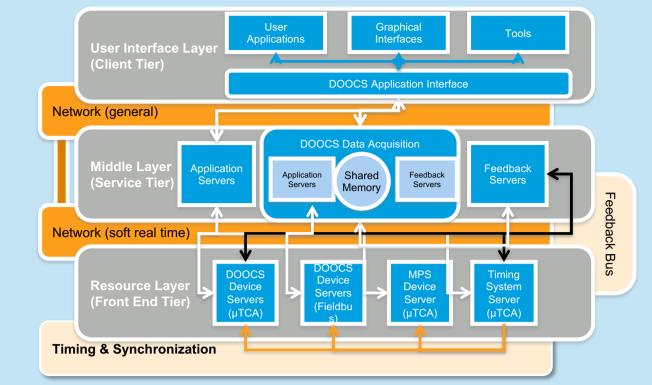
Abstract

After about two years in operation the bunchsynchronized data acquisition as used with the accelerator control system at the European XFEL is being revisited. As we have now gained quite some experience with the current system design it was found to have some shortfalls - specifically as for offered methods and tools for data retrieval and management. In this paper the current implementation is being discussed and taken as an input for an evaluation of new frameworks readily used by many internet and business companies in the context of modern data collection and management technologies. The main focus is currently put on streaming technologies which are being reviewed with respect to feasibility and adaptability for control system architectures at DESY's accelerator facilities.

The European XFEL Accelerator – Beam Line Layout









XFEL: X-Ray Program				European XFEL
TIN TL	64 bunches	9000 eV	²⁵⁰⁰ 	MID
14030 MeV 14030 MeV 0.25 nC 1000	150 bunches	9300 eV	۳۵ ۲۵ ایر 1879 میں 1879	SPB/SFX
Legend Interlock broken No beam in section Electrons in section Interlock set Photons in section	20 bunches out of 170 bunches	779 eV	^{₩000}	SCS
Accelerator: 14 GeV, 2.25 MHz, approx. 1000 k 2019-09-29 16:32	ounches, SASE delivery	SASE1: SI SASE2: M SASE3: SG	ID	

Public European XFEL Accelerator Status Panel

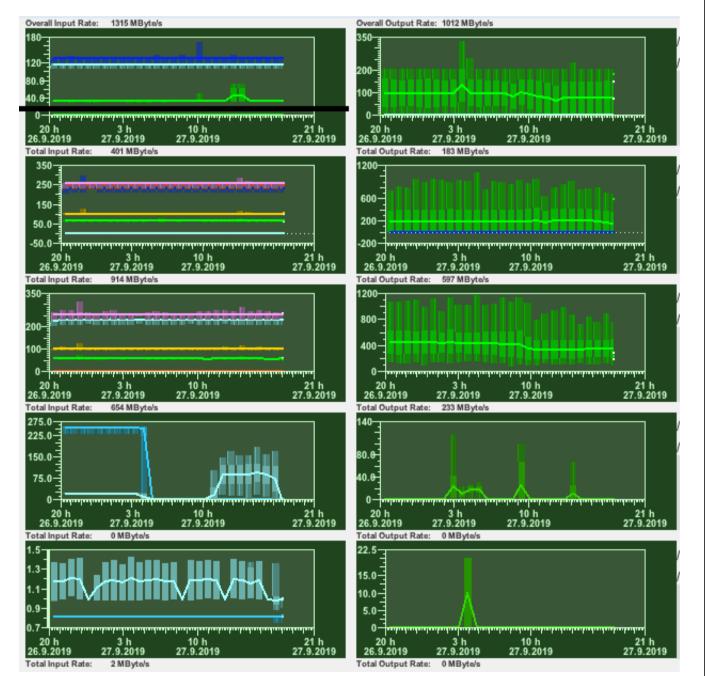
Specifications	Parameter	
Superconducting linear accelerator with	Electron beam energy	10.5 GeV / 14 GeV / 16.5 GeV
beam energies 10.5 GeV / 14 GeV / 17.5 GeV	Bunch charge	0.02 – 1 nC
Pulse repetition rate at 10 Hz with 27000 bunches/s	Peak current	2 – 5 kA
3 Photon beam lines with 6 experiment stations	Slice emittance	0.4 – 1.0 mm mrad
Photon energy 0.3 – 24 keV	Slice energy spread	4 – 2 MeV
Photon pulse length 10 – 100 fs	Shortest SASE wavelength	0.04 nm
Photon Pulse energy ~ mJ	Pulse repetition rate	10 Hz
	Bunches per pulse	2700
	RF-Pulse length (flat top)	600 µs

Accelerator Control System layout with integrated DAQ

On the device layer MicroTCA-based ADC modules, camera devices as well as PLC and other embedded devices are sending data to collector processes. A fast collector acquires data at the bunch repetition rate from triggered devices and a slow collector polling the data at rates of about 1 Hz from other hardware. The data has been tagged on the front-ends with a unique shot number provided by the timing system. Both collector processes are feeding the received data into a buffer manager using shared memory for storing the data. The distributor process functions as buffer manager and is in charge of managing the shared memory structure. Middle layer processes can connect to the buffer manager and read and/or write back to it. Once all data for a given shot number has been acquired in shared memory it is sent to the event builder and -writer processes. They will write the data as compressed files to disk. To tape it the dCache [5] facility at DESY is being utilized. Several applications interfaces exist to read the data files and extract data for individual control system parameters (i.e. DOOCS channels on the front-end server). Middle layer server can provide data for other applications including graphical user interfaces for online monitoring purposes.

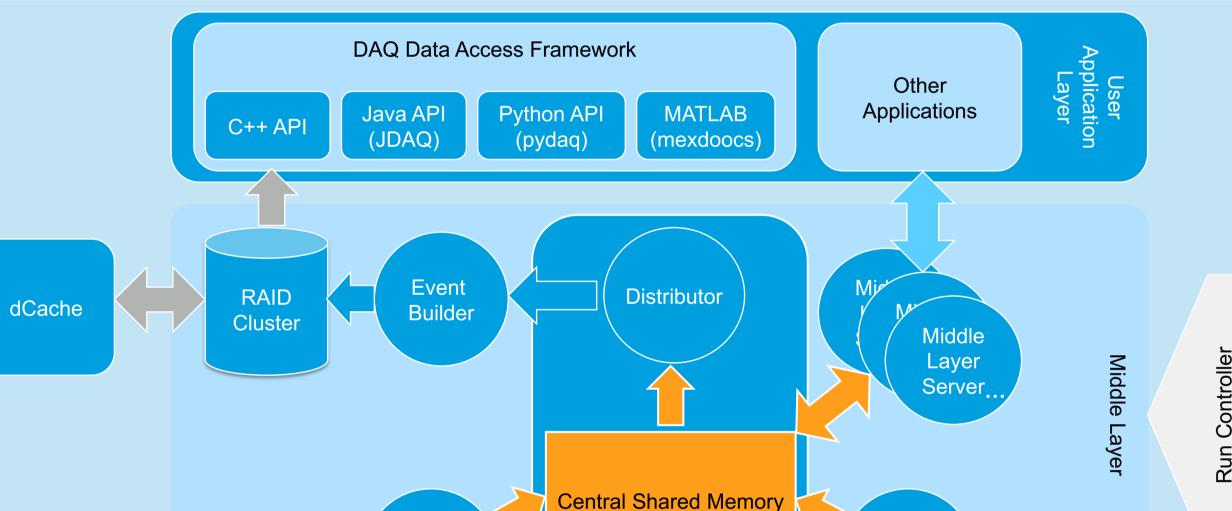
Status and Operation

Parameter		
Total input rate (24/7)	2.1 Gbyte/s	
Compressed storage rate	Up to 30 Tbyte/day	
Total number of channels	~ 100 k	
Number of DAQ instances	6 + 2	
Number of streams per instance	2 - 8	
Temporary storage space	Up to 1 Pbyte (dCache)	

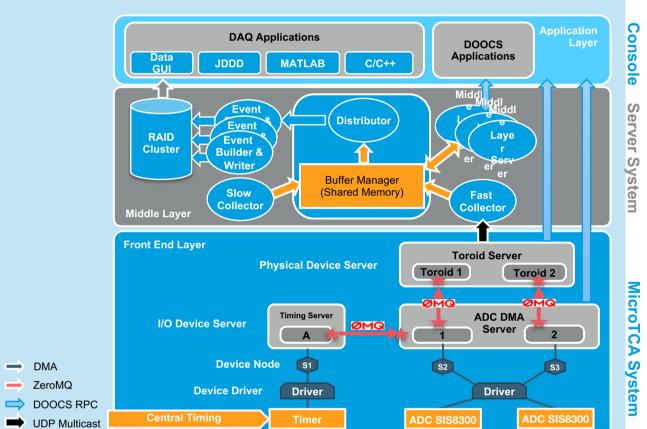


Input rates (left) and output rates (right) of DAQ instances

Schematic Overview of the Accelerator DAQ



Data Flow from MicroTCA Front-End to Applications



Fast Slow Collector Collector File I/O TCP - DataLink Device Layer Shared Memory PLC, slow ADC PLC, slow ADC Fast ADC PLC, ast ADC Fast ADC low ADC UDP Multicast Camera Camera Camera RPC Slow data (fixed rate or event-based) Fast, bunch-synchronous data

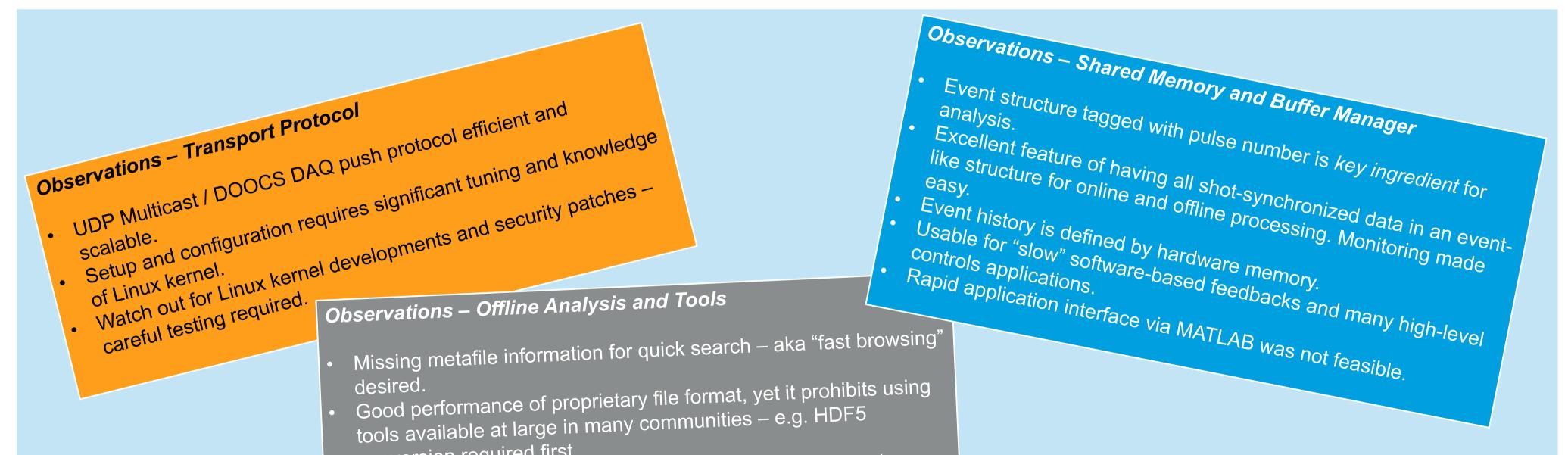
Data acquisition instance integrates into middle layer server of overall accelerator layout

📥 ТСР SHM (DAQ)

Timing System and its pulse or shot number

- Common timing system hardware (x2timer MTCA.4 AMC) in every front-end connected via fiber optics net to master.
- Unique identification of individual shot data by sending timing system information i.e. clock signals, trigger events, pulse number etc. to all front-end devices.
- A timer server program on every MicroTCA system provides the timing system information also via ZeroMQ to applications.
- The stamped data is sent via a push-type protocol based on UDP multicast to all subscribers e.g. also DAQ instances.

Lessons Learned



Outlook



- Event structure with shot-synchronized and bunch-resolved data requiring corresponding FE support (timing and synchronization).
- Streaming concepts for efficient data collection from FE and other sources which allow online processing.
- Near-real time access of shotsynchronized data at stream level.
- Control system framework independent, i.e. use plug-in mechanism for FE software (sender) and given control

Tools availability and functionality define community acceptance. Feature of parallel instances allows for redundant setup, testing Observations – Instances and debugging. Flexible and quick ramp up of new instances.

Observations – Offline Storage

Underestimated temporary space requirements.

Insufficient cluster design.

Upgrading intermediate storage space to 1 Pbyte system. Long-term storage uses dCache. Good experiences made here so

Connection to HPC systems desired (GFPS) - possible but not

system API.

• Metafile information database or catalog to provide tagging information and allow for categorizing system events like cavity trips, certain kinds of beam loss, SASE intensity loss or any kind of postmortem events defined by a trigger rule.

• Browser tools to access data with tagging functionality for event types (e.g. cavity trip, beam loss) – "quick search".



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