

# Cryomodule-on-Chip Simulation Engine (CMOC)

SLA

NATIONAL

Argonne

ERKELEY I

**Fermilab** Jefferson Lab

C. Serrano, L. Doolittle & V. Vytla



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Implementation



# **Motivation**



#### 2 Physics

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# LCLS-II



#### LCLS-II Feedback



### **LCLS-II LLRF System Architecture**



1.3 GHz Superconducting Cavities

# LLRF Modeling Flow

#### **Analytical Studies**

Determine feedback equations and apply control theory to analyze stability.

#### Software Models

Discretize feedback equations and run feedback numerical simulations to analyze dynamics and behavior.

#### **FPGA Models**

Implement state-space model inside FPGA to exercise controller HDL implementation.

### **Physics**



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### **LCLS-II Cavities**



# Cavity model: 9-cell Cavity ( $\sum \vec{V_{\mu}}$ )



$$\left.\begin{array}{l} R_{\mathrm{g}_{\mu}} = Q_{\mathrm{g}_{\mu}}(R/Q)_{\mu} \\ \\ \omega_{f_{\mu}} = \mathrm{mode}\,\mu\,\mathrm{bandwidth} \\ R_{\mathrm{b}_{\mu}} = Q_{\mathrm{L}_{\mu}}(R/Q)_{\mu} \\ \\ R_{\mathrm{p}_{\mu}} = Q_{\mathrm{p}_{\mu}}(R/Q)_{\mu} \end{array}\right\}$$

Coupling impedance to drive, beam and probe ports

### Cavity model: Single Eigenmode ( $\mu$ )



# **LCLS-II Cryomodule**



# **Cryomodule Model**



#### **Electro-mechanical interactions**



# Requirements



#### 2 Physics



#### 4 Conclusions

# **FPGA Model Architecture**



#### **FPGA Model Implementation**



#### **FPGA Controller Implementation**



### **User Interface**



\*Live Demo with Hardware at Thursday's speakers corner (THSH202).



#### One step closer to final result

FPGA simulation adds FPGA implementation testing before tests with RF.

#### Live simulations

CMOC provides the same level of user interaction as a real cavity and a tool to explore longer time-scale effects.

#### Communications, controls and training

Full framework for communications testing and a test bed for controls integration and training.