

FAST ORBIT CORRECTION FOR THE ESRF STORAGE RING

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Abstract

Up to now, at the ESRF, the correction of the orbit position has been performed with two independent systems: one dealing with the slow movements and one correcting the motion in a range of up to 200Hz but with a limited number of fast BPMs and steerers. The latter will be removed and one unique system will cover the frequency range from DC to 200Hz using all 224 BPMs and the 96 steerers. Indeed, thanks to the procurement of the Liberas Brilliance and the installation of new AC power supplies, it is now possible to access all the Beam positions at a frequency of 10 kHz and to drive a small current in the steerers in a 200Hz bandwidth. The first tests of the correction of the beam position have been performed and will be presented. The data processing will be presented as well with a particular emphasis on the development inside the FPGA.

INTRODUCTION

The ESRF storage ring is a high brilliance source with low emittance values ($\epsilon_x = 4.10^{-9}$ m.rad and $\epsilon_z = 4.10^{-12}$ m.rad) generating Xray from insertion devices installed on 5 m long straight sections. With $\beta_x = 36$ m and $\beta_z = 2.5$ m in the center of the high beta straight sections, the rms beam sizes at the BPMs located on both ends of the straight sections are $\sigma_x = 380\mu\text{m}$ and $\sigma_z = 14\mu\text{m}$. The parasitic motion of the beam due to slow drifts or high frequency vibrations of the quadrupole support girders must be kept at low enough values to avoid spoiling this emittance figure. Two kinds of motions can be observed: very slow drifts and vibrations at 7Hz, 30 Hz and 60 Hz. The amplitude of these vibrations at the ends of the straight sections is 10 μm rms horizontally and 3 μm rms vertically. The components of the correction scheme are shown in figure 1, all BPMs and steerers in place are used by the system.

System Layout

The design of our new system is based on the availability of the Libera Brilliance electronics and an associated "Communication Controller" developed at DLS [1] and using the Libera Rocket I/O ports. This allows the measurement and broadcast of the beam position at 224 locations with a very good resolution at a rate of 10 kHz. We are using the 96 corrector magnets embedded in the sextupole cores to steer the beam, and since the power supplies feeding these magnets are installed in four locations, this particular constraint sets the architecture and topology of this system. Therefore,

the correction computation is placed close to the power supplies and spread over 8 processors, 2 per location.

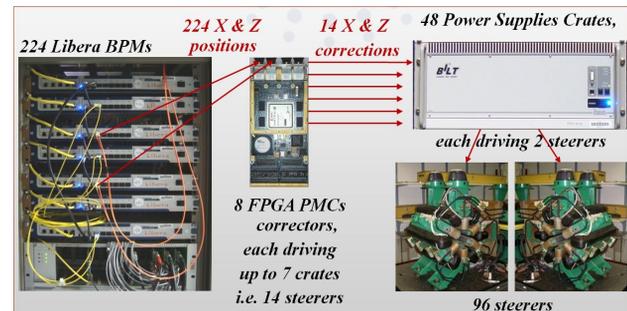


Figure 1: Fast Orbit Feedback components.

Upgrade of the Correctors Power Supplies

The present slow correctors are implemented by 3 pairs of auxiliary coils placed on the yoke of the sextupoles. Using the proper combination of currents in these 3 coil pairs we can produce any combination of vertical and horizontal kicks. The bandwidth of these correctors is affected by the eddy currents in the sextupole core and at the surface of the vacuum chamber since the vacuum chamber all over the storage ring is made of 2mm stainless steel. The inductance of these correctors is also quite large: 0.6H, however, it is possible to achieve, given the small amplitude of the high frequency currents needed for the fast correction, a small signal bandwidth of 500Hz thanks to a proper design of the power supplies. These power supplies are controlled through the Ethernet control system network and an additional trim setting can be added at a rate of 10KHz on 10% of the full dynamic range. This is done through a RS485 port used to input the data in a deterministic way and with a delay limited to 20 μs . Among the features of the power supplies, a diagnostic on each of the 288 channels is available to check the proper functioning of the serial links.

Processing

For the processing, we selected a PMC module with fiber-optic transceivers and a Xilinx Virtex-5 FPGA. The code embedded in this FPGA has several functions:

- 1) Collect the data from the BPMs at 10 kHz with the Communication Controller
- 2) Obtain the parameters from the PCI
- 3) Process the corrections
- 4) Send the set-points to the power supplies
- 5) Send the set-points for data recording with the Communication Controller

The choice of performing the corrections inside the FPGA was driven by the fact that no real-time operating system was supported at the ESRF when we launched this development. One clear advantage of this solution is the efficiency of the FPGA: an interesting feature is the capacity to do parallel computing and also the fact that the time lost transferring the data is very limited since all the functions are performed inside the same component. One particularity of this development is the use of System Generator from Xilinx / The MathWorks to design the part dedicated to the corrections and sequencing of the system. The design can be checked with Matlab / Simulink before the VHDL code generation.

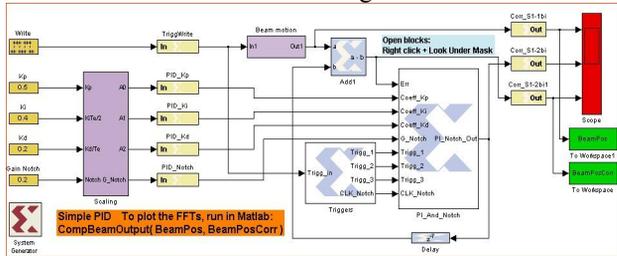


Figure 2: Corrector behaviour test with Simulink.

In order to check the behavior of the data processing at design stage, real data are introduced at the initialization of the simulation and processed with the components from Xilinx and then, output data can be transferred for analysis under Matlab. In the simulation of Fig.2, the power supplies, magnets and vacuum chambers are modeled as a simple delay of 700µs between the correction output and the reading of the beam position.

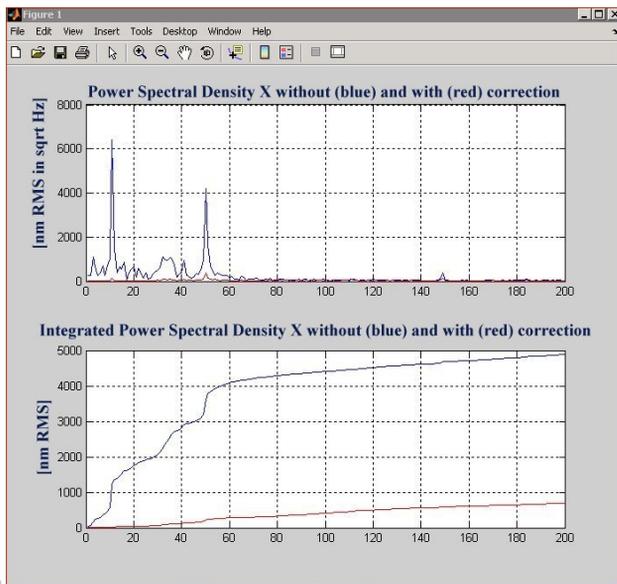


Figure 3: Theoretical results expected from the correctors.

Orbit Correction Algorithm

We derive the orbit correction from the BPM data using a correction matrix obtained from the inversion of the response matrix of the BPMs to each corrector. These response matrixes are inverted using the SVD method. We

use 96 eigen vectors for the inversion of the response matrix. Before starting the 10 KHz correction loop, we will measure the average orbit and set the corrector currents in order to suppress the error measured on this average orbit; these DC values will be applied using the Ethernet input of the power supplies. We will then start the fast correction loop which will add an additional trim current to the DC current set initially. During the first tests of this fast loop, we used a PI algorithm with an additional 50 Hz notch filter aimed to improve the damping of the perturbation at the AC main supply frequency for the 10 KHz iteration of the values of the trim correction currents. Over long periods of operation, the average value of the trim currents may eventually drift up to significant values. In this case, this average current will be added to the setting of the Ethernet input of the power supply, and the average value of the fast trim currents will drop to zero. In this way, if the fast loop is stopped, setting the values of the trim currents to zero will only result in a very small orbit jump and we keep the whole dynamic range for the fast correction.

Diagnostic

In addition to the 8 feedback processor/power supply controller modules, one FPGA PMC board has been added; this board is fully dedicated to diagnostics, and is able to log up to 10s of position and correction data; this board is a duplication of the so called “sniffer” developed for the SOLEIL and DLS orbit control systems[2]. It is also used to detect errors in the data collection by counting the “BPM data not received”.

TESTS

Orbit Correction Test Set Up

All the correctors are now equipped with their new power supplies and connected to the 8 FPGA processors themselves connected to the 224 BPMs.

Tests Results

The commissioning of the whole system started in September 2011 and the results are as expected (Fig. 5) apart from the correction of the 50Hz which is missing and will be performed with an additional notch filter which has already been tested with a reduced setup [3].

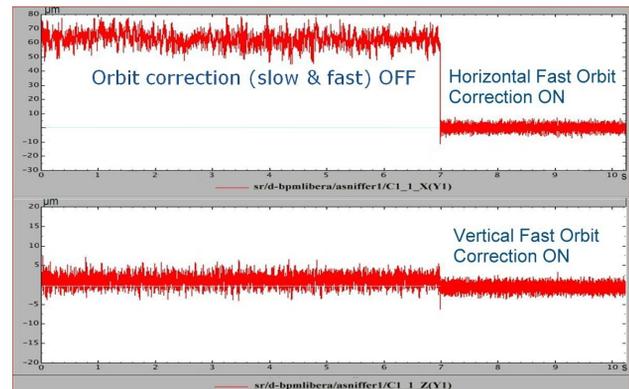


Figure 4: BPM signals with orbit correction OFF and ON.

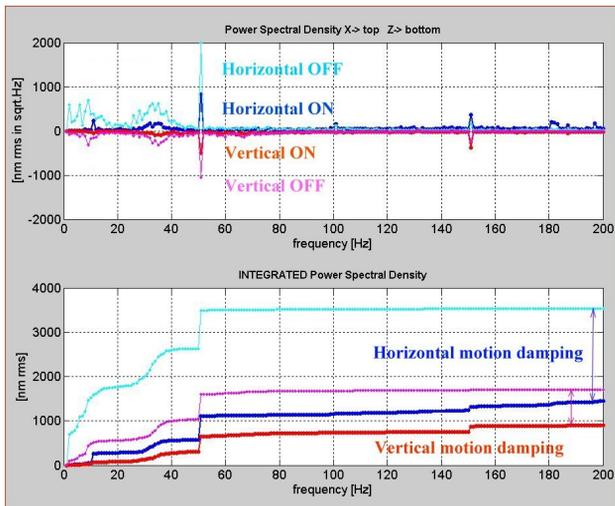


Figure 5: Beam motion averaged over 224 BPMs.

The correction bandwidth was set at 150Hz, and no weighting was applied on the damping time of the upper order eigen vectors of the SVD decomposition. We recorded the positions with the sniffer module. After the suppression of the remaining disturbance at 50 Hz, both horizontal and vertical motion will be reduced to less than 1µm RMS in a 200Hz bandwidth.

The plots of figure 5 show the reduction of the horizontal and vertical BPM signals. It is calculated from an average of the 224 positions.

We have also tested that starting from an orbit already set by a slow orbit correction, turning on or stopping the fast loop was not causing any significant orbit jumps.

Another test has been performed to assess the capability of the correction to drastically reduce the effect of the gap motion on the beam position. In fig. 6 we can see at first a record with no correction, the second part shows the result with a feed-forward control, the efficiency of which depends on the beam parameters, and lastly with the Fast Orbit Feedback “ON”, the beam stays at its position within 1µm during the phase changes.

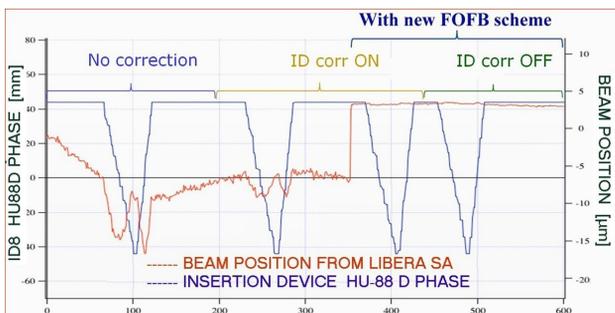


Figure 6: Correction of a beam displacement induced by an insertion device motion (Courtesy of J.Chavanne).

Other Measurements

The resolution of the 10 KHz FA data is 600nm in the range of currents that we store in operation. Such a resolution allows the measurement of SR parameters such as the measurement of the matrix of the response of the BPMs to the corrector current, or the analysis of the SR optics coupling with a very low excitation of the beam and a short acquisition time; applying excitation signals modulated by a sine signal at a well chosen frequency and a narrow bandwidth analysis of the beam response at this frequency, using the method tested at DLS [4], we checked that with a measurement time of 1s, a resolution of 6nm was achieved. We have used the sniffer to record the response of the BPMs to a 40 Hz modulated kick from a horizontal corrector over 1 second. The horizontal response amplitude is 5µm and the vertical response which is due to the coupling of the horizontal and vertical optics is very clean, though its amplitude is only 150nm.

CONCLUSION

We have tested the performance of this system and the damping of the orbit distortion that we achieved fulfils our expectation. There are some outstanding improvements to be made to the design of the correctors as well as to the control with the device servers. Then the next step will be to put this system in operation with a high level of reliability; this part requires special attention to the sequencing and diagnostic of the system. Concerning the development and commissioning, we are on schedule and we expect to put it into operation in spring 2012 at the end of the long shutdown planned for the construction work.

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