

Status of the LHC power converter* controls

* A CERN "power converter" = everyone else's "power supply"

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Context



- The LHC has ~1700 magnet circuits, each driven by a power converter
- Each power converter is controlled by an embedded computer called a Function Generator/Controller (FGC)
- The complete powering system cost 86 MSF + 280 FTE years
- The controls cost 7 MSF + 50 FTE years (8% + 17%)
- 2000 FGCs have been produced and 1700 have been installed in the LHC since 2007
- The LHC experiments also use FGCs to control their magnet power converters







Power Converter



- LHC power converters are current sources
- They are made from a voltage source, current transducer and a Function Generator/Controller (FGC) running a digital current regulation loop



FGC



- Embedded computer designed specifically for LHC power converters
- Microcontroller + Floating point DSP
- Error corrected memory
- 2 ADC channels to measure the current
- I DAC to send the voltage reference to the voltage source
- Digital inputs and outputs to control and monitor the voltage source
- Metal cassette to protect the circuit boards











WorldFIP



- Presented at ICALEPCS 2005
- 2.5 Mbps real-time fieldbus
- Supports:
 - Time and events distribution
 - Commands and responses
 - Real-time publication of status
 - Real-time control of current
 - Software updates
 - Remote Terminal access



- Low throughput per FGC but WorldFIP supports parallel communications, so an application can send commands to all 1700 FGCs simultaneously (120ms for most commands)
- CERN now owns the Alstom WorldFIP intellectual property

Current Regulation



- Digital Regulation uses a proportional-integral-integral controller (PII)
- Implementation uses an RST tri-polynomial algorithm
- Excellent results no overshoot and no tracking errors during the ramps

Status web pages

















FGC Crate











Radiation



- 750 converters are in the tunnel with ~10¹⁰ particles/cm²/year (E>20MeV) expected for nominal LHC operation
- Another 200 converters are in underground areas that will have lower but still significant radiation levels
- The FGCs were designed to tolerate this level and were tested at a 60 MeV cyclotron
- Single event upsets were seen
- Error corrected memory
- Use flash based CPLDs rather than RAM based FPGAs



Single Event Latch-ups



- Latch-ups require a power cycle to clear and none were seen because 60 MeV protons were not sufficiently energetic – we should have used a 250 MeV beam instead
- Tests using the CERN SPS accelerator have now shown that the Xilinx 95 series CPLDs *do* latch-up and even burn out
- Converters in the tunnel are redundant so a power cycle won't lose the beam
- Converters in the underground areas are not redundant so latch-ups will lose the beam
- Tests are on-going and all options are being considered



Summary



- ③ Metal cassette for the FGC
- ③ Wiring free backplane
- ③ WorldFIP Fieldbus
- © XML definition files with a Perl parser
- ③ AJAX based status web pages
- © Perl-Tk expert interface



Conclusions



- The LHC power converter controls are working very well – but the radiation tolerance of the FGCs is still being studied and may be a problem for a small number of converters
- Overall it is a huge system and the effort spent on automatic configuration and diagnostics was well invested

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