# Development of the Model of a Self Excited Loop

Introduction Development of model in digital domain RF Power System Limiter Controller Loop Phase Shifter Test Results Initial Experiments

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A convenient starting point for subsequent amplitude and phase locking:

- Smooth adjustment across the resonance curve using the Loop Phase Shifter.
- Immune to electro-mechanical instability



SEL WITH AMPLITUDE & PHASE LOCK

## SEL

A number of implementations existwith low level signal processing in analog domain

### > At BARC-TIFR

development of SEL based LLRF for three Superconducting heavy ion LINACs

- Pelletron Linac Facility, Tata Institute of Fundamental Research, Mumbai (150 MHz, Quarter wave, Pb on copper)
- Inter University Accelerator Centre, New Delhi (97 MHz, Quarter wave, Nb)
- Australian National University, Canberra (150 MHz, Splitloop, Pb on copper)

Self Excited Loop based on digital technology - under development First step: develop a computer model

## MODEL DEVELOPMENT RF Power System

### **Equivalent Circuit**



#### **Resonator field amplitude 'V'**

V=(I,Q) ; complex phasor

1) high power RF amplifier transmission line

2) beam current

**High-Q**;  $\Delta \omega = (\omega_0 - \omega) \ll \omega_0$ 

$$\tau_0 \frac{\mathrm{d}V}{\mathrm{d}t} + (1 + \mathbf{j}(\omega - \omega_0)\tau_0)\mathbf{V} = \mathbf{V}_{\mathrm{ext}}$$

$$\frac{\mathrm{d}\mathbf{I}}{\mathrm{d}t} = -\frac{1}{\tau}\mathbf{I} - \Delta\omega\,\mathbf{Q} + \frac{1}{\tau}\frac{\beta}{\beta+1}(2\mathbf{V}_{\mathrm{fi}} + \mathbf{Z}_{0}\mathbf{I}_{\mathrm{bi}})$$

$$\frac{\mathrm{d}Q}{\mathrm{d}t} = -\frac{1}{\tau}Q + \Delta\omega I + \frac{1}{\tau}\frac{\beta}{\beta+1}(2V_{\mathrm{fq}} + Z_0 I_{\mathrm{bq}})$$

### Model - RF Power System



## Limiter

### Modeled as a Feedback loop



Limiter

Choice of compensator- main design issue

Concern Good steady-state and transient response

Two loops – free running SEL Outer loop – low pass filter Limiter inner loop – high pass filter Composite system – Gain < unity at all frequencies

Adequate band-width with high enough steady state gain

Solutions Fast and precise signal processing

FPGAs Fast- Faster devices, Parallelism (25ns) Precise- Bus-widths as per the requirement (56bit)

#### **Limiter:** For its simplicity, we have chosen K ----- as compensator 1- a z<sup>-1</sup>



### **Linearised System**



**Frequency Response** 

**Pulse Response** 

### Limiter: Dynamic



## Controller

#### SEL dynamics:

$$\tau \frac{dV}{dt} + V = in - phase$$
 Drive  
 $\tau(\omega - \omega_0)V = quadrature Drive$ 

V: amplitude of RF in the resonator

- 1. Phase Control modulation of Quadrature drive
- 2. Amplitude control modulation of In-phase drive

#### Low beam current machines Variation of $\omega_0$ – dominant disturbance ensure electromechanical stability counteract cross-couplings

## Controller



## **Phase Shifter**

**Co-ordinate Rotation** 

$$I_{o} = I_{i} \cos(\phi) - Q_{i} \sin(\phi)$$
$$Q_{o} = I_{i} \sin(\phi) + Q_{i} \cos(\phi)$$

 $\boldsymbol{\phi}$  - the phase shift across the phase shifter

### **Resonator Model: Electro-mechanical**

Electro-mechanical modes excited by: resonator field and micro-phonics

$$\frac{d^2\omega_{\mu}}{dt^2} + \frac{2}{\tau_{\mu}} \cdot \frac{d\omega_{\mu}}{dt} + \Omega_{\mu}^2 \omega_{\mu} = -K_{\mu}\Omega_{\mu}^2 V V^* + \eta(t)$$

Where, 
$$VV^* = I^2 + Q^2$$

Instantaneous Resonant Frequency  $\omega_{c} = \omega_{co} + \sum \omega_{\mu} + \omega_{ex}$ 

Detuning 
$$\Delta = \omega_c - \omega$$



Low Frequency System Model: Linearized

### Mathlab/Simulink A sample run

the compensator in the limiter loop =  $.04/(1 - 0.999 z^{-1})$ , the computational delay in the feedback loop of limiter = 4 samples, sampling time= 25 nano-seconds



## **Initial Experiments**



SEL realised using An FPGA development Kit: Altera Cyclone III With a normal conducting resonator: loaded  $\tau = 12 \mu sec$ 

## **Self-Excited Oscillations**



Horizontal-axis: 10 μsec/divGreen trace: system resetYellow trace: resonator pick-up

## Conclusion

The model – simulation useful to understand the behaviour of an SEL and feedback control

The results obtained from the model and initial experiments are encouraging

Processing speed and precision

Development and implementation of a SEL based control in digital domain

## **Thanks!**

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