





Development of a Low-latency, High-precision, Intra-train Beam Feedback System Based on Cavity Beam Position Monitors

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Outline

- Introduction
 - Feedback at a linear collider
 - International Linear Collider
 - Feedback on Nanosecond Timescales
- Experimental setup at Accelerator Test Facility
- Cavity beam position monitor signals
- Modes of feedback operation
- Results

Introduction Feedback at a Linear Collider

- Successful collision of bunches at a linear collider is critical
- A fast position feedback system is required



Misaligned beams at interaction point (IP) cause beam-beam deflection

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Correct orbit of next bunch (correlated to previous bunch due to short bunch spacing)

Introduction International Linear Collider (ILC)

- Proposed linear electron-positron collider
- Centre-of-mass energy: 250-1000 GeV
- Vertical beamsize: 5.9 nm
- Bunch separation: 554 ns



- Test bed for the International Linear Collider
- Facility located at KEK in Tsukuba, Japan
- Goals:
 - 37 nm vertical spot size at final focus
 - Nanometre level vertical beam stability







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Feedback system



- ATF can be operated with 2-bunch trains in the extraction line and final focus
- The separation of the bunches is ILC-like (tuneable up to ~300 ns)
- Our prototype feedback system:
 - Measures the position of the first bunch
 - Then corrects the path of the second bunch
- Train extraction frequency: ~3 Hz

Introduction Feedback on Nanosecond Timescales (FONT)

- Low-latency, high-precision feedback system
- We have previously demonstrated a system meeting ILC latency, BPM resolution and beam kick requirements
- We have extended the system for use at ATF
- We aim for nanometre level beam stabilisation



P Stripline BPM



- 12 cm long strips
- 12 mm radius
- On x and y mover system





- Analogue: latency 13 ns
- Resolution of 330 nm
- Details in poster TUPME009







- C-band: 6.4 GHz in y
- Low Q: decay time < 30 ns
- Resolve 2-bunch trains





- Analogue, 2-stage downmixer
- Resolution of < 100 nm
- Developed by Honda et al.







- 9 ADC channels at 357 MHz
- 2 DAC channels at 179 MHz
- Xilinx Virtex 5 FPGA







- Made by TMD Technologies
- ± 30 A drive current
- 35 ns rise time (90 % of peak)



Local upstream feedback results presented in poster TUPME009





- Vertical stripline kicker
- 30 cm long strips for K1 & K2
- 12.5 cm long strips for IPK

Cavity BPM Signal Processing



IPB cavity Dipole mode frequency (in y) ~6426 MHz Reference cavity Monopole mode frequency (in y) ~6426 MHz

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Cavity BPM Signal Processing



simplified schematic

The IPB and reference cavity signals are downmixed using a common, external 5712 MHz local oscillator (LO)

Cavity BPM Signal Processing



The IPB signal is downmixed using the reference cavity signal as LO The I and Q output signals at baseband are used to obtain the beam position



- Use position at P2 & P3 to correct position at IPB
- Correction calculated locally, then sent along 60 meters of cable
- Latency: 202 ns
- Effect measured at IPB









- IPB position is used to drive the local kicker IPK
- Latency: 212 ns
- Effect measured at IPB







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Conclusion

- Demonstrated low-latency, high-precision, intra-train feedback systems
- Cavity BPM feedback latency: 212 ns
- Achieved beam stabilisation at the ATF IP in 2 modes:
 - Feedforward: ~100 nm
 - IP feedback: ~100 nm

Thank you for your attention!

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