

ILC MARX MODULATOR DEVELOPMENT PROGRAM STATUS*

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Abstract

A Marx-topology klystron modulator is under development for the International Linear Collider (ILC) project [1]. It is envisioned as a lower cost, smaller footprint, and higher reliability alternative to the present, bouncer-topology, baseline design. The application requires 120 kV (+/-0.5%), 140 A, 1.6 ms pulses at a rate of 5 Hz. The Marx constructs the high voltage pulse by combining, in series, a number of lower voltage cells. The Marx employs solid state elements; IGBTs and diodes, to control the charge, discharge and isolation of the cells. Active compensation of the output is used to achieve the voltage regulation while minimizing the stored energy. The developmental testing of a first generation prototype, P1, has been completed. This modulator has been integrated into a test stand with a 10 MW L-band klystron, where each is undergoing life testing. Development of a second generation prototype, P2, is underway. The P2 is based on the P1 topology but incorporates an alternative cell configuration to increase redundancy and improve availability. Status updates for both prototypes are presented.

INTRODUCTION

The ILC will require 576 Rf stations. Each 10 MW L-band klystron will require a modulator capable of 120 kV, 140 A, 1.6 ms (27 kJ) at a 5 Hz repetition rate. The baseline klystron modulator employs a transformer-based topology. The large size, weight, and cost of this

transformer, owing to the long pulse length, have motivated research into alternative topologies that do not employ power magnetics.

DESIGN OVERVIEW

The reliability/availability requirements for ILC systems mandate the use of solid state switching elements to control the klystron modulator output. The Marx topology provides an approach to array solid state switches to the voltage and power levels required for this application. A simplified schematic of the Marx topology selected for the ILC application is shown in Fig. 1. The Marx is composed of cells, which form the basic Power Electronics Building Block (PEBB) [2]. Each cell contains an energy storage capacitor, an IGBT switch to control the discharge of the capacitor (discharge path shown in green), and an inductor to limit di/dt in the event of a fault. A second IGBT switch and the diodes provide the path to charge the energy storage capacitor, and the auxiliary power supply (both paths shown in red), of all the Marx cells in parallel while isolating these paths during the series discharge of the Marx. A beneficial attribute of this configuration is that cells can be bypassed during discharge (e.g. left cell in Fig. 1), which allows cell turn on to be delayed for pulse shaping, or omitted if the cell has malfunctioned. There are several variations on this topology, however the design illustrated in Fig. 1 is used for both the P1 and P2 designs.

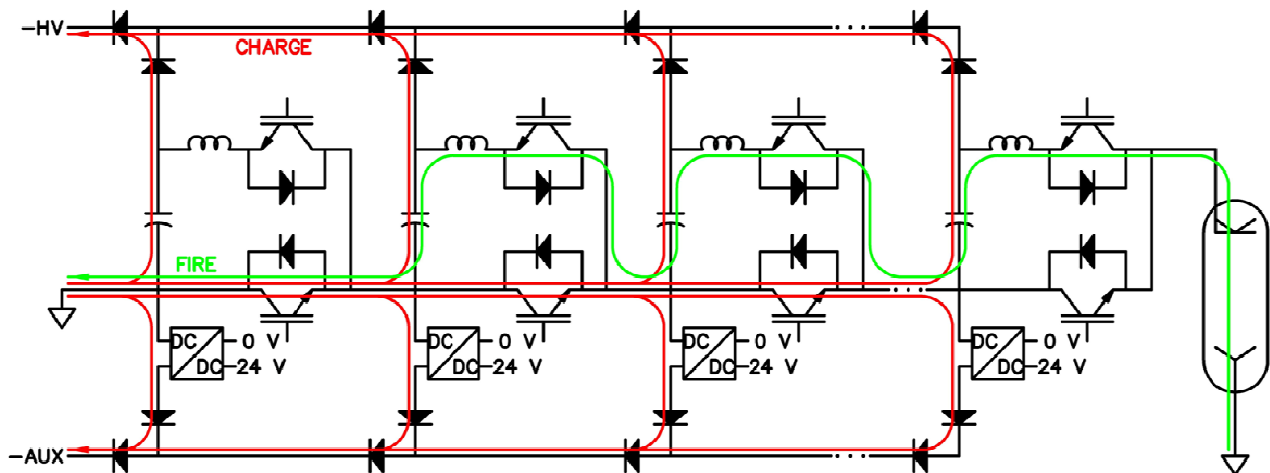


Figure 1: Simplified schematic of the ILC Marx modulator topology (4 cells). The charging current paths are shown in red; HV charging along the upper path, auxiliary along the lower. The discharge current path is shown in green.

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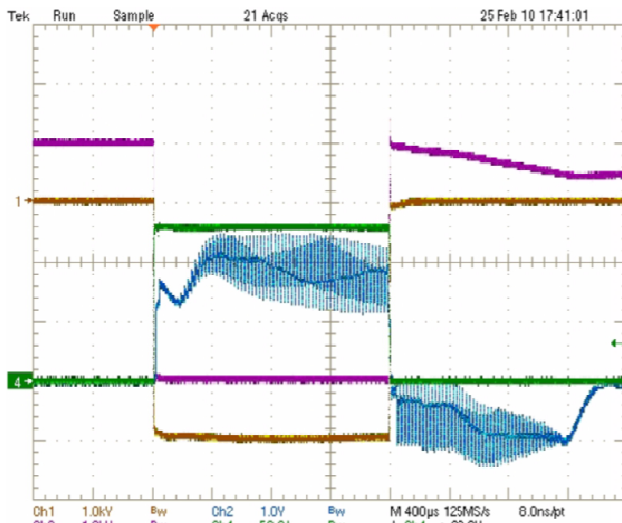


Figure 5: P2 cell output voltage regulation with a resistive load, load voltage (Ch1), PWM filter inductor (L2) current (Ch2), main switch (Q1) voltage (Ch3), and load current (Ch4).

This regulation approach is illustrated in Fig. 5, obtained while operating a single cell with a resistive load. The PWM switch operates at 40 kHz as the pulse width is varied such that the current through the filter inductor ramps the voltage on the filter capacitor Cf1 as C1 discharges and the sum of the two voltages remains constant. The results shown were achieved under open-loop control, which produces some perturbations on the output that will be eliminated when closed-loop operation is implemented. Incorporating the voltage regulation into each cell, all cells are then identical (unlike the P1), and provides true redundancy. With 32 cells, N+2 redundancy is achieved, which will promote high system availability. The specifics of the cell design, and life and availability estimates, have been presented previously [6]. A conceptual design of the P2 is shown in Fig. 6. Each of the 32 cells slide into the support structure and can be easily removed for service. In addition to physical support, this structure provides the electrical interconnection between cells, flow channels for air cooling, and field shaping elements to control the electrostatic fields. The field shaping elements are essential to achieving a compact modulator. The maximum electric fields within the enclosure are less than 18 kV/cm. The enclosure is 2.6 m long, 1.4 m deep, by 2.2 m tall, approximately 20% smaller than the P1.

The prototype cell testing is nearing completion. Fabrication and testing of the complete modulator will take place in 2011.

CONCLUSIONS

A Marx-topology modulator has been successfully developed to meet the ILC klystron modulator requirements. The first generation prototype, P1 Marx, is undergoing life testing at SLAC. A second generation prototype, P2 Marx, with an increased service life is under development.

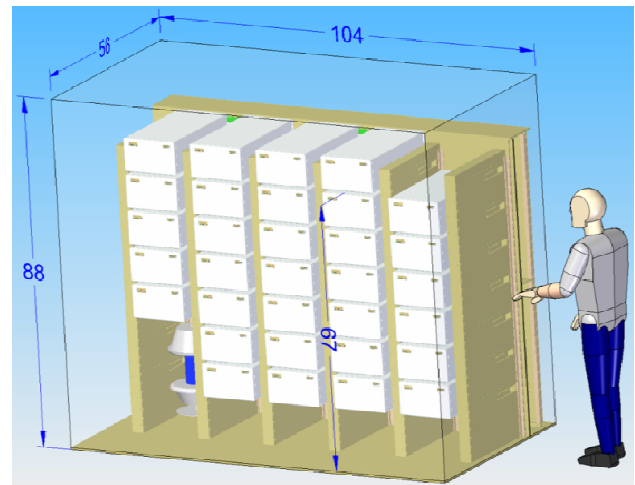


Figure 6: Conceptual design of the P2-Marx.

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REFERENCES

- [1] ILC Reference Design Report, <http://www.linearcollider.org/cms/?pid=1000437>
- [2] T. Ericson, "Power Electronic Building Blocks - A systematic approach to power electronics," in Proc. IEEE Power Eng. Soc. Summer Meeting, Seattle, WA, 16-20 July 2000, pp. 1216-1218.
- [3] C. Burkhart, T. G. Beukers, R. S. Larsen, M. N. Nguyen, J. Olsen, T. Tang, "ILC Marx Modulator Development Program Status," Linac 08 Conference, Sept 29 - Oct 3, 2008, Victoria, Canada, <http://trshare.triumf.ca/~linac08proc/Proceedings/>.
- [4] Tang, T.; Burkhart, C.; Nguyen, M.; , "A vernier regulator for ILC Marx droop compensation," *Pulsed Power Conference, 2009.*, pp.1402-1405, June 2009 URL: <http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5386221&isnumber=5386092>
- [5] Macken, K.; Burkhart, C.; Larsen, R.; Nguyen, M.N.; Olsen, J.; , "A hierarchical control architecture for a PEBB-based ILC Marx modulator," *Pulsed Power Conference, 2009. PPC*, pp.826-831, June 2009 URL: <http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5386259&isnumber=5386092>
- [6] Macken, K.; Beukers, T.; Burkhart, C.; Kemp, M.A.; Nguyen, M.N.; Tang, T.; , "Design considerations for a PEBB-based Marx-topology ILC klystron modulator," *Pulsed Power Conference, 2009*, pp.811-816, June 2009 URL: <http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5386366&isnumber=5386092>