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ABSTRACT:

In the IFMIF EVEDA project [1], INFN-LNL Laboratory has been involved in the design and construction of a normal conducting Radio Frequency Quadrupole (RFQ) used to bunch and accelerate a 125 mA steady beam to 5 MeV. The EPICS based control system has been entirely developed in house using different hardware solutions: PLC for tasks where security is the most critical feature, VME system where the acquisition speed rate is crucial, common hardware when only integration is required without any particular feature in terms of security. Integration of PLCs into EPICS environment was originally accomplished through OPC DA server hosted in a Windows embedded industrial PC. Due to the issues analyzed in injector LCS, LNL proposed to migrate to the usage of EPICS Direct Driver solution based on s7plc. The driver itself is suitable for direct communication between EPICS and PLCs, but it doesn't take care of data update and synchronization in case of communication failure. As consequence LNL team designed a dedicated method based on state machine language to manage and verify data integrity between the two environments, also in case of connection lost or fail.

migration due to reliability issue

How can I have

bidirectional write

access with s7plc?



OPC Configuration

Windows embedded industrial PC IPC427) hosted (Siemens the Simatic Net OPC server, IOC application and device support for the OPC server itself to monitor and control about 1100 PVs organized as one unique group with a refresh rate of 1s. The device support module used is the one developed at Helmutz Zentrum in Berlin (HZB).

PLC DEVELOPER

Istituto Nazionale di Fisica Nucleare	Private RFQ Control System Services – Main Server		s7plc Configuration	
IFMIF		SoftIOC Server	Software IOC (softIOC)	



running on a virtual machine

- PLC subnetwork separated by EPICS network by virtualized firewall
- PLC and EPICS IOC communication via Send/Receive over TCP/IP

PLC DEVELOPER LCS s7plc Architecture s7plc OUT OF THE BOX



PRO:

Identification of the exchanged variables in PLC memory

CONS:

- Bidirectional read/writes access not supported
- Memory consumption
- Increased execution time
- Exchange of big data buffer can takes several PLC cycle

LCS OPC Architecture

PRO:

- Complete PLC memory addressing
- Configurable variable access rights (read, write and read/write)
- Bidirectional read/writes access and multiclient architecture
- Separation between PLC sub-network and the rest of control network
- Data access if required (upon change)

CONS:

- Identification of the exchanged variables in PLC memory \bullet
- Overwriting of the PLC data in case of IOC reboot
- Detected freezed PVs at IOC level

IOC Itegrity Check

Data exchange between EPICS and PLC

PLC Implementation



Buffer Size PLC Sockets Variables [Bytes] Fast 2260 1361 Vacuum

Migration from OPC DA to s7plc required several efforts on both side (EPICS and PLC), especially to keep bidirectional variable access, but it pays back us with:

Requirements:

- PLC process control data must be correct at each moment including IOC reboot, no connection, etc.
- Naming and organization of exchanged variables (UDT CIConf, CICmd, CIStates)
- Link the variable to the send receive buffer in accordance to the connection status
- Support bidirectional write access at list for CIConf and CICmd



Results:

- SND/RCV buffers common structure with header containing information to manage communication
- SND/RCV communication Function Block to manage data between process and buffers in each connection and communication status

LCS s7plc Architecture s7plc AS IMPLEMENTED

PLC DEVELOPER ~ •

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PRO:

- Identification of the exchanged variables in PLC memory \bullet
- Easy export of send/receive DB to EPICS data base
- Consistent data transfer and initialization procedure Bidirectional writes access for a supported by dedicated procedure for a defined subset of variables Several connections can be arranged according the exchanged data functionality or scan rate

	Total	2023	6010
	Skid Slow	623	1592
	Skid Fast	363	1388
	Slow	380	998
Cooling	Fast	657	2032
	Total	2131	4382
	Slow	770	2122

- Consistent data transfer and initialization procedure
- Well organized communication method on PLC side
- Possibility of define several connections respect \bullet the exchanged data functionality or scan rate
- Easy export of process data DB to EPICS data \bullet base
 - **RFQ LCS Team**



CONS:

- Bidirectional read/writes access not supported for all the variables
- Memory consumption
- Increased execution time \bullet
 - Exchange of big data buffer can takes several PLC cycle

IFMIF-EVEDA RFQ at Rokkasho





INFN-LNL: http://www.lnl.infn.it EPICS: http://www.aps.anl.gov/epics EPICS@LNL: https://web2.infn.it/epics/

