Em# Platform: towards a hardware interface standardization scheme

O. Matilla, J. Avila-Abellan, M. Broseta, G. Cuní, D. Fernández-Carreiras, A. Ruz, J. Salabert, X. Serra-Gallifa, ALBA-CELLS Synchrotron, Cerdanyola del Vallès, Spain

Em# is the 4-channel electrometer evolution of the in-house developed ALBA Em which has been widely used at ALBA from 2011. In 2013 a redesign of the equipment raised from the increase of needs in new experimental applications. The new approach to the project is based on hardware interface standardization allowing a more efficient parallel tasking and an easier involvement of different development groups. The new instrument is designed for being capable of independent channel trigger, FPGA based real time processing, feedback implementation via its four analog DACs in the kHz's range or acquisition under 1kV ground bias voltage.

EM# 4-CHANNEL ELECTROMETER New Hardware Approach: do not select the components, select SPECIFICATIONS THE INTERFACES Eight independent ranges Based on the selection of expected long-life interfaces for communication Current (from 100pA to 1mA) and five between modules. Then, modules are selected to fulfil the interface and Amplifier 2nd order analog filters (from specification requirements. In case of module obsolescence, its replacement 0.1Hz to full bandwidth) implies lower redesign effort than in full custom hardware. ADC 4x 400kS/s @18 bits SAR Ground Up to 1kV Voltage Bias Analog 4x ±10V 100kS/s @16 bits Outputs Ethernet PCle FMC Trigger In 1 x CMOS/TTL compatible 4x configurable Input/Output High-Speed 100MHz BW (can be used as 1/0 independent channel triggers) 9x Input/Output @5V 20MHz General I/O (unipol/diff) + 4x 5V output Intel NUC DE3815TYBE OHWR SPEC **Custom Electronics** max 500mA **Current Measurement with Voltage Bias** Integrated Control System **Flexible Dual Bus Implementation** PCIe 1/0 9x RS-485 I/O DAC 4x DAC Outp Current In 1 MATH PID Current In 3 **OS Embedded Linux** Current In 4 Management of communication SDB standard implementation vel (DC-1KHz Wishbone Bus for configuration protocol (remote control) ratio Data Bus for high speed transfer Implementation of high-level Flexible Data Processing. Higherequipment functionality 100 pA Range 10 nA Range 1 µA Range grade applications possibility: Efficient Driver installation Feedback, Close Loop **Control System Integration** Big fast storage capacity: 32MS considering whole architecture ALBA Current Amplifier excellent performance . OHWR FMC with isolated 4ch 18b 400kS/s Status of the project ADC withstanding up to 1kV isolation Digital I/O and Analog Output availability for Working Prototype with basic advanced applications: Feedback, Close Loop functionality and improved resolution Project in the final stage of HW design Development Collaboration Agreement has been signed between ALBA and MAXIV to **Next Steps** foster project development. The inclusion of other institutes is open and welcome.

• First working series planned to be produced on January 2016

LB

 Development focused on software and firmware

> omatilla@cells.es www.albasynchrotron.es

