

NSLS-II Beam Diagnostics Control System



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<u>Abstract.</u> A correct measurement of NSLS-II beam parameters (beam position, beam size, circulating current, beam emittance, etc.) depends on the effective combinations of beam monitors, control and data acquisition system and high level physics applications. This paper will present EPICS-based control system for NSLS-II diagnostics and give detailed descriptions of diagnostics controls interfaces including classifications of diagnostics, proposed electronics and EPICS IOC platforms, and interfaces to other subsystems. Device counts in diagnostics subsystems will also be briefly described.

Introduction

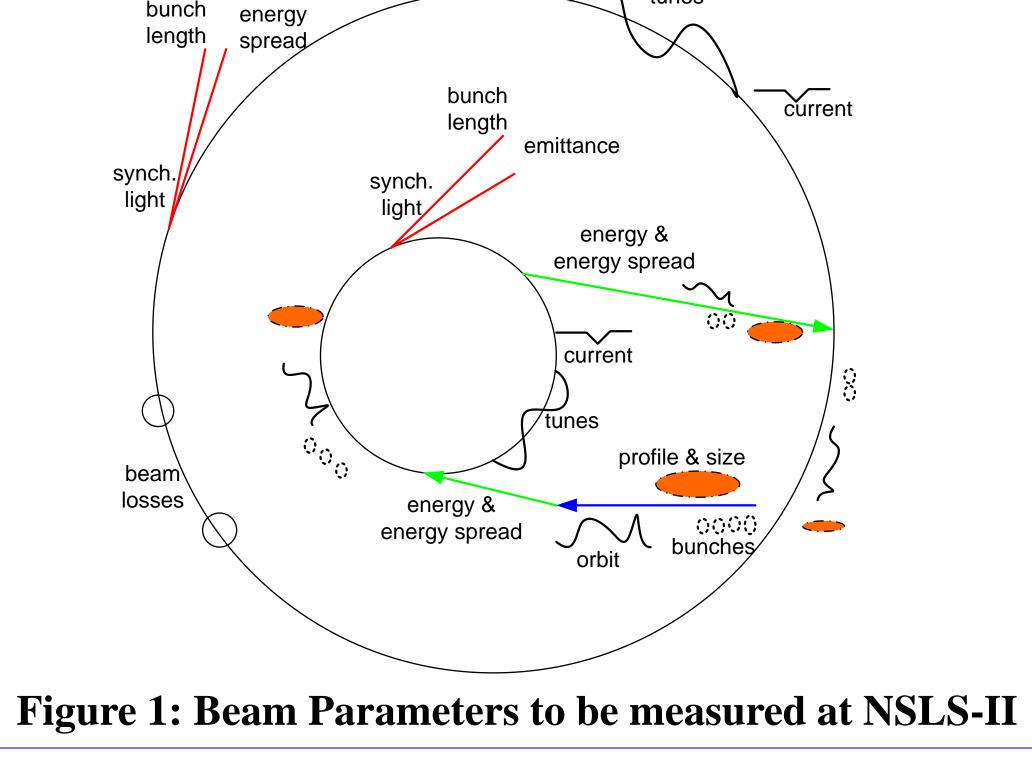
The NSLS-II beam diagnostics and control system is designed to monitor the electron beam of NSLS-II accelerator complex. The beam quality is measured by a variety of parameters such as bunch charge, bunch structure (filling pattern), beam position/orbit, beam size/profile, energy & energy spread, circulating beam current, tunes, beam emittance, bunch length and beam losses. Figure 1 briefly shows the beam parameters to be measured from Linac to Storage Ring.

Device Counts in Diagnostics

The NSLS-II accelerators consist of one injector and one storage ring (SR). According to the functionality as well as geographical distribution, the injector is divided into 4 subsystems: Linac, Linac to Booster (LtB) transfer line (including 2 beam dumps), Booster, Booster to Storage ring (BtS) transfer line (including 1 beam dump). Table 1 gives a summary of the diagnostic monitors distributed over the whole machine. Although Linac and Booster are turnkey solutions provided by vendors, BNL will specify the requirements for diagnostics and controls and vendors implement them. For better standardization and maintenance, the same type of diagnostics (e.g. CCD) cameras) used in different accelerator subsystems will be provided by the same manufacture (e.g. GE1290 by Prosilica) so that they almost have the same control interfaces and requirements.

 Table 1: Device Counts in NSLS-II Diagnostics

	Linac	Lth	Booster	BtS	SR
WCM	5				
Screen/Flag	6	9	6	9	3
BPM	5	6	37	7	240
Bergoz FCT		2	1	2	
BergozICT		2		2	
Energy Slit		1		1	
Faraday Cip	1	2		1	
BergozDCCT			1		1
Streak Camera					1
Visible Light Monitor			1		1
Pinhole system					2
Ame Monitor			1		1
Transverse Feedback System					1
Beam Loss Monitor					5
Beam Scrapers					5
Photon/x-ray BPM					2per front-end



Controls Interfaces for Diagnostics

Diagnostics controls are actually more about data acquisition (DAQ) than device control. Diagnostics control subsystem will conform to NSLS-II control system standards. It will be EPICS-based and the preferable operating systems for IOCs are RTEMS (Real-Time Executive for Multiprocessor Systems) and Linux/Debian.

Classifications of Control Interfaces

From point view of controls, the beam monitors output signals/interfaces can be classified into the following several groups.

Table 2: Diagnostics Electronics and IOC Platform

Beam Monitor	Diagnostic s Electronics	IOC platform	
WCM & FCT & FC DCCT & ICT	Acqinis DC252 (2GHz bw, 10-bit,4~8GS&)	c PCL/Limx	
	1)GE ICS-710-A (24-bit, 200KS/s,8-ch)		
	2) Allen-Bradley PLC (DAC, Digital I/O)		
BPM	In-house BPM receiver [4]	PCLimix	

- 1) Analog output with high-bandwidth (>500MHz): WCM, FCT, etc.;
- 2) Analog output with low-bandwidth (<10KHz): DCCT, ICT&BCM;
- 3) Simultaneous 4-channle RF signals: BPM;
- 4) Gigabit-Ethernet camera interface: pinhole camera, flag/CCD, streak camera etc.
- 5) Stepper motor driven: linear stage in pinhole system, energy slit, beam scraper, etc.
- 6) Ethernet-based instrument: Windows XP-based network/spectrum analyzer for tune monitor and beam stability monitor;
- There are other miscellaneous I/Os for diagnostics: binary input/output including TTL I/O for DCCT range settings, pneumatic actuator with limit switch in flag, limit switch in stepper-based stage, 24 V binary outputs for XIA filter inserter in pinhole system, 12-bit DAC for illuminator control in pinhole and flag system, temperature sensors for diagnostics beamline mirror, etc.

Controls and Data Acquisitions for Diagnostics

Each type of beam monitor requires electronics (device controller) to process its output signal. The electronics for the above groups and associated EPICS IOC platform are listed in Table 2. Figure 2 shows the controls interfaces for these various beam monitors.

Prosilica GigE Camera	PC/Limix	PC/Limx	
Stepper- motor-based	Delta Tau GeoBrick LV PC	PCLimx	
Instrument controls	Windows-based network/spectrum analyzer	PCLimx	
Misc: digital 1/0, DAC, temperature sensor	Allen-Bradley PLC	PCLimix	

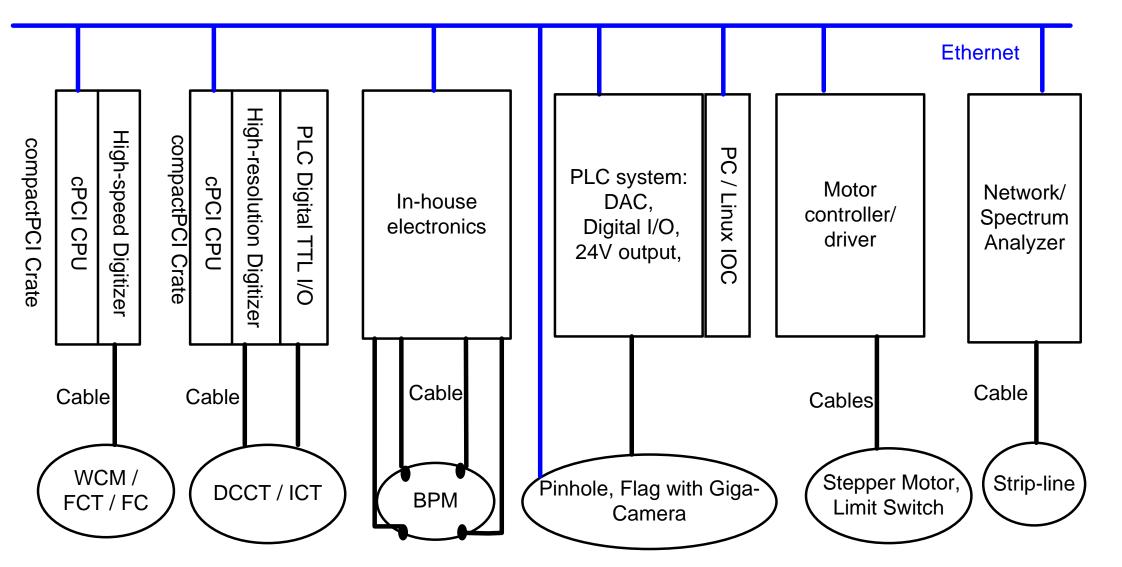
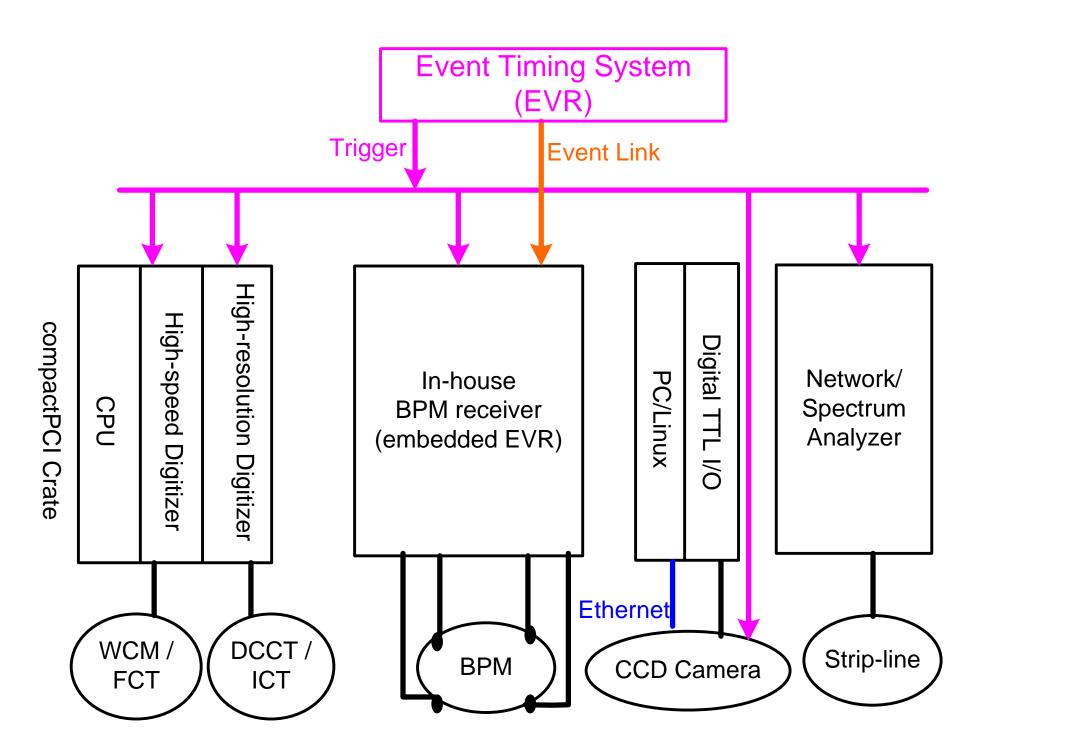


Figure 2: Diagnostics Controls Interfaces



Interfaces to Other Subsystems

Interfaces to Timing System

To capture the electron beam signal at the right time, the beam monitors should be sampled and synchronized to the passage of the beam. This function can be achieved by using Event Timing System to deliver delayed-trigger or clock signal to the diagnostics electronics.

The interfaces between diagnostics controls and timing system are shown in Figure 3. Some diagnostics controls, such as stepper motor, limit switch and DAC, don't need timing while other diagnostics electronics require trigger signals from Event Timing System (EVR). Additionally, the reference for BPM receiver Machine Clock should be provided at the Booster/Storage Ring revolution frequency. For transverse bunch-by-bunch feedback system and streak camera system, trigger/clock signals are the only control signals.

Interfaces to Machine Protection System

The diagnostics controls should send hardwired interlock signals, to machine protection system (MPS) if any specific parameter, such as beam positions, beam loss rate, beam current, is out of range.

The design of the interfaces between diagnostics controls and MPS are still in progress. Fig. 4 shows the draft diagram of the system.

The followings are some cases that diagnostics interlock protection should take action:

- 1) BPM position data are out of pre-defined position range;
- 2) For top off operation, the stored beam current must exceed 50mA;
- 3) Excessive beam loss is detected by LCM system;

Figure 3: Diagnostics Interface to Timing System

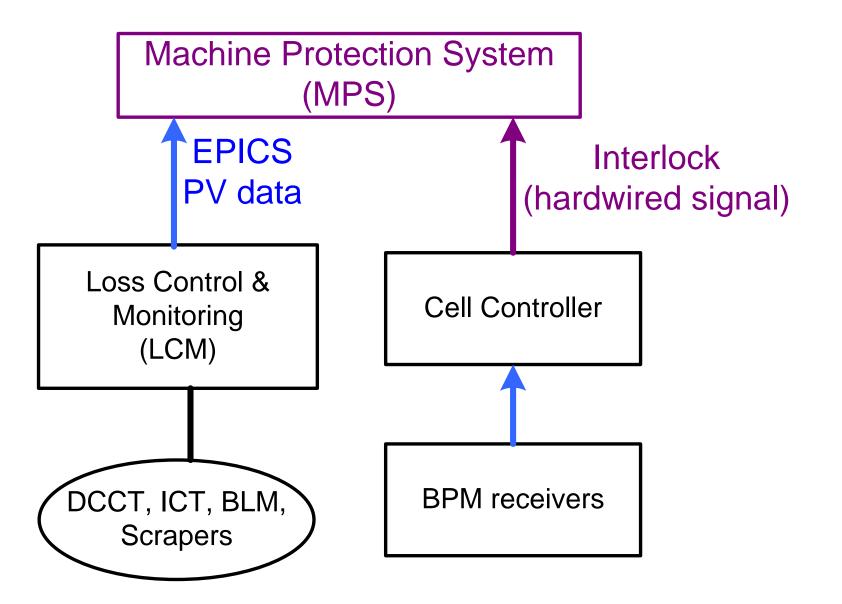


Figure 4: Interface to Machine Protection System