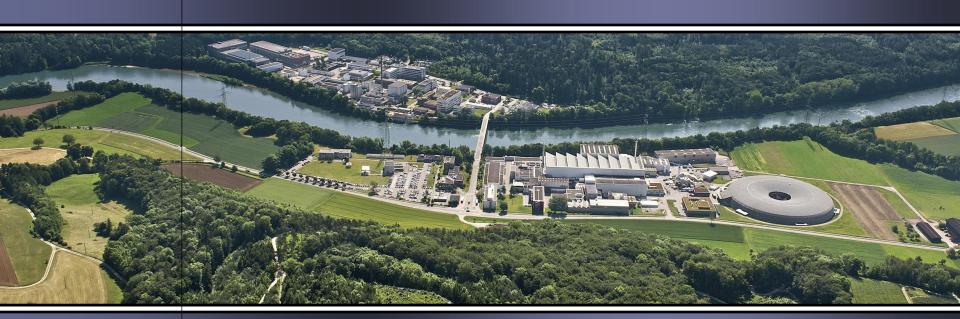


Cavity BPM Activities at PSI

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Paul Scherrer Institut

For the PSI Beam Based Feedbacks Group



Introduction

PSI Provides:

- Undulator & TL cavity BPM electronics for
 - FLASH-2: 22 BPMs, 1st beam 1/2014.
 - E-XFEL: ~140 BPMs, 1st beam 2015.
 BPMs also used for transverse Intra Bunch train feedback "IBFB".
- Cavity BPM system for SwissFEL: ~170 BPMs,
 1st beam 2016, 3 types/apertures.

BPM Requirements/Specs

	SwissFEL			E-XFEL	
Parameter	ВРМ38	BPM16	BPM8	IBFB/TL	Undulator
Quantity	6	114	50	18	117
Length	255 mm	100 mm	100 mm	100 mm	255 mm
Inner Aperture	38 mm	16 mm	8 mm	40.5 mm	10 mm
Pos. Range	±10 mm	±5 mm	±1 mm	±1 (10) mm	
Pos. Noise	<10 µm*	<5 µm*	<1 µm**	<1 (10) µm**	
Drift/Week	<10 µm	<5 µm	<1 µm	<1 (10) µm	
Charge Noise***	<0.1%				
Charge Range	10 - 200 pC			20 - 1000 pC	
#Bunches/Train	1 - 3		1	2700	
Train Rep. Rate	100 Hz			10 Hz	
Bunch Spacing	28 ns		-	222 ns	
Latency	<1 ms		<400 ns		

^{*} Within 30% of max. range.

^{**} Within 50% of max. range.

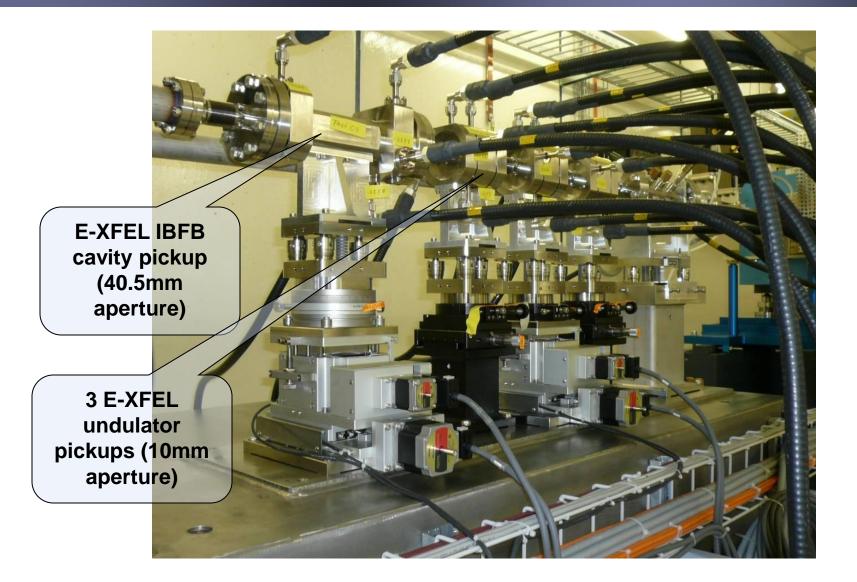
^{***} Or 30fC, whatever is larger.

BPM Requirements/Specs

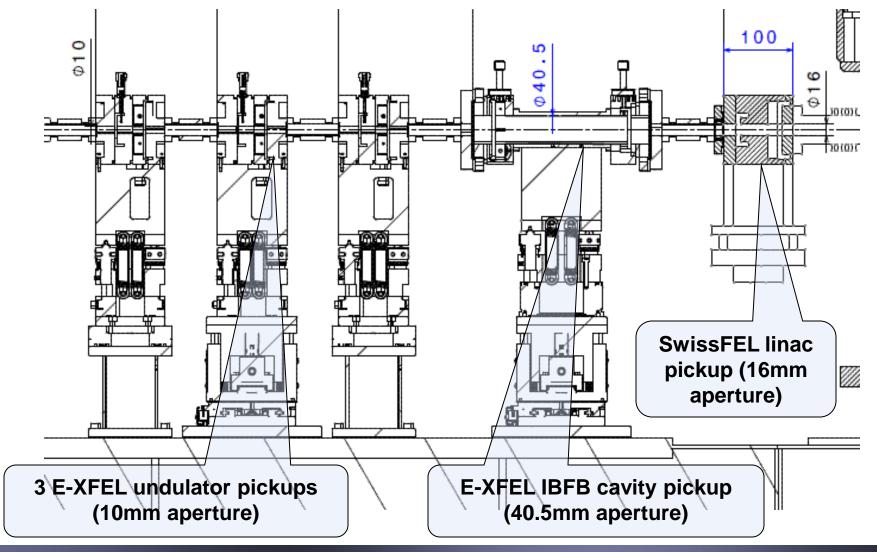
Comments:

- E-XFEL & SwissFEL use normal BPMs to measure beam energy (BC, dump, collimator, ...) via dispersion. BPM resolution & drift will determine energy resolution & drift! Need ±1mm range (and low drift & noise over whole range): Energy chirp over bunch train.
- E-XFEL IBFB: Overall feedback loop latency <1000ns expected to be sufficient. First few bunches in train are most likely dumped anyway (LLRF ...). Main random perturbation source that requires feedback: Mechanical vibrations → IBFB should move bunches to nominal orbit within ~10µs. E-XFEL will most likely run at >1000ns bunch spacing for quite some time ...

SwissFEL Injector Test Facility



SwissFEL Injector Test Facility



SwissFEL Pickup Parameters

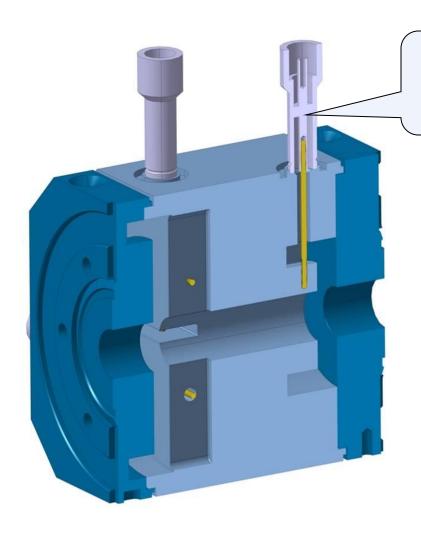
Parameter	ВРМ38	BPM16	BPM8
Material	Stainless Steel 316LN		
Distance From Position To Ref. Resonator [mm]	180	60	50

Parameter (Ref. Resonator)	ВРМ38	BPM16	ВРМ8
Gap [mm]	7		
Q_{L}	40		200
TM ₀₁₀ Frequency [GHz]	3.284		
Charge Signal [V/nC]	66.4	135	47.5
No. of Couplers	2	1	1

SwissFEL Pickup Parameters

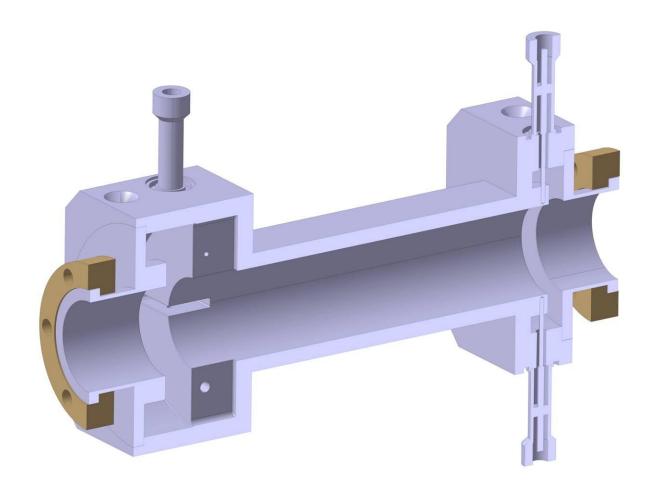
Parameter (Position Resonator)	ВРМ38	BPM16	BPM8	E-XFEL Und.
Gap [mm]	14	7	14	3
Q_L	40		200	70
TM ₁₁₀ Frequency [GHz]	3.284			3.3
TM ₀₁₀ Frequency [GHz]	2.389	2.252	2.202	
Position Signal [V/mm/nC]	5.74	7.07	5.23	2.9
Angle Signal [µm/mrad]	15.5	4.3	9.5	1

SwissFEL BPM16 Pickup

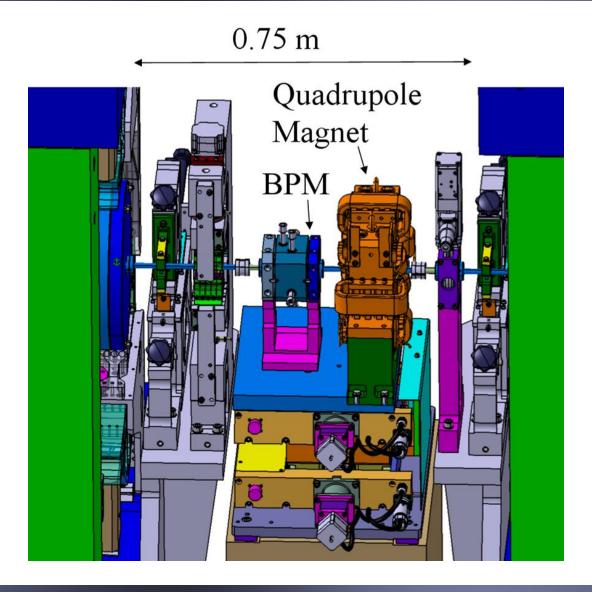


Swiss made feedthroughs (PSI design, F. Marcellini, M. Rohrer) ~100CHF/piece for series

SwissFEL BPM38 Pickup



SwissFEL BPM8 Pickup



BPM Status

E-XFEL & FLASH-2 Electronics

Pre-series in production for FLASH-2.

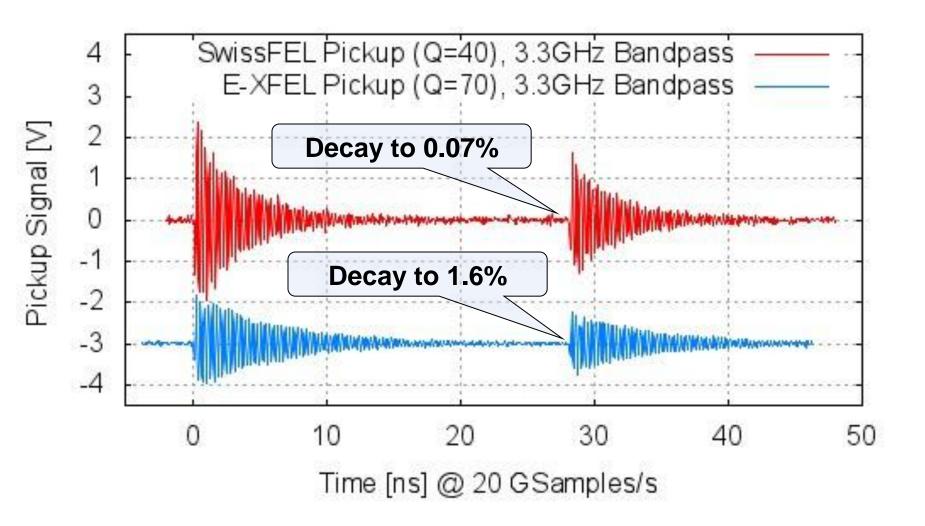
SwissFEL Pickups

- BPM16 (linac pickup): Design done, starting production (series of 15 first, >100 later).
- BPM38 ("injector" pickup): RF design done, mechanical construction in progress.
- BPM8 (undulator pickup): RF tolerance study in progress.

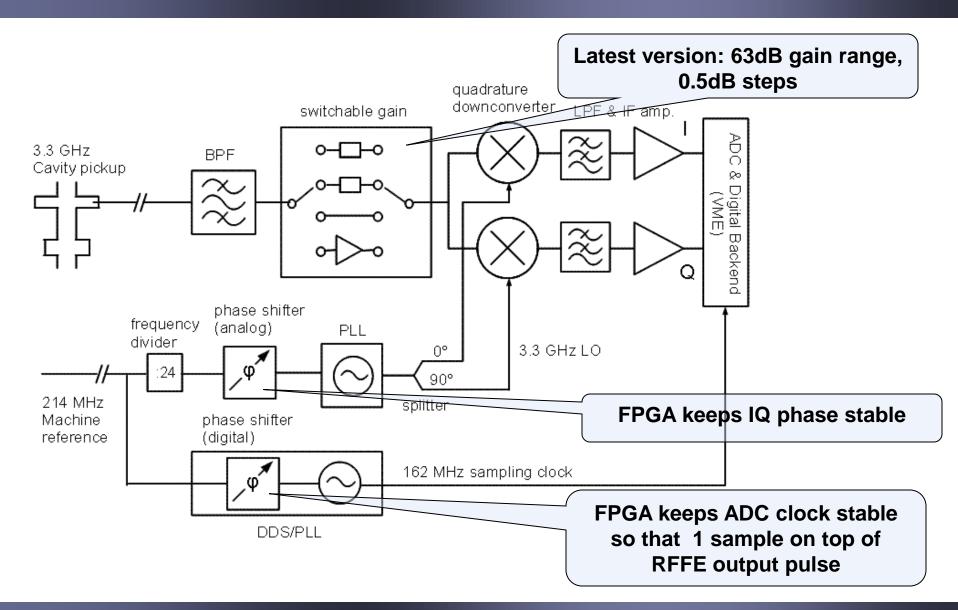
SwissFEL Electronics

• Use E-XFEL electronics, modify where needed (bunch spacing, ...). 1st linac prototype tested with beam.

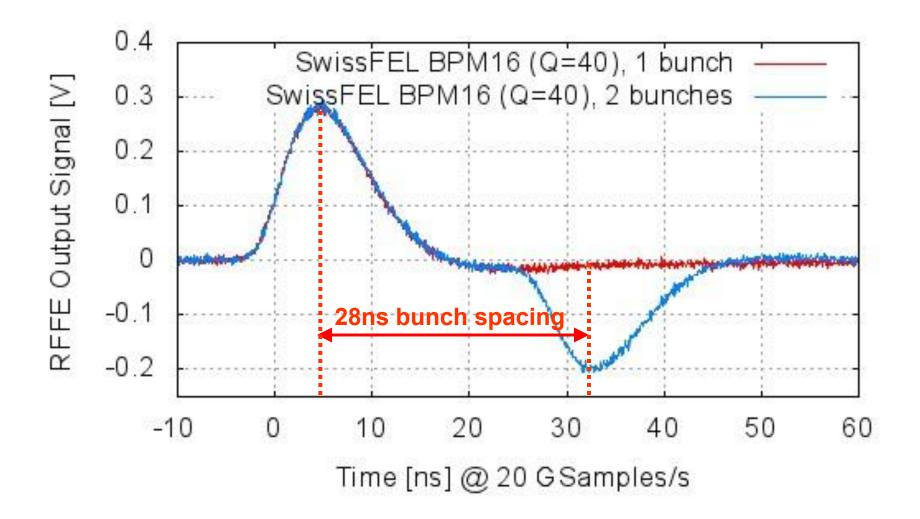
Pickup Signals



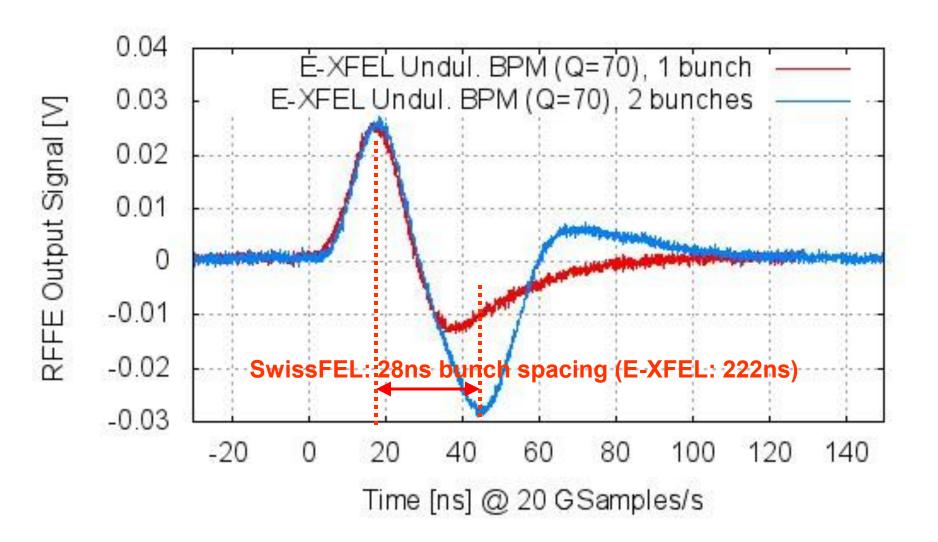
RFFE (Simplified)



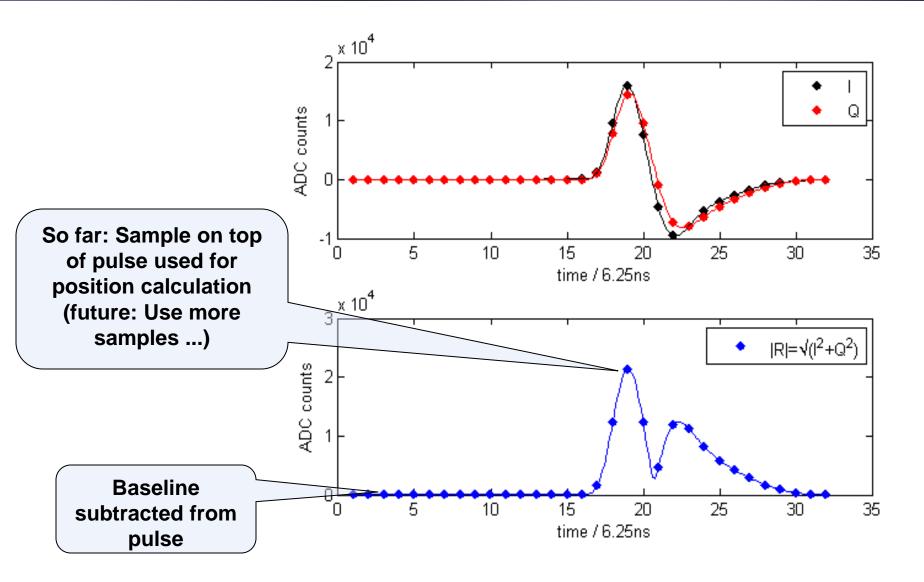
RFFE Output: SwissFEL BPM16



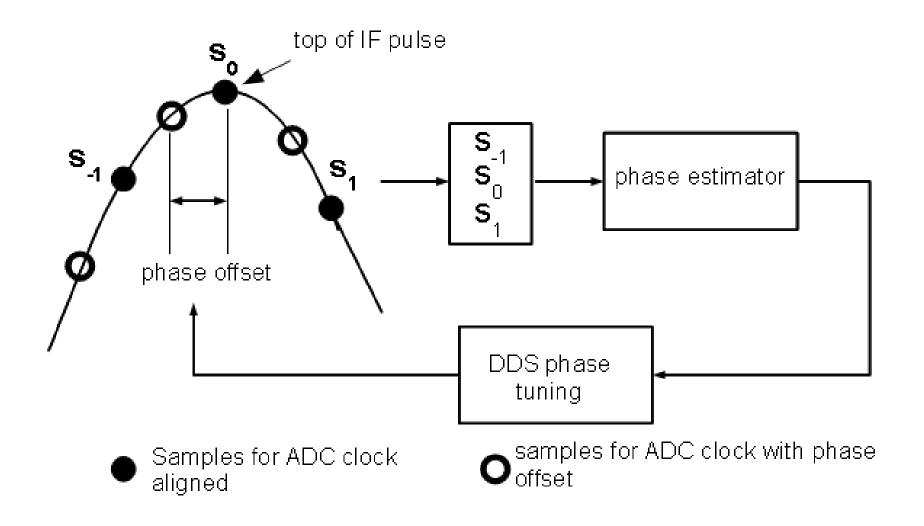
RFFE Output: E-XFEL Undulator



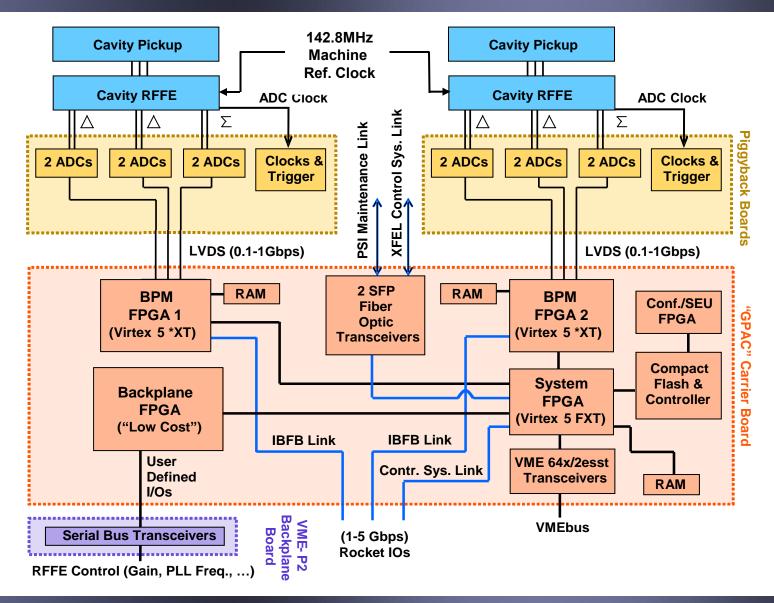
E-XFEL BPM ADC Waveform



ADC Clock Phase Feedback

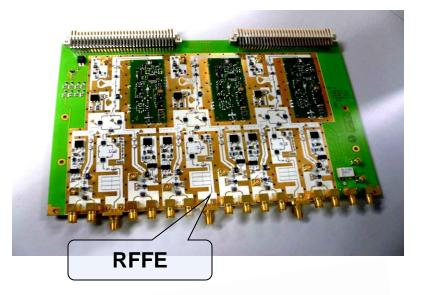


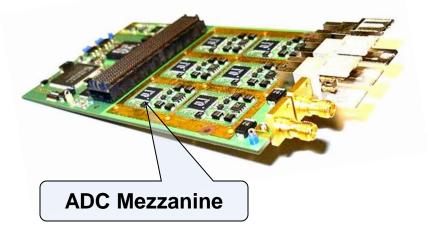
Hardware

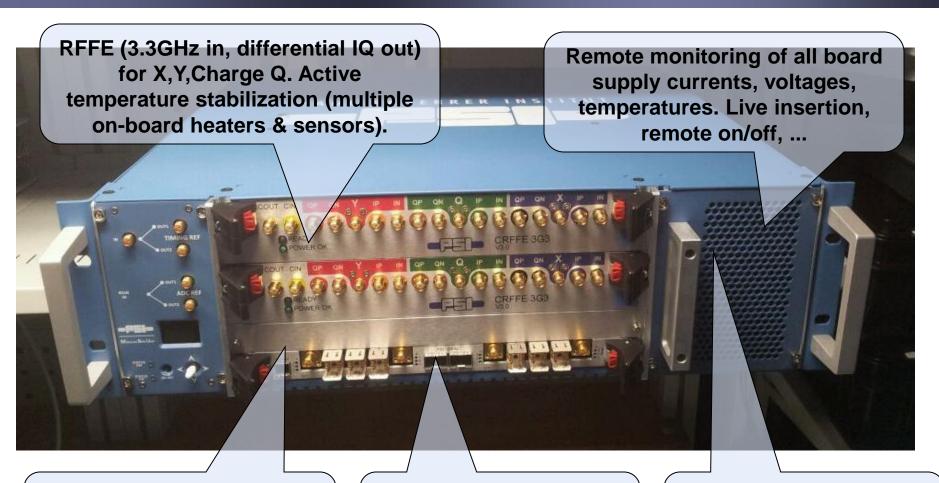


Hardware









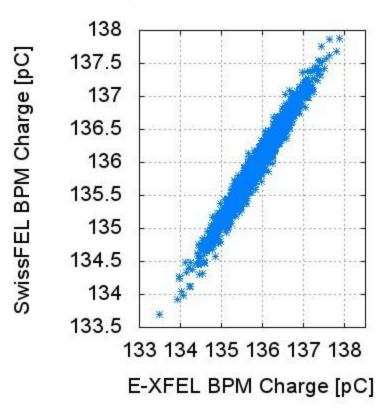
FPGA carrier with two 6x16bit ADC mezzanines, 160MSPS.

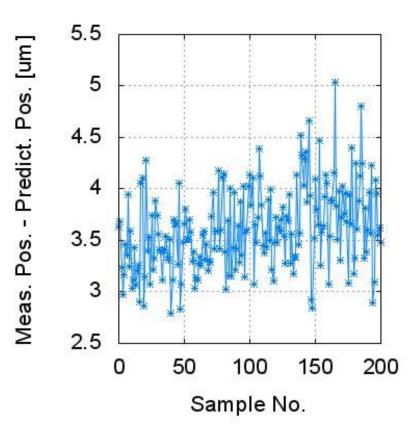
SFP+ (PCIe, Ethernet, custom protocol, ...). 6 more SFP+ at rear side.

8 fans with individual regulation, multiple temp. sensors, ...

SwissFEL BPM16 Test (Linac)

- Product of pos. noise & charge = 15pC·µm @ low charge
- Pos. noise <0.8µm RMS @ 135pC, ±1mm range.
- Charge noise <0.1% (<0.1pC RMS at Q=135pC).





E-XFEL Undulator BPM Beam Test

Beam Offset (mm)	Beam Charge (pC)	Linear Meas. Range	Measured Resolution (µm-rms)	Predicted Resolution (µm-rms)
0.1	285	±2mm	0.35	0.33
0.5	285	±2mm	0.40	0.36
1	285	±2mm	0.56	0.4
0.05	183	±500µm	0.18	0.165
0.2	2	±6.4mm	11.2	13
≈0.06	350	±250µm	0.12	(0.06)
unstable				

Position computation in FPGA (incl. IQ imbalance correction, ...). Overall latency <400ns.

PSI Team

- F. Marcellini & M. Rohrer (SwissFEL pickups) → IBIC'12
- M. Stadler (cavity RFFE HW) → IBIC'12
- M. Roggli, D. Treyer (ADC)
- R. Baldinger (FPGA carrier board HW)
- G. Marinkovic, W. Koprek (FPGA FW, SW, HW)
- R. Ditter, R. Kramert (electronics/mechanics)

Thanks also to:

- DESY BPM team (D. Lipka, S. Vilcins et al.)
- PSI vacuum group, mechanical construction & SITF operations team.





Thank you for your attention!