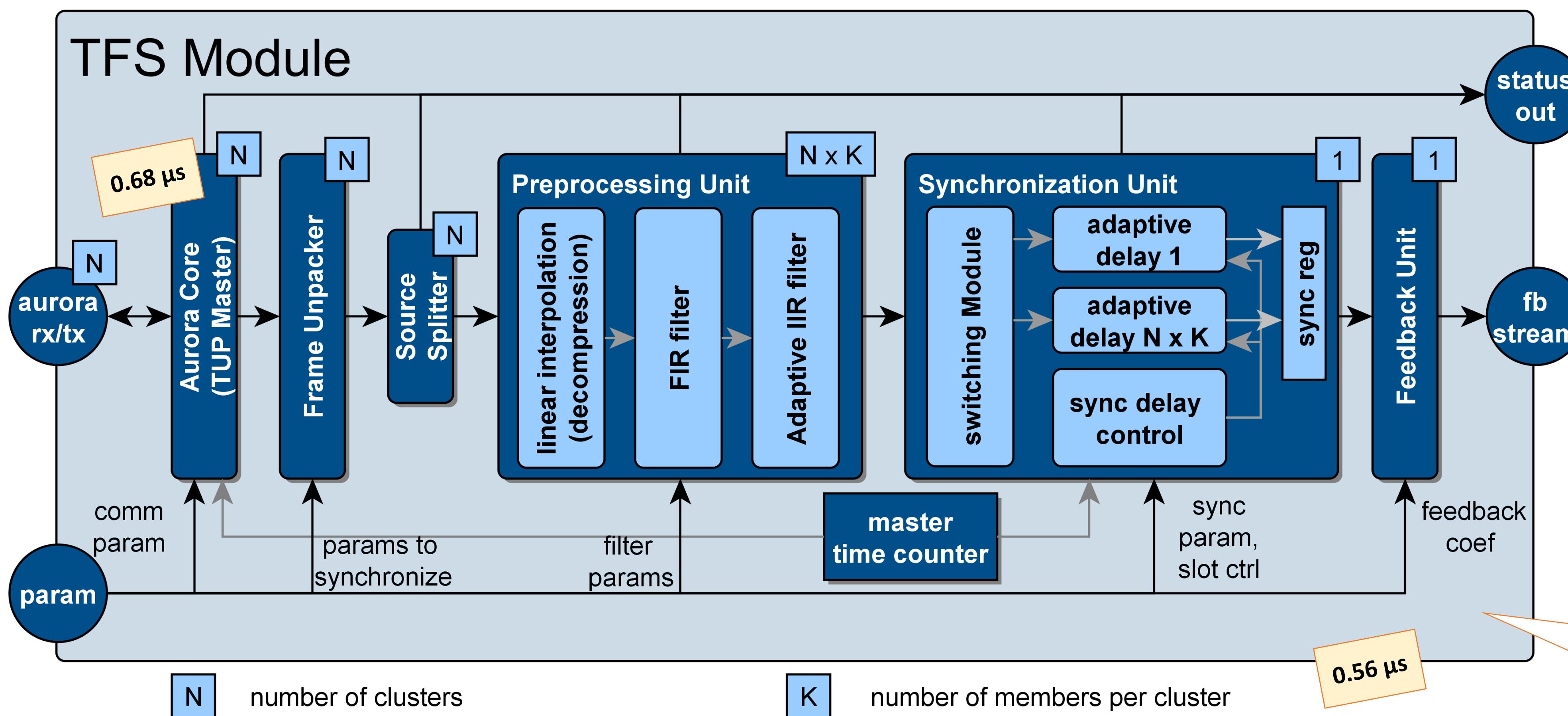


Realization of a Transverse Feedback System for SIS18/100 using FPGA

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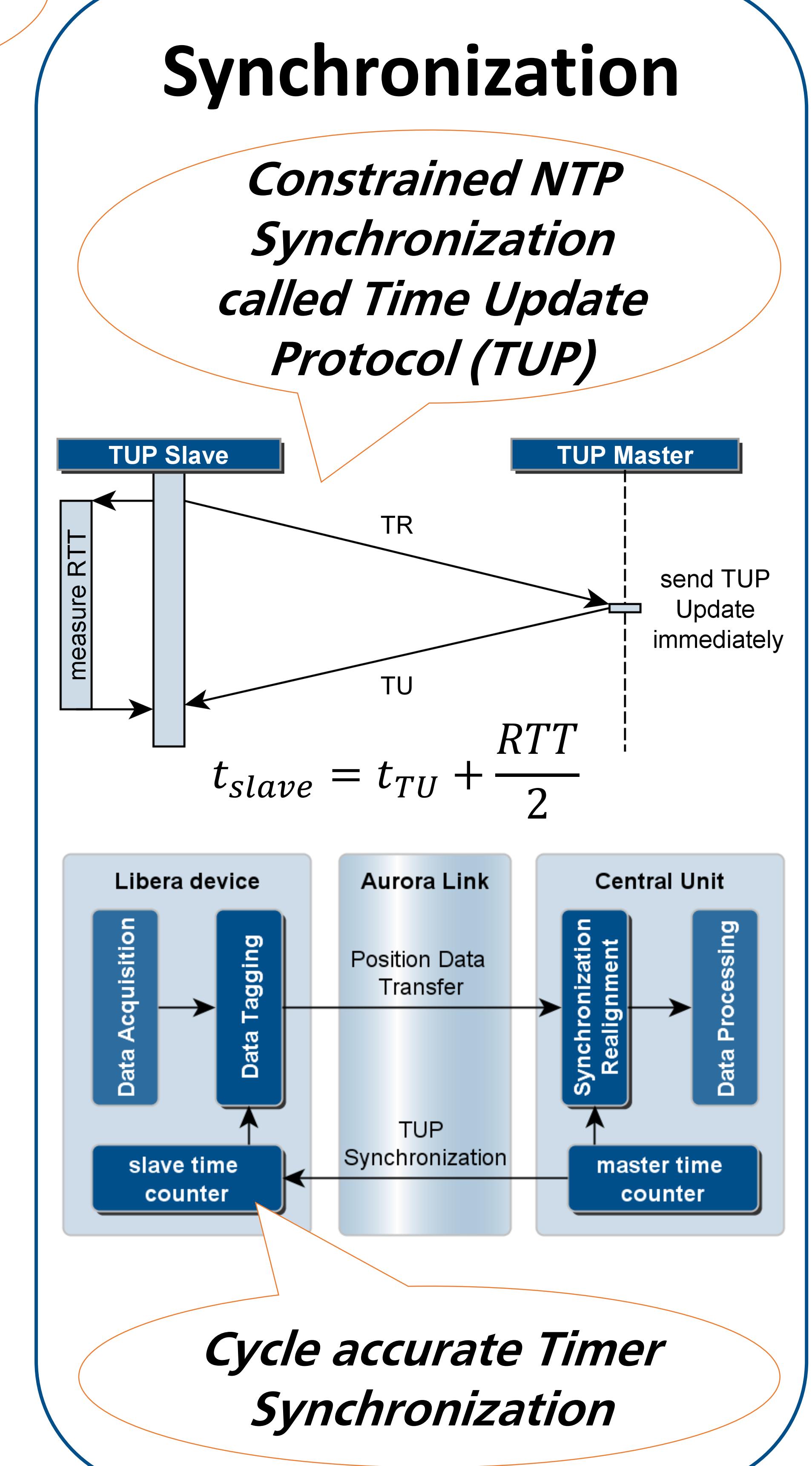
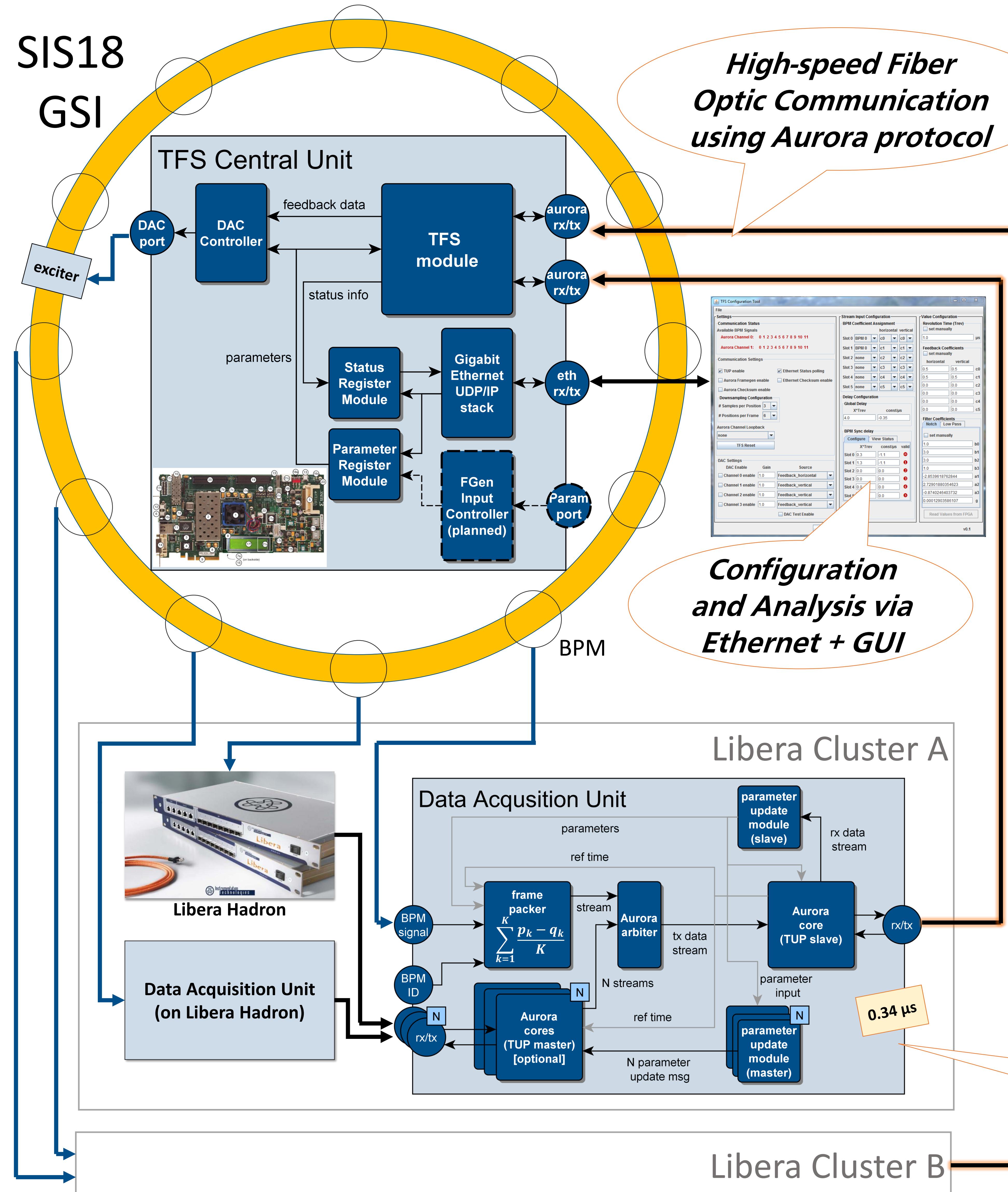


TECHNISCHE
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- ✓ Distributed
- ✓ Scalable
- ✓ Configurable
- ✓ Fast

*Low Latency design,
adaptive and well
controllable*



*Distributed System,
Plug&Play extendable*