



# Photocathode Development for the Cornell Injector

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### Introduction



### Cathode status at Cornell

We no longer use GaAs for injector commissioning and operations. Is still being studied as a source for sub-thermal emittance. It is a good cathode for low average current, but

Problems:

-Short lifetime at high current

-long tails for high QE (>5%) (could make thin layers)

-machine problems often kill the cathode, causing long delays for commissioning



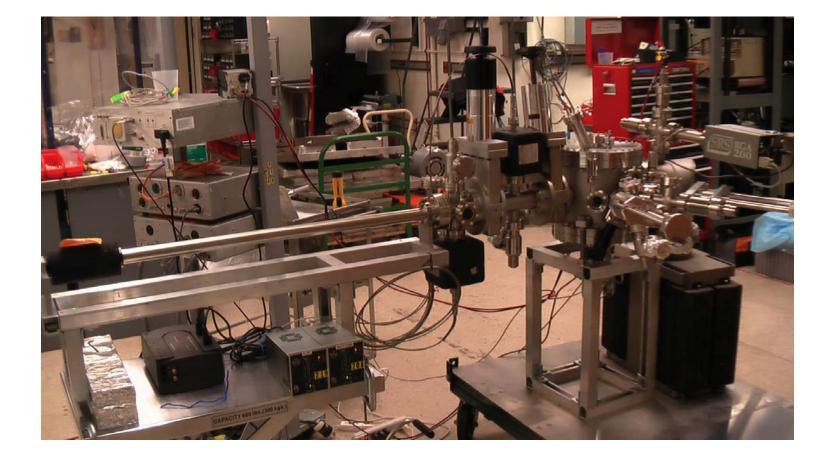


# Alkali Cathode Progress



#### Alkali UHV Growth Chamber

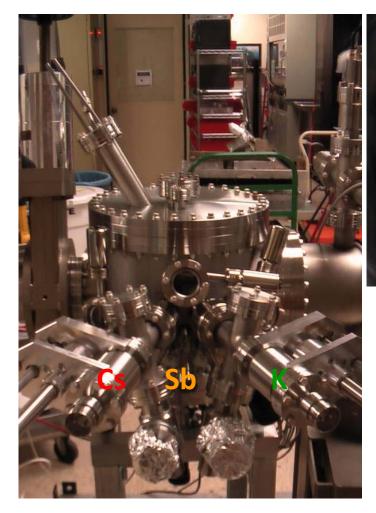




### Growth chamber, load lock and transfer mechanism

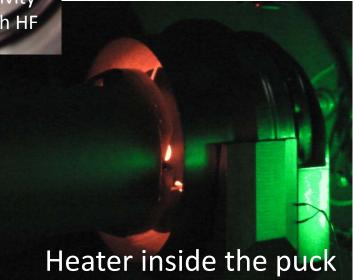
### UHV Growth Chamber





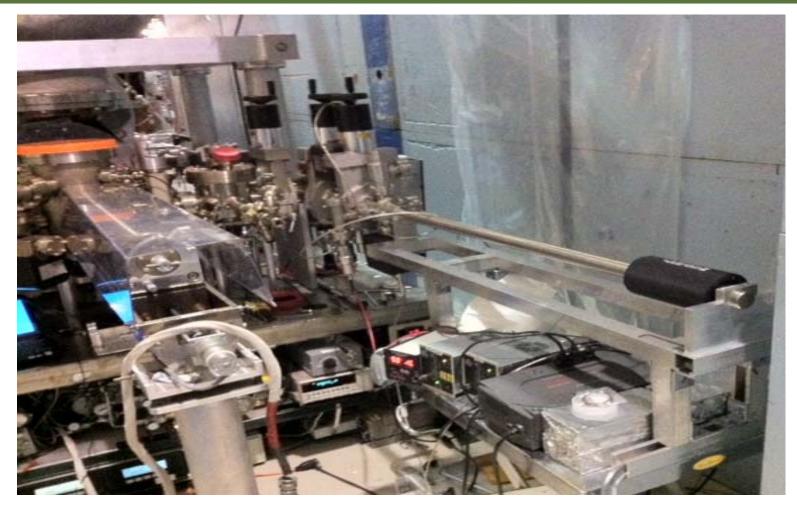


Si (100) high conductivity SiOx layer etched with HF First deposition has been carried out using SAES alkali metal dispenser. Now using using high capacity ALVATEC sources.



### Vacuum Suit Transfer Cart





A vacuum-suitcase transfer system is used to transport the cathode from the growth chamber to the electron gun. The left side shows the GaAs preparation system.

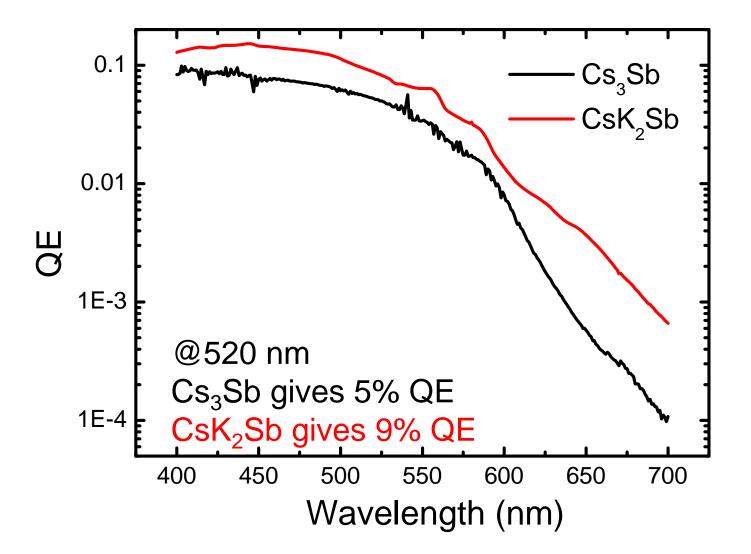


# **Preliminary Results**

- Few preliminary deposition test have been carried out during past year:
  - Cs<sub>3</sub>Sb
  - CsK<sub>2</sub>Sb
  - NaK<sub>2</sub>Sb
- So far the better results in terms of QE have been achieved with CsK<sub>2</sub>Sb depositions

## Spectral response

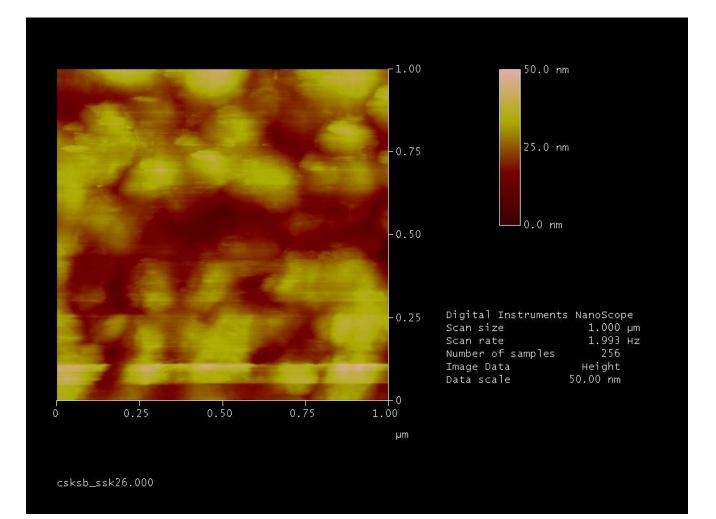






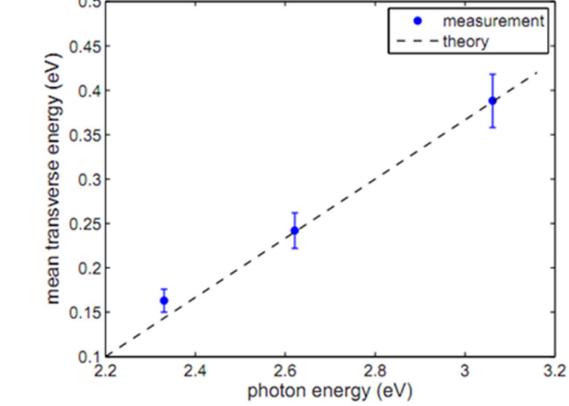
 $CsK_2Sb$ 

ERL



Roughness "peak-to-peak" less than 50 nm



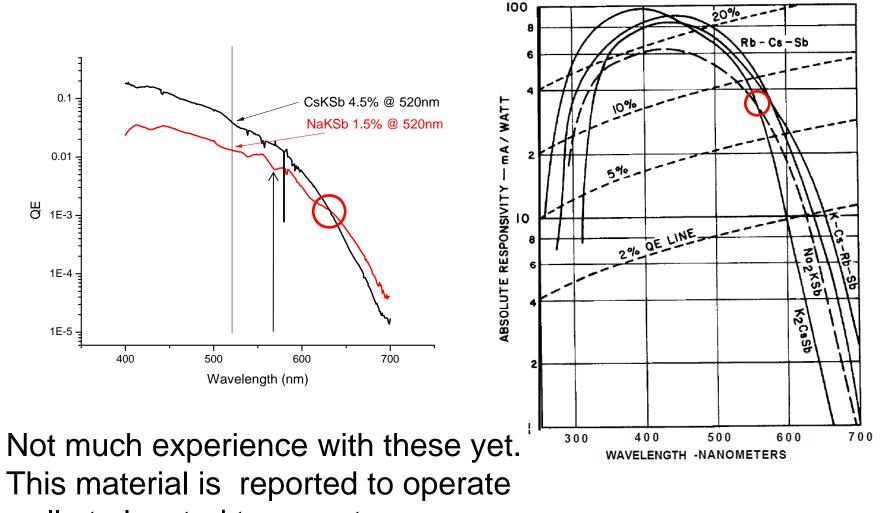


 $CsK_2Sb$  thermal emittance measurement (MTE). 25% higher than GaAs at 520nm, good enough!  $Cs_3Sb$  gives the same results









well at elevated temperatures.



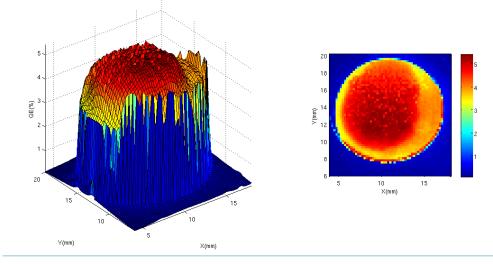
# CsK<sub>2</sub>Sb Recipe

- The growth of the  $CsK_2Sb$  photocathode onto Si(100) substrates follows this procedure:
  - The substrate is heated to 600°C to remove the hydrogen passivation from the Si surface (it comes from HF Si wafer rinsing)
  - Temperature is lowered to approximately 160 °C and then evaporation of 15 nm of antimony is performed;
  - Evaporation of the K is carried out while the substrate is slowly cooling down and the quantum yield is constantly measured until a peak on the photocurrent is reached;
  - When the substrate temperature falls below 100°C Cs evaporation starts until the photocurrent reaches a maximum;
  - The substrate is allowed to cool down to room temperature.

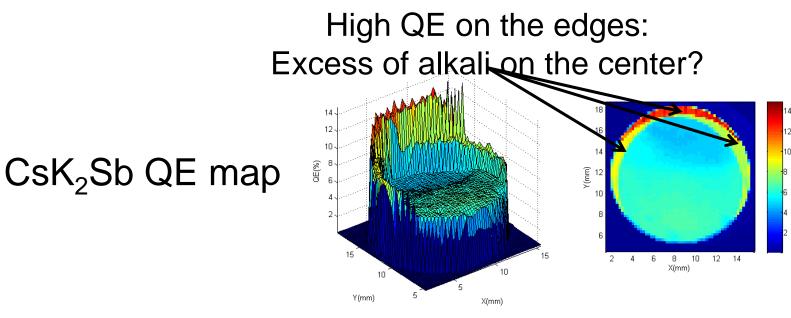


### QE uniformity





# Cs<sub>3</sub>Sb QE map @532nm





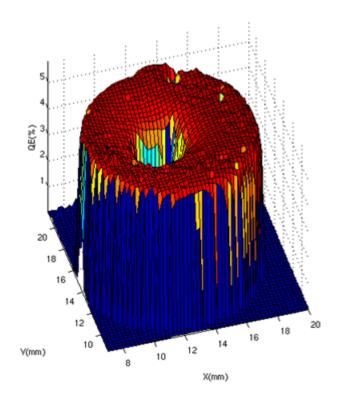


# Damage at high currents

GaAs Damage



## Non-recoverable QE damage on GaAs at high current– can't be recovered by heat treatment and reactivation

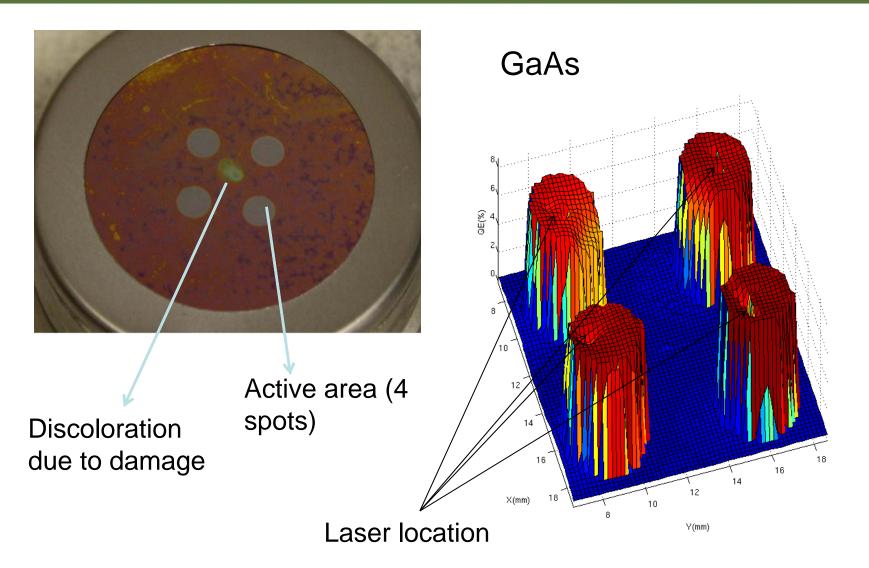


### Cause of Damage?

- Ion Backbombardment
- Ion implantations
- •Rise in vacuum pressure
- •Field emission/arcing

## **Off-Center Cathodes**



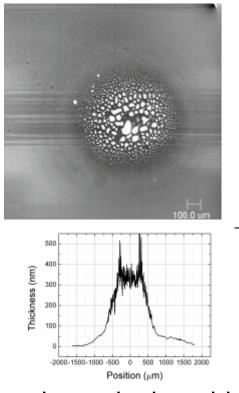


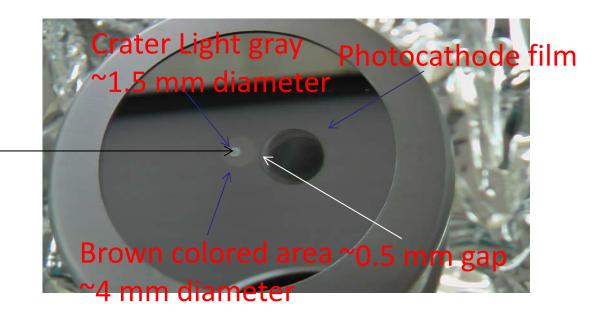


CsK<sub>2</sub>Sb Damage



### Analysis after 8 hour/ 20 mA run – CsK<sub>2</sub>Sb on Si





Large bump in the middle!



### **XRD** Analysis



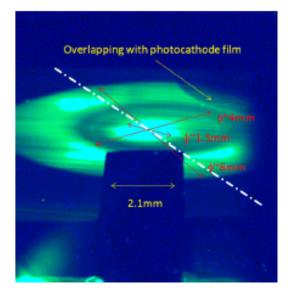


FIG. 10: Various diameters of the regions of interest from the XRD C dotted line is the diameter over which data was sampled for Fig. (11).

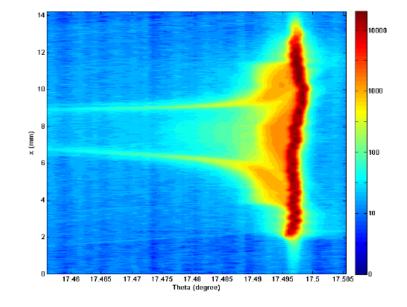


FIG. 11: Counts on the CCD detector as a function of the position along a diameter of damage region (x, vertical axis) and the diffraction angle.

X-ray diffraction measurements indicate crystallographic changes to the substrate at the center of the cathode





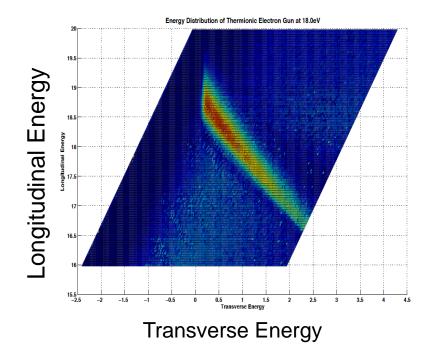
# Future R&D Efforts

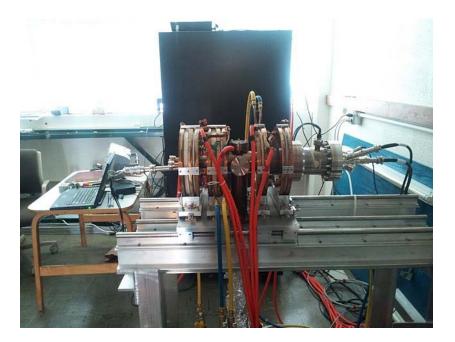




•Employs strong longitudinal magnetic fields and adiabatic invariance principle to get longitudinal and transverse electron distributions simultaneously

•Energy resolution up to 10meV possible so far (can go down to 2meV)





Similar to Orlov apparatus





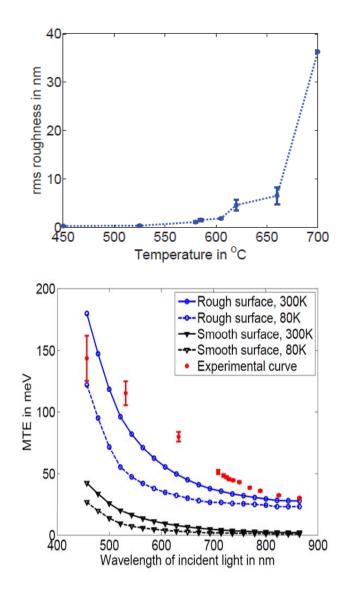
# MTE from GaAs – a mystery?

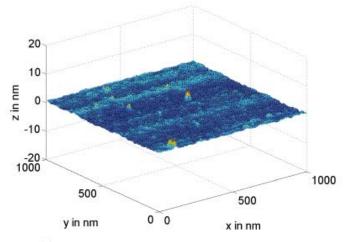
GaAs MTE

- Due to small effective mass of gamma valley electrons, theory predicts MTE as low as 2meV at 800nm
- Some groups have observed these small MTE values
- But most do not (including us). Why???
- Possible causes Surface roughness or dirty GaAs surface or different structure of Cs/F layer

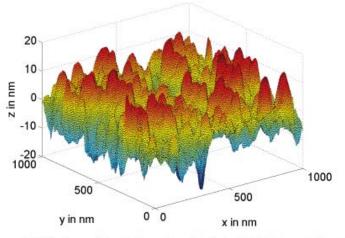
### Surface roughness of GaAs







(a) Surface of atomically polished GaAs crystal before heat cleaning (smooth surface)

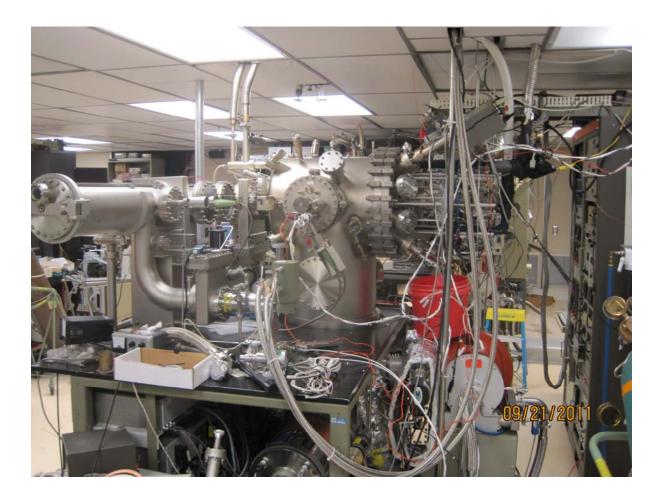


<sup>(</sup>b) Surface of heat cleaned and activated GaAs crystal used in the Cornell dc photoemission gun (rough surface)



#### Cathode Growth





We now have access to MBE machines for growing both GaAs-like and GaN-like cathodes. Just about ready for our first growth run



### Summary



- •We no longer use GaAs for high current running or for machine commissioning
- •Still studying GaAs properties as a source for subthermal electrons
- •Have growth facilities for CsK<sub>2</sub>Sb, Cs<sub>3</sub>Sb, NaK<sub>2</sub>Sb and other
- •Measured the MTE of  $CsK_2Sb$  and  $Cs_3Sb$  to be 160 meV @ 520nm
- Operated CsK<sub>2</sub>Sb at 20 mA CW, 5 MeV, for 8 hours
  Research plans for growing and characterizing other cathode materials





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