STORAGELESS RESONANT CONVERTER FOR ACCELERATOR MAGNETS

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Abstract

of the work, publisher, and DOI operation since 1993 and was revised in 2009. Recently, Elettra has been funded for a complete renewal of the of the state of the art converters, mostly of which will be designed into the house

For this purpose, it has been decided to evaluate the per-formance of a storage-less resonant converter, proposed by Dr. Slobodan Ćuk, which is a step down DC/DC converter consisting of four switches, one resonant capacitor and two For this purpose, it has been decided to evaluate the per-E resonant inductors. For this purpose, the voltage conversion ratio of the converter has been derived. The topology was confirmed with simulation and a PCB layout has been was confirmed with simulation and a PCB layout has been must designed, which is still to be tested.

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of this work Low current magnets have been designed for the new 6BA design of the storage ring, so that each one will be independently energized. With a larger number of power uo supplies, the main goal is to get high efficiency to keep the distributi facility environmentally friendly and rise the MTBF by lowering the thermal stresses on active devices. The fol-Flowing converter is fulfilling our two requirements.

STORAGELESS RESONANT **CONVERTER**

3.0 licence (© 2019). The examined storage-less resonant converter proposed by Dr. Slobodan Cuk [1], illustrated in Fig. 1.a, mainly relies on the energy transfer by resonance, instead of storing energy in inductors such as in buck converters.

В As it can be seen in Fig. 1, the converter is composed of four switches (Q_1 to Q_4), two resonance inductors (L_{r1} and 20 L_{r2}), one resonant capacitor (C_r) and one output filter caof the pacitor (Co).

The basic operation principle of this converter is to charge the resonant capacitor (C_r) by means of a resonance circuit formed with one of the inductors and to discharge the same capacitor through the other resonant inductor. This way, the transferred energy does not need to be stored in an inductive element, such as the case of a buck converter.

Although the structure is similar to switched capacitor \gtrsim converters [2], the addition of resonant inductances changes the operation of the converter and requires two of the switches, namely Q3 and Q4, to operate like diodes. TUPMP011

Basic Operation Principle

The operation principle of the converter will be explained over the simplest case, where resonance inductors have the same value, $L_{r1} = L_{r2}$, switching frequency is equal to the resonance frequency, fr, and the duty cycle, D, is equal to 50%. Here D is defined as

$$D = T_1 \cdot f_r \tag{1}$$

Where T_1 is the time interval where the switches Q_1 and Q₃ are conducting.

As it can be seen in Fig. 1.a and Fig. 1.b, there are two main operational states of the converter. In the first state, the resonant capacitor, Cr, is charged through the switches $Q_1 \& Q_3$ and L_{r1} . In this state, the current is supplied by the input voltage source and Cr is being charged.

In the second state, the switches $Q_1 \& Q_3$ are turned off, while the switches Q₂ and Q₄ are turned on. In this state, the power for the load is provided by the resonant capacitor, which is being discharged. As seen above, the switch pairs Q_{1,3} and Q_{2,4} are operated complementary to each other.



Figure 1: (a) Storage-less Resonant Converter and operation intervals of the converter; while (a) resonant capacitor is charging and (b) is discharging.

MC7: Accelerator Technology T11 Power Supplies For mentioned states, the following voltage equations can be written, which are basically second order ordinary differential equations:

$$V_{g} - V_{o} = V_{cr} + V_{Lr1} = \frac{1}{C_{r}} \int i_{Lr1} dt + L_{r1} \frac{di_{Lr1}}{dt}$$
(2)

$$V_{o} = V_{cr} - V_{Lr2} = \frac{1}{C_{r}} \int i_{Lr2} dt - L_{r2} \frac{di_{Lr1}}{dt}$$
(3)

The solutions for these equations are sinusoidal waveforms, which are as follows:

$$i_{Lr1}(t) = \frac{(V_g - V_o)}{\sqrt{\frac{L_{r1}}{C_r}}} . \sin(\omega_1 t)$$
(4)

$$i_{Lr2}(t) = \frac{V_o}{\sqrt{\frac{L_{r2}}{C_r}}} . \sin(\omega_2 t)$$
(5)

Where,

$$\omega_1 = \frac{1}{\sqrt{L_{r1}.C_r}} \tag{6}$$

$$\omega_2 = \frac{1}{\sqrt{L_{r2}.C_r}} \tag{7}$$

The current waveforms on the resonant inductors are illustrated in Fig. 2. As it can be seen, the current waveforms are sinusoidal and the magnitudes of the currents at switching instances are equal to zero, which enables soft switching Zero Current Switching (ZCS) for all four switches.



Figure 2: Resonant inductor current waveforms at D = 0.5.

The voltage conversion ratio at this case can be calculated considering the fact that the total change of the capacitor charge during one cycle at steady state should be zero. If the Equations (4) and (5) are integrated and equated, it will be seen that:

$$V_o = V_g - V_o \tag{8}$$

$$V_o = \frac{V_g}{2} \tag{9}$$

Where, V_g is input voltage and V_o is output voltage. This is the maximum achievable output voltage for this converter.

Constant Off-Time Variable On-Time Control

The operation of the converter can be extended to duty cycles that are different than 50%, which in return governs the output voltage.

However, it is obvious that if the switches are turned on/off with a period different than the resonant frequency, the ZCS feature will not be achieved anymore. Thus, the efficiency will decrease.

For maintaining the ZCS feature as much as possible, it is better to use a constant off-time and variable on-time control strategy, where the on time of the switches Q_2 and Q_4 are fixed to be equal to the half of the second resonance period T_2 and the on-time of switches Q_1 and Q_3 are varied.

$$T_2 = 2.\pi/\omega_2 \tag{10}$$

This way, the switches Q2 and Q4 always operate at ZCS turn-on and ZCS turn-off. Moreover, it is also obvious that for preventing any abrupt change of current on Lr1, the Q3 has to behave like a diode. Thus, it has been connected in a reverse way, so that the body diode will always ensure diode operation. The current waveforms for this operation are shown in Fig. 3.

Utilizing the constant-off time strategy, switches Q1 and Q3 still maintain the ZCS turn-on operation, whereas the switch Q1 has to turn off while it is conducting a high amount of current.



Figure 3: Resonant inductor current waveforms at D = 0.5.

The analysis of the converter under constant off-time control results in the following polynomial equation as the expression of output voltage:

$$V_{o}^{2}\left[4.K - \frac{4.A.C_{r}}{\left(1 - \frac{A}{2}\right)} - \frac{T_{sw}}{R}\right] + V_{o}\left[\frac{2.A.C_{r}.V_{g}}{\left(1 - \frac{A}{2}\right)} - 4.V_{g}.K\right] + V_{g}^{2}.K$$
(11)

Where $T_{sw}=T_1+T_2/2$ is the switching period,

$$K = \left(\frac{\sin(\omega_1 T_1)}{(1 - A/2)}\right)^2 \cdot \frac{C_r \cdot L_{r_2}}{2 \cdot L_{r_1}}$$
(12)

$$A = \left(1 - \cos(\omega_1 T_1)\right) \tag{13}$$

MC7: Accelerator Technology T11 Power Supplies The solution of the quadratic polynomial function for the case of $L_{r1}=L_{r2}$ (meaning $f_1=f_2=f_r$), at various loads are illustrated in Fig. 4. As given previously, here the duty cycle is $D=T_1$. f_r , and normalized output voltage is $V_{on} = V_o/V_g$.



Figure 4: Resonant inductor current waveforms at D = 0.5.

SIMULATIONS

According to the parameters in Table 1, a simulation model, shown in Fig. 5, has been built via PSIM 11, for checking the behaviour of the converter.

To ensure low current ripple at the input and output, it has been decided to employ an LC filter at the input and Pi filter at the output.



Figure 5: Simulation model in PSIM 11.

Table 1: Designed Circuit Parameters

Parameter	Value
Input Voltage, Vg	48 Volts
Max. Output Voltage, Vout	24 Volts
Max. Output Current, Iout	60 Amperes
Resonance Frequency, f_{res}	159 kHz
Resonant Capacitor, Cr	5 µF
Resonant Inductor 1, L _{r1}	200 nH
Resonant Inductor 2, L _{r1}	200 nH
GaNFETs	EPC2020
Gate Driver	Si8274
Synch. Rect. Controller	UCC24612-1

PCB DESIGN

The chosen GaNFET is particularly small and adds some manufacturing issues for the width of the pads (270 μ m) which are replicated on all 8 layers of 35 μ m thickness PCB, shown in Fig. 6. Vias with the hole of 100 μ m are needed on the pads to split the current (60 Amps) flowing

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on the drains of the transistors. The overall size of this converter is 165 x 75 mm (including the 30 mm gap for current probe) for a 1400 W device. Two of the GaNFETs are controlled with synchronous rectifier controllers so that they work like diodes. Thermal losses are predicted to be maximum 30 W.

The PCB has been employed with multi-layer ceramic capacitors only for size shrinking. The used inductors are quite small, since they are only 200 nH.



Figure 6: Designed PCB layout.

CONCLUSION

At the present time the PCB has been designed and manufactured and needs mounting the GaNFETs. Results from the field will be made available.

The clear advantage of the examined topology is the usage of very small inductances without the need for increasing the switching frequency to MHz levels. This is due to the fact that the energy transfer relies on resonance. Moreover, three of the GaNFETs are operated in soft switching mode, whereas only one is operating at hard turn-on.

REFERENCES

- S. Ćuk, "Four-Switch Step-down Storageless Converter", United States Patent US 8, 134, 351 B2, Mar. 13, 2012.
- [2] A. Cervera *et al.*, "A High-Efficiency Resonant Switched Capacitor Converter With Continuous Conversion Ratio", *IEEE Transactions on Power Electronics*, vol. 30, no. 3, pp. 1373-1382, Mar. 2015.