

Figure 3: Front panel of RF monitor unit.

Event Receiver (EVR)

The beam mode and timing signals are controlled by an event-timing system, which consists of an event generator (EVG) and event receiver (EVR) in the linac. It is necessary to receive an event code delivered by the event-timing system to recognize a beam mode. An EVR, which is an 8b/10b high-speed serial interface designed by the Virtex6 GTX transceiver, has been installed on an FPGA to receive an event cord.

The EVR frequently malfunctioned during the test run of the RF monitor. This is because the phase noise of a reference clock for the GTX transceiver exceeded the restriction value. Originally, the 114.24-MHz reference clock was supplied from an event-timing system. To improve the phase noise, the clock source and the distribution system have been changed. Figure 4 shows the improved phase noise and a restriction value of the GTX.

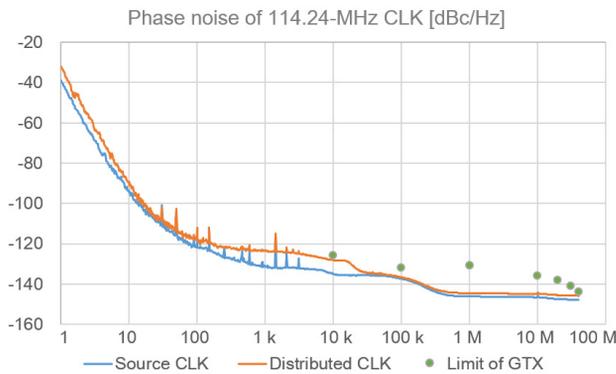


Figure 4: Phase noise of improved 114.24-MHz clock.

Data Transfer by SiTCP

SiTCP is the FPGA logic to connect a measuring instrument for physics experiments to the Ethernet [5]. Ethernet communication by hardware is possible without mounting the CPU on the FPGA. Ethernet is used as a data bus between the RF monitor unit and the EPICS-IOC in this system. The data acquired by the ADC is transferred to the SiTCP through FIFO. The capacity of the FIFO is 16-bit × 2048 word per ADC. The network transfer rate is sufficient

for 50 Hz data acquisition. The event code, which is received by the EVR, is previously attached to the transmit data. Therefore, even if network latency occurs, the correspondences of the acquired data and beam mode are secured.

APPLICATION SOFTWARE

Originally, the data acquired by the RF monitor unit are the I/Q components of a monitor signal. EPICS-IOC provides the data converted to amplitude and phase. Moreover, the amplitude and phase are sorted for every beam mode. An amplitude waveform and a phase waveform are monitored constantly; when an abnormality occurs, the data is archived.

Waveform Viewer

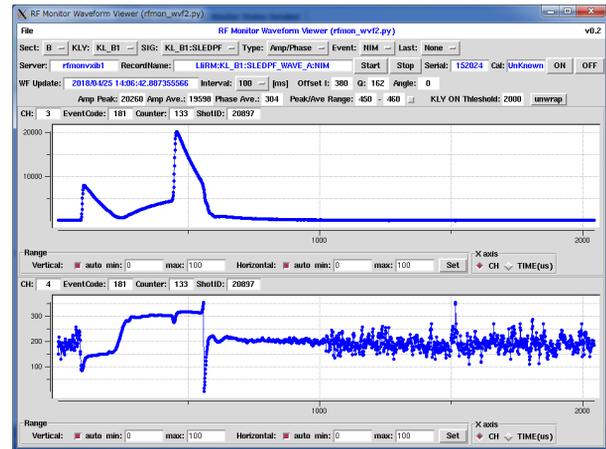


Figure 5: Waveform viewer.

The waveform viewer displays the amplitude and phase of the monitor system acquired in real time. Figure 5 is a sample that shows an SLED output signal. The upper waveform is the amplitude and the lower waveform is the phase. The user can choose the monitor signal and the beam mode by a pull-down menu.

CSS Archiver

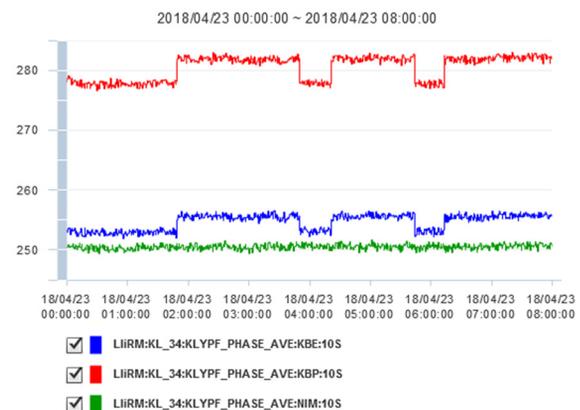


Figure 6: CSS archiver.

The EPICS-IOC samples' typical values of amplitude and phase are from the waveform data. The sampled data are stored in an EPICS archiver. A CSS-Archiver is a general-purpose software to display archived data. Figure 6 shows the archived phase data of the klystron output. The chosen beam modes are KEKB e- (KBE) and KEKB e+ (KBE) and no-injection (NIM).

Last Waveform Archiver

This is the program operating on the server computer that accumulates the waveform data to the ring memory. When a high-powered RF source is stopped by a safety interlock, the waveform data on the ring memory is archived. Fig. 7 is the data immediately before a high-powered RF source suspends because of VSWR. An increase in the backward power of the SLED (SLEDPB) occurred at the last pulse.

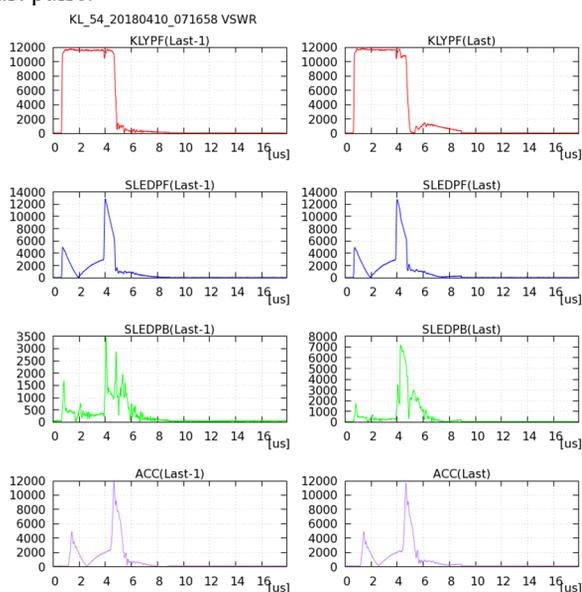


Figure 7: Archived last waveform.

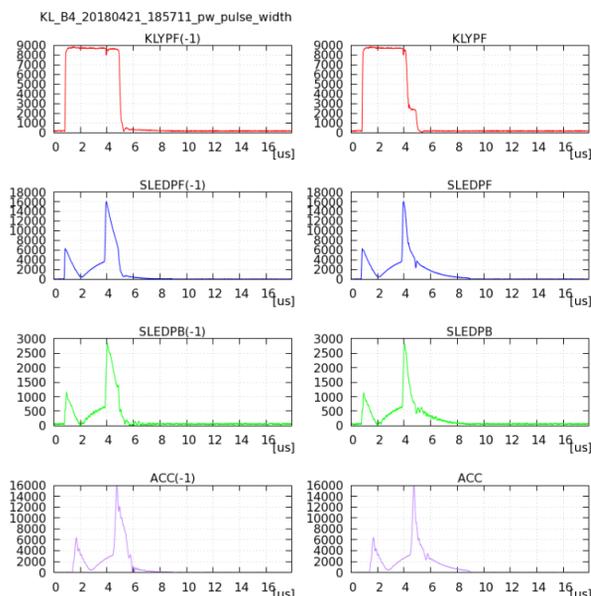


Figure 8: Archived pulse-shortening waveform.

Pulse-Shortening Detector

When pulse shortening occurs, the klystron output pulse width becomes shorter. The high-powered RF source is not always suspended. The pulse-shortening detector is a program that detects a pulse width that is shorter than usual and archives the waveform data. Figure 8 shows the waveform data acquired by this program. The peak of the reflection power (SLEDPB) did not change and only the pulse width of the klystron output (KLYPF) became shorter.

SUMMARY

The installation of the primary components of the RF monitor system has been completed. We are considering the measurement of the beam-induced wave [6] for beam commissioning. To detect the beam-induced wave by the RF monitor unit, additional component have been installed experimentally.

To improve the phase noise of the 114.24-MHz clock, the replacement of clock distribution modules is scheduled. The RF monitor unit has a function that corrects the detection error of the I/Q demodulator. The calibration depends on the temperature and LO power level of the demodulator. Therefore, the calibration of the demodulator in an actual environment is planned. An application software will be upgraded according to the user request.

REFERENCES

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