ADVANCES IN NORMAL CONDUCTING ACCELERATOR TECHNOLOGY
FROM THE X-BAND LINEAR COLLIDER PROGRAM*

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Abstract

In the mid-1990’s, groups at SLAC and KEK began dedicated development of X-band (11.4 GHz) rf technology for a next generation, TeV-scale linear collider. The choice of a relatively high frequency, four times that of the SLAC 50 GeV Linac, was motivated by the cost benefits of having lower rf energy per pulse (hence fewer rf sources) and reasonable efficiencies at high gradients (hence shorter linacs). To realize such savings, however, requires operation at gradients and peak powers much higher than that hitherto achieved. During the past twelve years, these challenges were met through innovations on several fronts. This paper reviews these achievements, which include developments in the generation and transport of high power rf, and new insights into high gradient limitations.

INTRODUCTION

The goal for a next generation electron-positron linear collider is to produce up to 1 TeV center-of-mass collisions. During the past twelve years, SLAC and KEK worked to develop a collider design based on X-band (11.4 GHz) rf technology for the main linacs. In 1999, they were joined by FNAL, who contributed significantly to the accelerator structure development.

The linac rf system that was ultimately proposed uses 11.4 GHz components similar in function to those in the 2.9 GHz SLAC Linac [1]. The baseline rf unit is illustrated in Figure 1. In each linac there would be about 1100 such units for 500 GeV operation and twice that number for 1 TeV operation. Each rf unit contains a modulator that drives two klystrons, each of which produces 75 MW, 1.6 µs pulses at 120 Hz or 150 Hz. The klystrons power an rf compression system that produces higher peak power (by about three times) in shorter (four times) pulses that match the accelerator structure requirements. The resulting 400 ns, 475 MW pulses feed eight, 0.6 m long accelerator structures, producing a 65 MV/m unloaded gradient in each structure. For efficiency, multiple bunches (192) are accelerated on each rf pulse. At the design bunch intensity (0.75×10^10 particles) and bunch spacing (1.4 ns), the beams (0.86 A) load the accelerator structures by 20%, yielding a 52 MV/m loaded gradient.

The choice of rf frequency strongly influences the power source requirements. The rf attenuation time in the structures (~100 ns) sets the scale for the structure fill time (~ attenuation time) and the bunch train length (~3 fill times) for good rf-to-beam efficiency and low cost. With the parameters chosen, the overall AC-to-beam energy transfer efficiency is about 8%. When including the power for cooling and other purposes, 208 MW of AC wall plug power would be required for both main linacs to generate 6.8 MW average power beams for 500 GeV operation.

In the following sections, the performance that has been achieved in prototypes of the major rf sub-systems is reviewed. With the recommendation by the ITRP last August that a next generation linear collider use superconducting rf technology, these normal conducting rf activities have been heavily curtailed (some close-out activities continue). The authors hope that the advances from the linear collider program will be useful in other applications.

MODULATORS

The basic requirement for the modulators is to power the 75 MW klystrons, which each require 500 kV, 270 A, 1.6 µs pulses. Although this voltage is fairly high, it was not a major design constraint. The bigger challenges have been to achieve fast rise/fall times (~200 ns) for efficiency, and to power several klystrons to reduce costs [2].

Conventional line-type modulators like those used in the SLAC Linac were considered initially for this application. These modulators have several drawbacks including low efficiency and their reliance on thyratrons, which have relatively short lifetimes (10-20 khr) and require periodic tuning. As an alternative, a solid-state induction-type modulator was developed by a SLAC, LLNL and Bechtel Nevada collaboration. The design takes advantage of recent advances in high-power, solid-
state switches (Insulated Gate Bipolar Transistors or IGBTs), which are used primarily by the motor drive industry. The basic concept is to sum many low-voltage sources (few kV) inductively to yield the desired klystron voltage. For this purpose, each source drives a toroidal transformer (11 cm ID, 30 cm OD) made with Metglas®, an amorphous ferromagnetic metallic alloy. These cores are stacked and secondary windings are threaded through their IDs to sum the voltages. Each source is essentially a capacitor that is slowly charged and then partially discharged (~2%) through an IGBT switch to generate the pulse. Because of this limited discharge, much higher density capacitors are possible than in conventional modulators. To improve reliability, additional sources are included that automatically replace ones that fail. Overall, this design has the potential of being less expensive, easier to maintain and more efficient and reliable. Also, the pulse can be shaped by delaying turn-on of some of the sources.

For cost reasons, it is preferable to drive as many klystrons per modulator as possible. In an earlier rf system configuration, each rf unit contained eight klystrons that generated 75 MW, 3.2 µs pulses. Given this layout, the first full-scale induction modulator was designed to drive eight such klystrons with about 1 GW of peak power. A three-turn secondary was used with 76 transformers to match the IGBT ratings for pulsed operation (with two IGBTs per transformer, each ran at 2.2 kV and 3.3 kA).

The development and construction of this ‘Eight Pack’ modulator took several years. Initial tests with a water load achieved 1 kA pulses at 500 kV, and included ‘spark down’ evaluations where the output was shorted to simulate klystron gun arcs. Preventing IGBT failures from over-current in these cases required the adoption of a more robust IGBT design and the addition of feedback to the IGBT drive circuit to limit the current based on the rate of current change. These changes produced slower switching times and the increased IGBT heating that resulted limited the pulse rate. Tests also included driving one or two S-band klystrons at 400 kV, 450 A each with 3 µs pulses. Gun arcs were forced by increasing the voltage level. The modulator survived these events, as did the klystrons, even though much more energy was dissipated (up to 200 J) than had been thought possible without damaging the guns.

The original plan was to use this modulator in an rf system test to power eight 75 MW klystrons at the Next Linear Collider Test Accelerator (NLCTA). However, the klystron production was delayed and eventually the rf unit configuration was changed to that described above. So instead, four 50 MW solenoid-focused tubes were installed, each of which required 300 A at 400 kV. Only one driver board was used per transformer in this case, which made operation easier as the power sharing between the two boards had not always been balanced. However, this did not help the IGBT heating problem, which was in fact made worse by the two-times higher stray capacitance of the 50 MW tubes relative to that anticipated with the 75 MW tubes. To allow 60 Hz operation, a chilled water system was used to cool the modulator. The increased stray capacitance also produced a 20% overshoot in the pulse, which was flattened by delaying the turn-on of some of the sources. With this procedure, a flattop of +/- 1% was achieved during the 1.6 µs long, 400 kV pulses.

The modulator ran in this configuration for over 1500 hours at 60 Hz, producing 400 kV, 1200 A, 1.6 µs pulses. The efficiency from ‘wall plug’ to usable HV pulse was 58%, while the original goal was > 75% (the higher stray klystron capacitance is the cause of some of this difference). One problem encountered during this period was failure of the bypass diodes that carry the current when the switch is off or delayed. These failures were due in part to the high turn-on voltage of the diodes. The problem was fixed by using an array of smaller, lower turn-on-voltage versions that were plugged into the unused board slots. A more persistent problem was failure of the multi-laminate connections between the boards and the transformer housing. The bad contacts led to overheating, arcing and intermittent board behavior, and required nearly daily maintenance to replace, clean or re-seat one or more of them.

Even before the Eight Pack modulator was first operated, a program was underway to improve its performance and lower its cost. The design that resulted has robust, spring-contact connectors, better current balance between boards, improved oil cooling, and faster, higher-voltage IGBTs (operated at 4 kV, so there are fewer, but thicker transformers). Also, a ‘two-pack’ layout was later adopted with the change to a two-klystron rf unit for the linear collider. To quickly demonstrate such a system, a hybrid modulator was configured in which 15 of the new transformers with dual-drive boards were stacked, and a single turn secondary wound through them to drive a conventional 10:1 turns-ratio transformer. This ‘Two Pack Hybrid’ was tested using a water load and produced 500 kV, 500 A, 1.6 µs pulses at 120 Hz with 60% efficiency (worsened by the conventional transformer). However, the cooling was still somewhat marginal and the IGBTs were not as robust as desired. Currently, this unit is being upgraded and will be installed in place of the Eight Pack transformer stacks to power two 50 MW klystrons. A fully integrated Two Pack modulator was designed but will not be built. However, research continues to develop smaller IGBT modules with integrated gate drivers.

**KLYSTRONS**

To reduce the power source cost, it is desirable to use high peak power klystrons. Such klystrons are challenging to design because they require a high-voltage, large-area gun, a strong beam focusing system with both low power consumption and low beam loss, a stable, high-gradient output section and robust, high power vacuum
windows. In general, pulse lengths longer than the 400 ns needed for the accelerator structures are possible, so rf pulse compression is employed to utilize the available energy. Such schemes are cost effective for pulse lengths from about 1.2 μs to 3.2 μs, but with the longer pulses, achieving adequate klystron cooling is a challenge.

The first tubes developed used conventional solenoid focusing, although the associated power dissipation (20 kW) was too high to be practical for the large number of tubes required for the linear collider. At SLAC, the 50 MW XL4 klystrons were the culmination of this program, which started with a goal of producing 100 MW tubes. The XL4 klystrons have a micro-pervenance of 1.2 and contain four-cell, traveling-wave output cavities. Twelve have been built and they have been used extensively for component testing and beam acceleration where they produce up to 50 MW, 1.6 μs pulses at 60 Hz with a 41% efficiency. At KEK, a similar tube was developed with BINP and it produced 50 MW, 1.5 μs pulses with 35% efficiency.

To eliminate the power consuming solenoid electromagnets, a program at SLAC was started in 1995 to develop periodic permanent magnet (PPM) focusing. The goal for these PPM klystrons has been to produce 75 MW in up to 3.2 μs pulses with better than 55% efficiency. In the designs that resulted, about 40 magnet rings with alternating polarities are interleaved with iron pole pieces to generate a sine-like axial field along the ~0.5 m long, ~10 mm diameter beam tube between the gun anode and beam collector. The resulting focusing strength is proportional to the rms of this axial field, and about 2 kG can be achieved practically compared to the ~5 kG field in the solenoid-focused tubes. This has led to a lower micro-pervenance (0.8) design in which the space charge defocusing is smaller. This choice requires a fairly high voltage gun (500 kV), but increases the klystron efficiency due to the improved bunching. For the magnets, NbFeB has been mainly used because of its low cost. To maintain field quality, the magnet temperature must be kept below about 50°C.

To date, groups at SLAC and at KEK (in collaboration with Toshiba) have each designed and built seven PPM klystrons [3,4]. The learning curve has been steep as the structures includes a pulse compressor that shortens the pulse while increasing the peak power. In early rf system designs, a SLED II pulse compression scheme was proposed, which is a delay line version of the SLAC Linac Energy Doubler (SLED). It includes a hybrid that splits the klystron power and routes each half to a delay line made of circular waveguide. These lines are shorted at the far end and have irises at the near end that partially reflect the rf. During operation, the lines are resonantly filled and then effectively discharged by a 180° change of the klystron rf phase. The lower-power SLED II systems have performed well at NLCTA, with over 30,000 hours of 60 Hz operation up to 250 MW. However, they are not particularly efficient (65%) due to the rf that is transmitted from the delay lines during filling.

RF DISTRIBUTION

The rf distribution system connecting the klystrons to the structures includes a pulse compressor that shortens the pulse while increasing the peak power. In early rf system designs, a SLED II pulse compression scheme was proposed, which is a delay line version of the SLAC Linac Energy Doubler (SLED). It includes a hybrid that splits the klystron power and routes each half to a delay line made of circular waveguide. These lines are shorted at the far end and have irises at the near end that partially reflect the rf. During operation, the lines are resonantly filled and then effectively discharged by a 180° change of the klystron rf phase. Three lower-power SLED II systems have performed well at NLCTA, with over 30,000 hours of 60 Hz operation up to 250 MW. However, they are not particularly efficient (65%) due to the rf that is transmitted from the delay lines during filling.
While more efficient compression methods were considered for the linear collider, SLED II was eventually chosen because of its relative simplicity, which also made it easier to demonstrate (e.g., the Delay Line Distribution System alternative would have required eight klystrons). The basic SLED II requirements are to produce 475 MW, 400 ns pulses with 81% efficiency. The version proposed for the linear collider differs from the early NLCTA design in several ways, including larger diameter (17.1 cm) delay lines to lower power loss, dual-moded (TE01 and TE02) delay line transport to halve the delay line length, a lower compression ratio (4 instead of 6) to improve efficiency, and the use of over-height planar hybrid and waveguide components to lower surface fields. A design surface field limit of 50 MV/m was chosen based on tests of waveguide and a high-power SLED II prototype [5,6].

A prototype dual-moded SLED II system for the linear collider was built at NLCTA in 2003 as part of the Eight Pack Project [7]. After high-power processing this system for three weeks, 580 MW, 400 ns pulses were achieved, limited only by the available klystron power. The system was then run at 500 MW for 200 hours at 30 Hz and 100 hours at 60 Hz to measure its performance. During the 30 Hz operation, 11 rf breakdowns occurred in the SLED II system and associated over-height waveguide, while none occurred during the 60 Hz operation. The average rate of 0.06 breakdowns per hour (60 Hz equivalent) is better than the 0.16 per hour limit for the linear collider, and the improvement during the last 100 hours suggests that the breakdown rate was decreasing. The efficiency achieved after optimal tuning was 77%. This system was subsequently run at 300 MW for over 1500 hours for structure testing. It has yet to be fully characterized as to its power handling capability.

ACCELERATOR STRUCTURES

The basic structure design chosen for the linear collider is traveling wave with a disk-loaded waveguide geometry. As is typical in such designs, the rf group velocity is varied along the structure to produce a fairly constant surface gradient profile, with or without including the effect of beam loading. The choice of operating gradient was based in part on cost optimization studies that considered the relative cost of the power sources (which increase with gradient) and the beam line (which decreases with gradient). The design unloaded gradient of 65 MV/m is less than the cost optimum (~ 80 MV/m), but the cost difference is only a few percent of the collider cost. Another important consideration was the practical gradient that could be achieved, which required a major R&D effort to quantify.

The structure gradients are limited by rf breakdown and the surface damage it causes. While the breakdown rate strongly depends on the surface field strength, the damage that results (i.e., the pitting on the iris surfaces) appears to depend on the incident power level. At high power, this damage leads to breakdown on subsequent pulses, which ultimately limits the achievable gradient. With this realization, the design efforts focused on reducing the input power, which is complicated by efficiency and wakefield constraints.

The strength of both the short-range (inter-bunch) and long-range (bunch-to-bunch) structure wakefield must be limited to preserve the small beam emittances in the linear collider. For long-range wakefield suppression, a combination of dipole mode damping and detuning was implemented, which required innovations in wakefield modeling and in quality control during cell fabrication. The detuning was achieved by tailoring the cell dipole frequencies to produce a Gaussian-like distribution, and damping is provided by four manifolds that run parallel to the structure and couple to each cell. These methods were shown to produce a hundred-fold reduction in the long-range wakefield by the arrival time of the next bunch (1.4 ns), as is required for the linear collider [8].

The short-range wakefield limit tightly constrains the allowed minimum average iris radius, which would otherwise be reduced to improve the rf-to-beam efficiency. The average iris radius finally adopted is 17% of the rf wavelength. This decision was also tied to the choice of bunch charge, and a less than optimal rf-to-beam efficiency (48% in steady state compared to ~ 70% maximum with the final structure design) was accepted as a tradeoff for smaller wakefields. With these choices, the linac beams must be centered within the structures to about 10 µm rms, which has been demonstrated using the dipole signals from the damping manifolds as an alignment guide [9].

An aggressive program to establish stable high gradient operation of the structures began in 1999 when it was realized that the long (1.8 m), high-power designs that had been developed to optimize the cost of the collider performed much worse than the short, low-power, high-gradient designs that had been developed initially for expediency. Since then, 40 structures have been tested at NLCTA with over 25,000 hours of high power operation logged [10,11]. The viable structure designs that have resulted from this program are 60 cm long and have low group velocity (3-4% c initially) and high phase advance per cell (150°). Relative to the 1.8-m structures, they are 16% less efficient, but have vastly improved performance at the gradients of interest with no indication of significant damage after initial processing to about 75 MV/m (typically < 2° change in net phase advance).

During the past year and half, this program has focused on quantifying the structure performance. With the large number of structures required (~18,000) for the linear collider, the structure trip rate from rf breakdown must be very small (< 0.1 per hour at the 60 Hz rate used when testing) to ensure essentially 100% full linac energy availability with the planned 2% overhead of spares and a 10 second trip recovery time. Also, the transverse fields generated during breakdown would deflect the beam so
the magnitude of these kicks must be small. Finally, the structures need to be robust in that they do not degrade in performance over time, and recover quickly if accidentally vented to air.

To measure the long-term performance, eight structures were operated at 65 MV/m (unloaded) with the design pulse shape (100 ns amplitude ramp plus a 300 ns flattop). Of these structures, five have all the wakefield suppression features required for the linear collider. After more than 1500 hours of 60 Hz operation, the structure breakdown rates ranged from 0.001 to 0.07 per hour, with an average of 0.04 per hour, which is well within the 0.1 per hour goal. During this period, the rates generally decreased exponentially with time constants of 400-700 hours. In the linear collider, the rates would likely be even lower due to the field reduction from beam loading.

In a dedicated test, the beam kicks caused by the fields generated during breakdown were measured and found to be large enough (~ 10 keV/c rms) to cause some luminosity loss from off-center beam collisions in the linear collider (< 1% loss at the 0.1 per hour breakdown rate limit), but not large enough to cause the beams to hit the collimators that would be used. Another test showed that the structures could run without a recovery period after breakdown when the rf is normally shut off and then ramped back up. Finally, a series of venting experiments were conducted on a pair of previously processed structures to determine the influence of nitrogen and air exposure on performance. Even with exposure to unfiltered air, the low breakdown rates were recovered after a few days of operation during which a few dozen breakdowns occurred. Thus, the structures meet the linear collider requirements and appear to be robust.

Currently, measurements of wakefield suppression and beam-based structure alignment are underway to verify that the 60-cm structures perform as well as the 1.8-m structures in these regards [12]. Also, some high gradient testing continues at SLAC and KEK [13].

SUMMARY

The X-band linear collider program made significant advances in pulsed HV and rf power generation, high gradient acceleration and wakefield suppression. For the rf systems, a major constraint was surface field limits, which appear to depend on the energy flow through the devices. From tests of X-band traveling-wave structures, waveguides and single cell cavities, the general trend observed in sustainable surface fields is plotted in Figure 2 versus group velocity (or equivalent), which characterizes the energy flow in these systems (the field limits are shown for 250 ns pulses: they scale roughly as the -1/6 to -1/3 power of pulse length). For the linear collider, the structure design evolved to be within these limits, while the waveguide systems were initially. If rf breakdown proves to be a fundamental limit to stable operation of the 75 MW klystrons, it would be consistent with these data. Thus, future accelerator designers should be wary of this trend with group velocity.

![Figure 2: Trend of sustainable surface fields versus group velocity in various structures, waveguides and cells for pulse lengths of 250 ns. The range of group velocities for the linear collider components are indicated by ‘Structures,’ ‘Klystrons’ and ‘WG.’](image)

REFERENCES